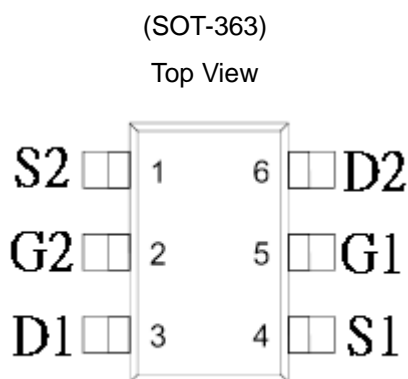


Dual N - Channel 60V (D-S) MOSFET, ESD Protection

GENERAL DESCRIPTION

The ME2N7002DKW-G is the Dual N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits , and low in-line power loss are needed in a very small outline surface mount package.

PIN CONFIGURATION

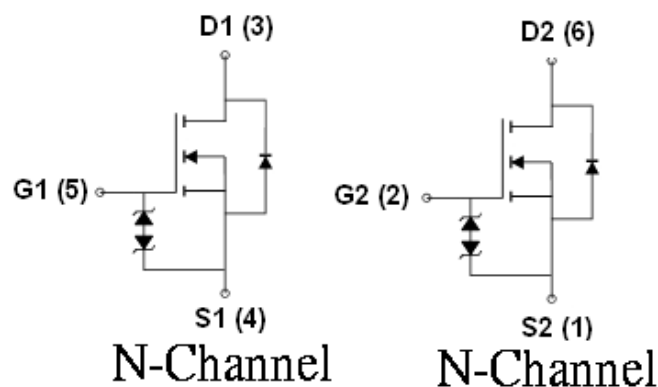


FEATURES

- $R_{DS(ON)} \leq 3\Omega @ V_{GS}=10V$
- $R_{DS(ON)} \leq 4\Omega @ V_{GS}=4.5V$
- $R_{DS(ON)} \leq 4.5\Omega @ V_{GS}=3V$
- ESD Protection HBM >2KV
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

APPLICATIONS

- Power Management in Note book
- DC/DC Converter
- Load Switch
- LCD Display inverter



Ordering Information: ME2N7002DKW-G (Green product-Halogen free)

Absolute Maximum Ratings (TA=25°C Unless Otherwise Noted)

| Parameter | Symbol | Maximum Ratings | Unit |
|---|------------------|-----------------|------|
| Drain-Source Voltage | V _{DS} | 60 | V |
| Gate-Source Voltage | V _{GS} | ±20 | V |
| Continuous Drain | TA=25°C | I _D | A |
| | TA=70°C | I _D | |
| Pulsed Drain Current | I _{DM} | 1.1 | A |
| Maximum Power Dissipation | TA=25°C | P _D | W |
| | TA=70°C | P _D | |
| Operating Junction Temperature | T _J | -55 to 150 | °C |
| Thermal Resistance-Junction to Ambient* | R _{θJA} | 375 | °C/W |

* The device mounted on 1in² FR4 board with 2 oz copper

Dual N - Channel 60V (D-S) MOSFET, ESD Protection
Electrical Characteristics (TA=25°C Unless Otherwise Specified)

| Symbol | Parameter | Limit | Min | Typ | Max | Unit |
|---------------------|---------------------------------|---|-----|------|-----|------|
| STATIC | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250 μA | 60 | | | V |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250 μA | 1 | | 2.5 | V |
| I _{GSS} | Gate-Body Leakage | V _{DS} =0V, V _{GS} =±20V | | | ±10 | μA |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =60V, V _{GS} =0V | | | 1 | μA |
| R _{DS(ON)} | Drain-Source On-Resistance* | V _{GS} =10V, I _D =500mA | | 2 | 3 | Ω |
| | | V _{GS} =4.5V, I _D =200mA | | 2.4 | 4 | |
| | | V _{GS} =3V, I _D =10mA | | 3.9 | 4.5 | |
| V _{SD} | Diode Forward Voltage * | I _S =200mA, V _{GS} =0V | | 0.82 | 1.2 | V |
| DYNAMIC | | | | | | |
| Q _g | Total Gate Charge | V _{DS} =30V, V _{GS} =10V, I _D =200mA | | 4.5 | | nC |
| Q _g | Total Gate Charge | V _{DS} =30V, V _{GS} =4.5V, I _D =200mA | | 1.4 | | |
| Q _{gs} | Gate-Source Charge | | | 2.6 | | |
| Q _{gd} | Gate-Drain Charge | | | 0.4 | | |
| C _{iss} | Input Capacitance | V _{DS} =30V, V _{GS} =0V, f=1MHz | | 21 | | pF |
| C _{oss} | Output Capacitance | | | 3 | | |
| C _{rss} | Reverse Transfer Capacitance | | | 1 | | |
| t _{d(on)} | Turn-On Delay Time | V _{DS} =30V, R _L =150Ω V _{GS} =10V, R _{GS} =10Ω I _D =200mA | | 6.8 | | ns |
| t _r | Turn-On Rise Time | | | 2.4 | | |
| t _{d(off)} | Turn-Off Delay Time | | | 15 | | |
| t _f | Turn-Off Fall Time | | | 27.3 | | |

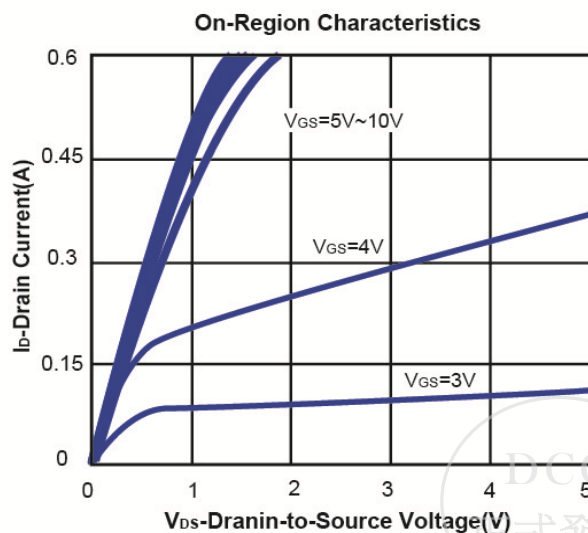
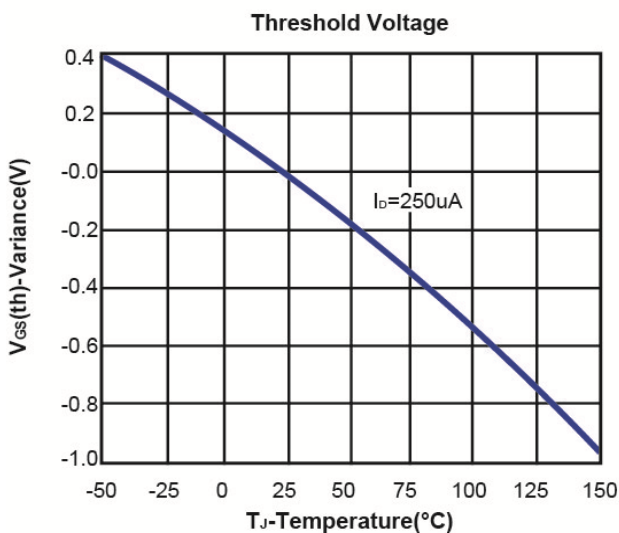
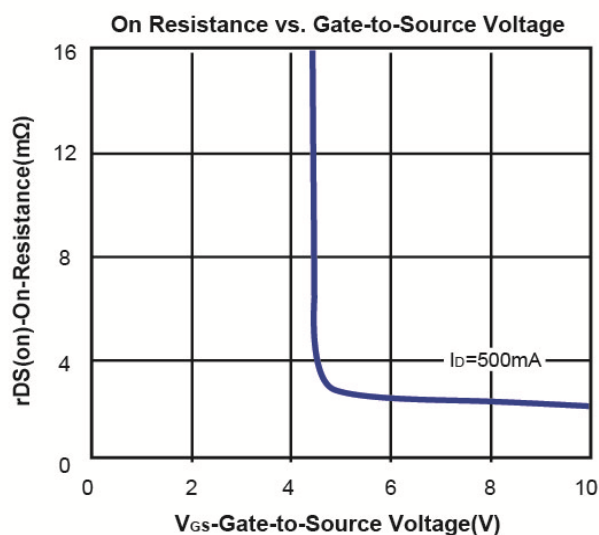
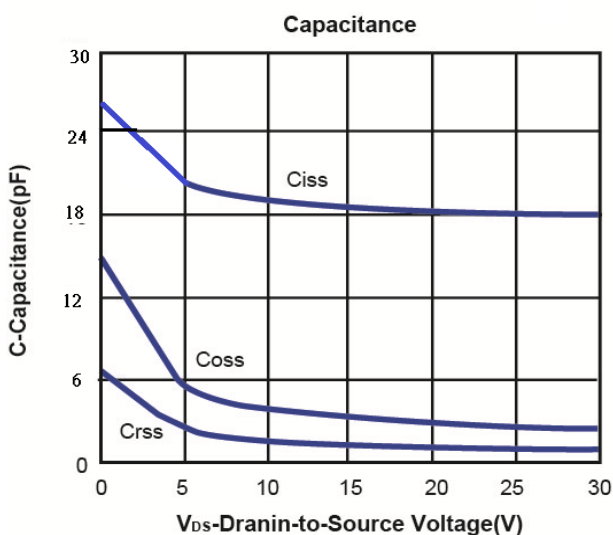
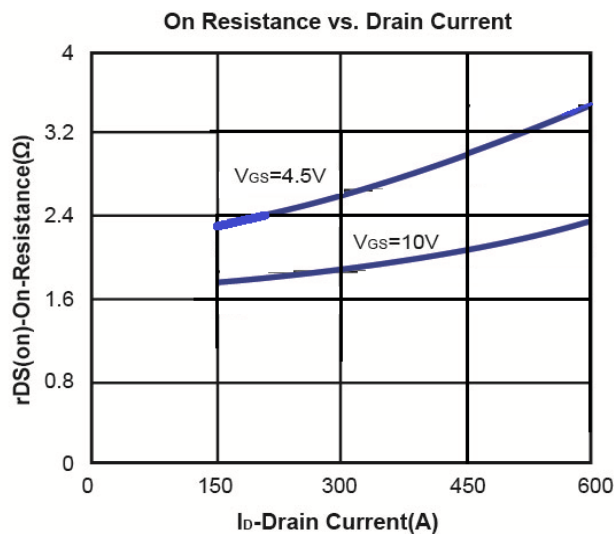
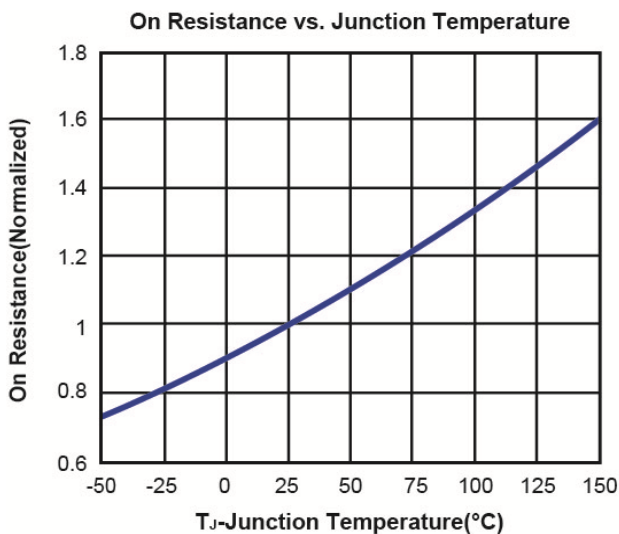
Notes: a, pulse test: pulse width ≤ 300us, duty cycle ≤ 2%, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.



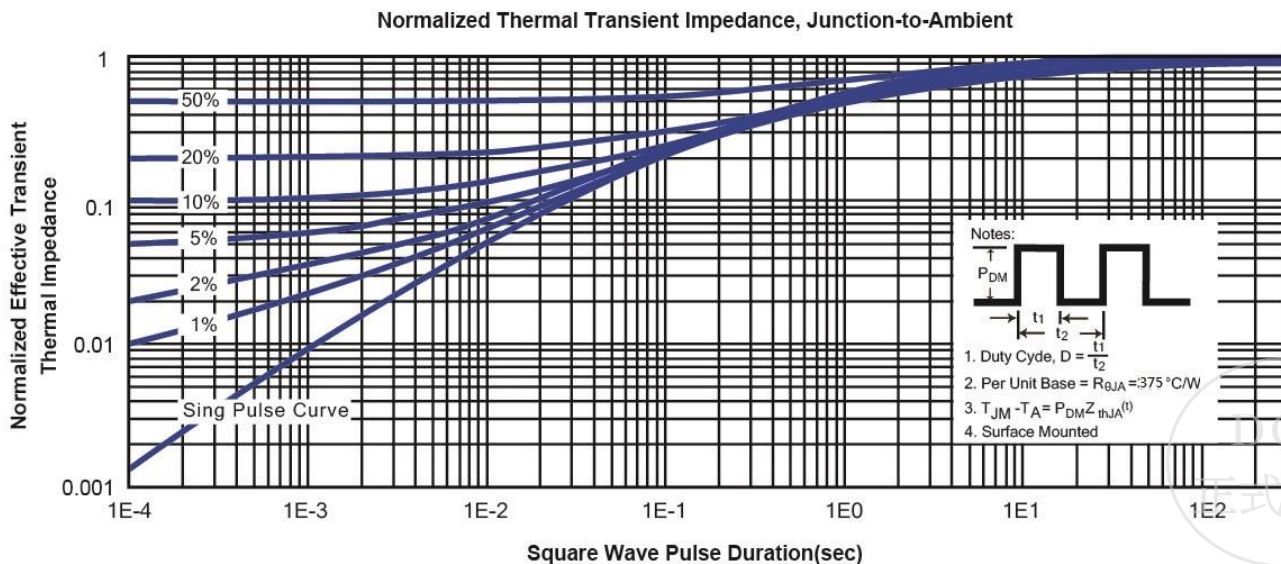
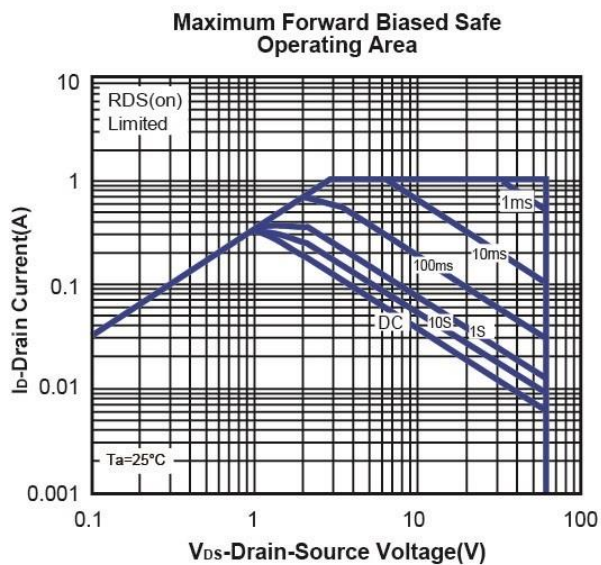
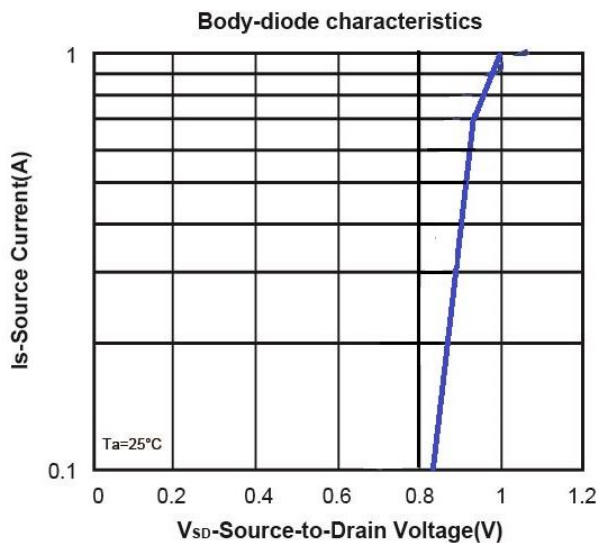
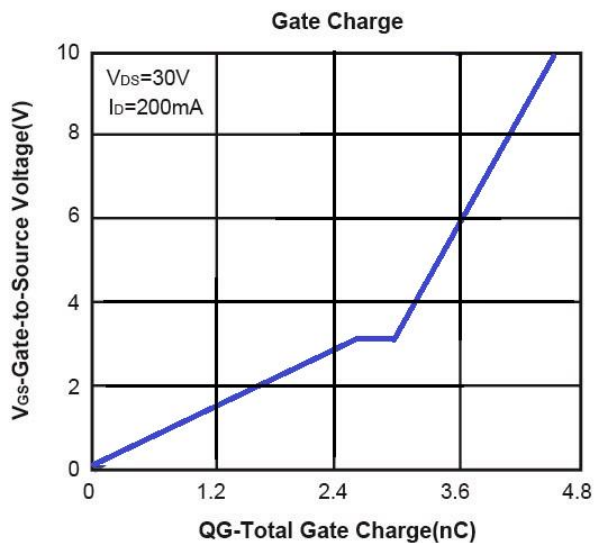
Dual N - Channel 60V (D-S) MOSFET, ESD Protection

Typical Characteristics (T_J =25°C Noted)

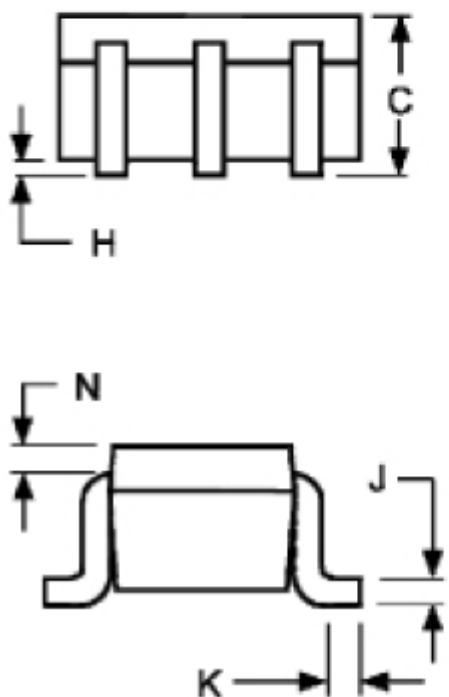
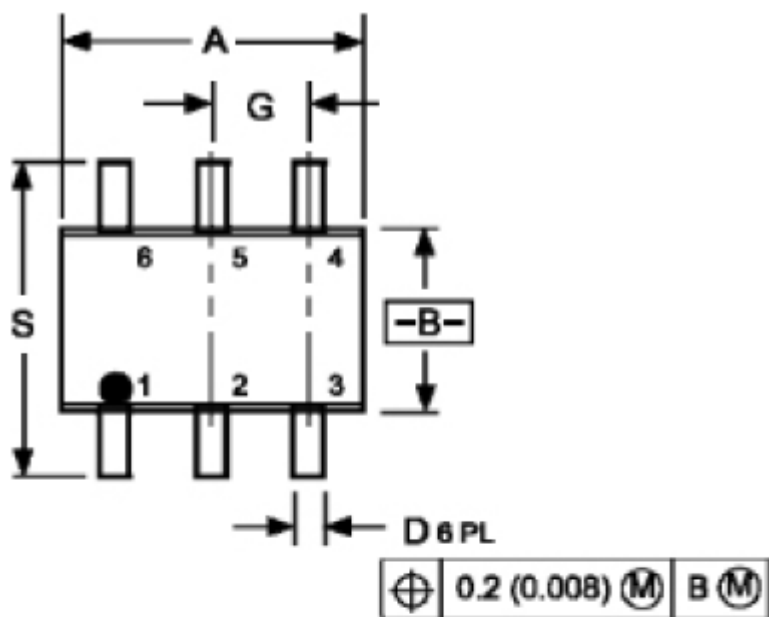


Dual N - Channel 60V (D-S) MOSFET, ESD Protection

Typical Characteristics (T_J =25°C Noted)



SOT-363 Package Outline



| DIM | INCHES | | MILLMETERS | |
|-----|-----------|-------|------------|------|
| | MIN | MAX | MIN | MAX |
| A | 0.071 | 0.087 | 1.80 | 2.20 |
| B | 0.045 | 0.053 | 1.15 | 1.35 |
| C | 0.031 | 0.043 | 0.80 | 1.10 |
| D | 0.004 | 0.012 | 0.10 | 0.30 |
| G | 0.026 BSC | | 0.65 BSC | |
| H | --- | 0.004 | --- | 0.10 |
| J | 0.004 | 0.010 | 0.10 | 0.25 |
| K | 0.012 | 0.018 | 0.30 | 0.45 |
| N | 0.008 REF | | 0.20 REF | |
| S | 0.079 | 0.087 | 2.00 | 2.20 |

正式發行

Dual N - Channel 60V (D-S) MOSFET, ESD Protection

Device name: **ME2N7002DKW-G**

Package: **SOT-363**

Marking Code:



D76:DeviceMarkingCode

M:Date code

MONTH CODE

ODD YEARS(2007,2009)

| | |
|-----|---|
| Jan | 1 |
| Feb | 2 |
| Mar | 3 |
| Apr | 4 |
| May | 5 |
| Jun | 6 |
| Jul | 7 |
| Aug | 8 |
| Sep | 9 |
| Oct | T |
| Nov | V |
| Dec | C |

EVEN YEARS(2006,2008)

| | |
|-----|---|
| Jan | E |
| Feb | F |
| Mar | H |
| Apr | J |
| May | K |
| Jun | L |
| Jul | N |
| Aug | P |
| Sep | U |
| Oct | X |
| Nov | Y |
| Dec | Z |

