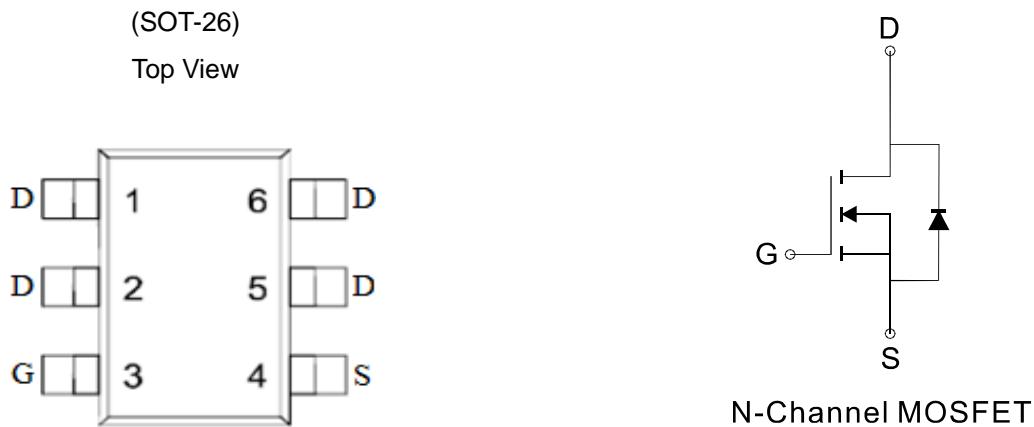


N-Channel 150V(D-S) MOSFET
GENERAL DESCRIPTION

The ME3466-G is the N-Channel logic enhancement mode power field effect transistors, using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone, notebook computer power management and other battery powered circuits, and low in-line power loss that are needed in a very small outline surface mount package.

PIN CONFIGURATION

FEATURES FEATURES

- $R_{DS(ON)} \leq 375\text{m}\Omega @ V_{GS} = 10\text{V}$
- $R_{DS(ON)} \leq 400\text{m}\Omega @ V_{GS} = 6\text{V}$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

APPLICATIONS

- Power Management in Note book
- DC/DC Converter
- Load Switch
- LCD Display inverter

Ordering Information: ME3466-G (Green product-Halogen free)

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Maximum Ratings	Unit
Drain-Source Voltage	V_{DSS}	150	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ($T_J=150^\circ\text{C}$)	I_D	1.7	A
$T_A=70^\circ\text{C}$		1.3	
Pulsed Drain Current	I_{DM}	7	
Maximum Power Dissipation ($T_A=25^\circ\text{C}$)	P_D	1.7	W
$T_A=70^\circ\text{C}$		1.1	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Thermal Resistance-Junction to Ambient*	$R_{\theta JA}$	74	°C/W

*The device mounted on 1in2 FR4 board with 2 oz copper



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Electrical Characteristics (T_J = 25°C Unless Otherwise Specified)

Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC PARAMETERS						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	150			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250 μA	2		4	V
I _{GSS}	Gate-Body Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =150V, V _{GS} =0V			1	μA
R _{D(S(ON))}	Drain-Source On-Resistance ^a	V _{GS} =10V, I _D =1.5A		310	375	mΩ
		V _{GS} =6V, I _D =1.4A		330	400	
V _{SD}	Diode Forward Voltage	I _S =1.7A, V _{GS} =0V		0.8	1.2	V
DYNAMIC PARAMETERS						
Q _G	Total Gate Charge	V _{DS} =75V, V _{GS} =10V, I _D =1.5A		5.1		nC
Q _{GS}	Gate-Source Charge			0.6		
Q _{GD}	Gate-Drain Charge			1.7		
C _{ISS}	Input Capacitance	V _{DS} =30V, V _{GS} =0V, f=1MHz		290		pF
C _{OSS}	Output Capacitance			40		
C _{RSS}	Reverse Transfer Capacitance			23		
t _{d(on)}	Turn-On Delay Time	V _{DS} =75V, R _L =75Ω V _{GS} =10V, R _G =6Ω I _D =1A		7.6		ns
t _r	Rise Time			5.2		
t _{d(off)}	Turn-Off Delay Time			19.5		
t _f	Fall Time			13.8		

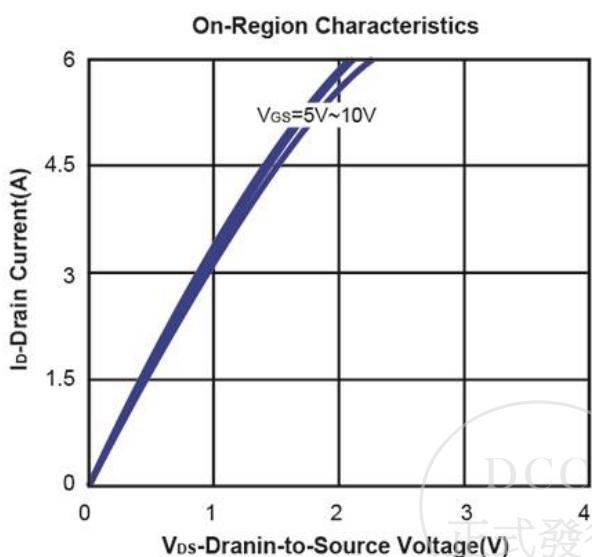
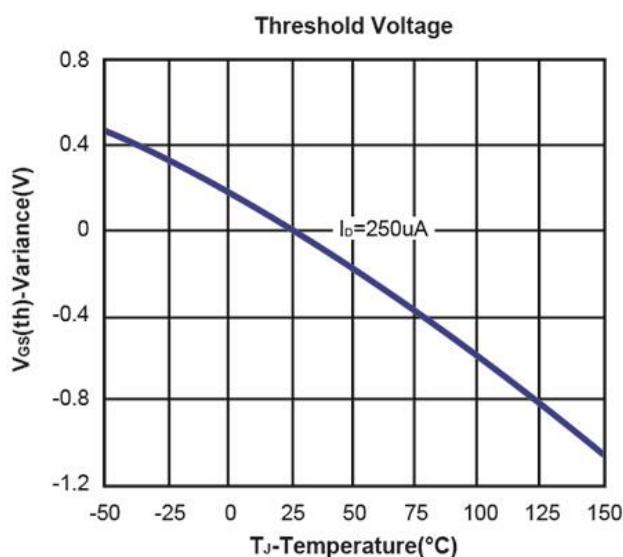
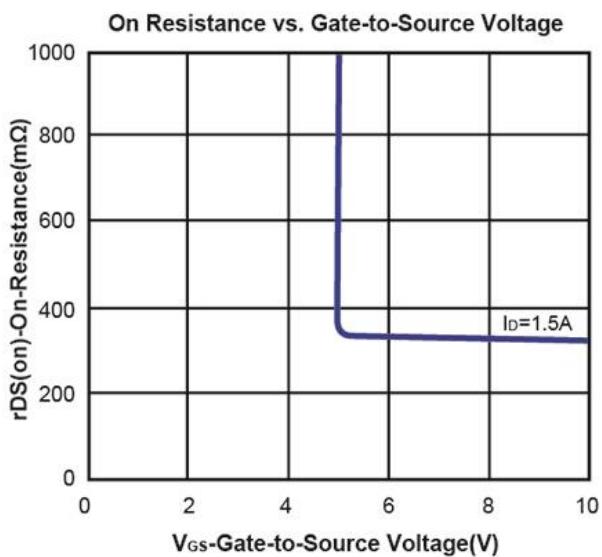
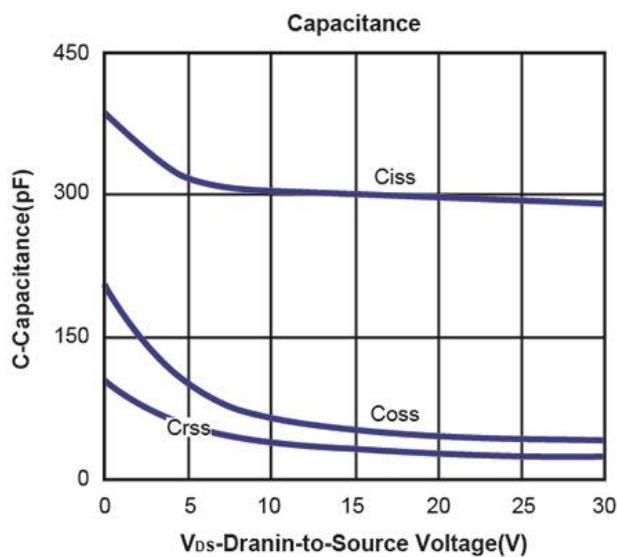
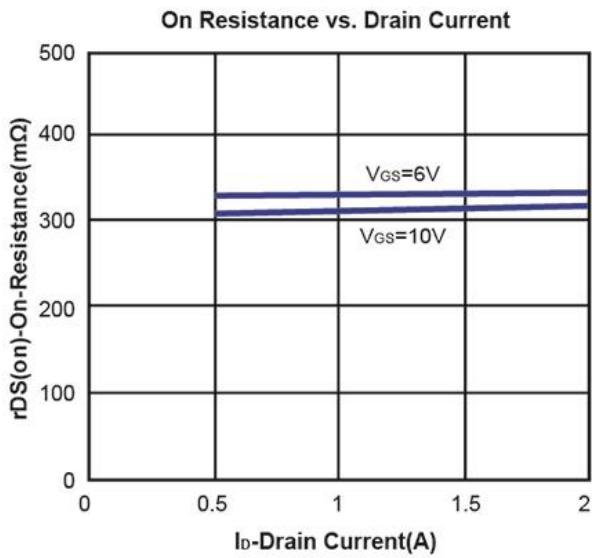
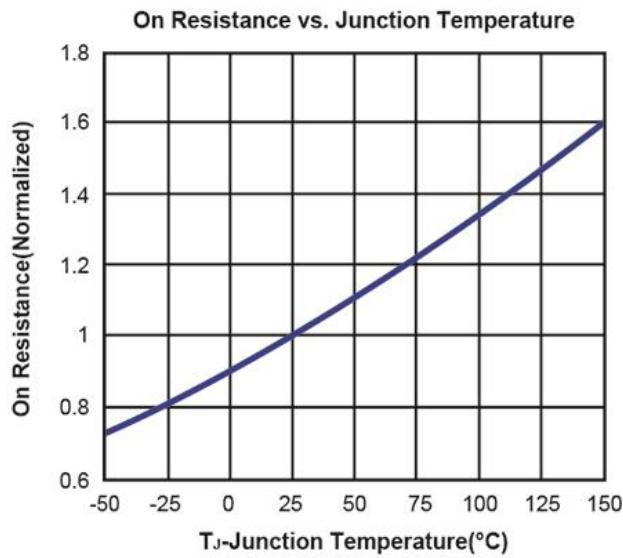
Notes: a. Pulse test: pulse width ≤ 300us, duty cycle ≤ 2%, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.



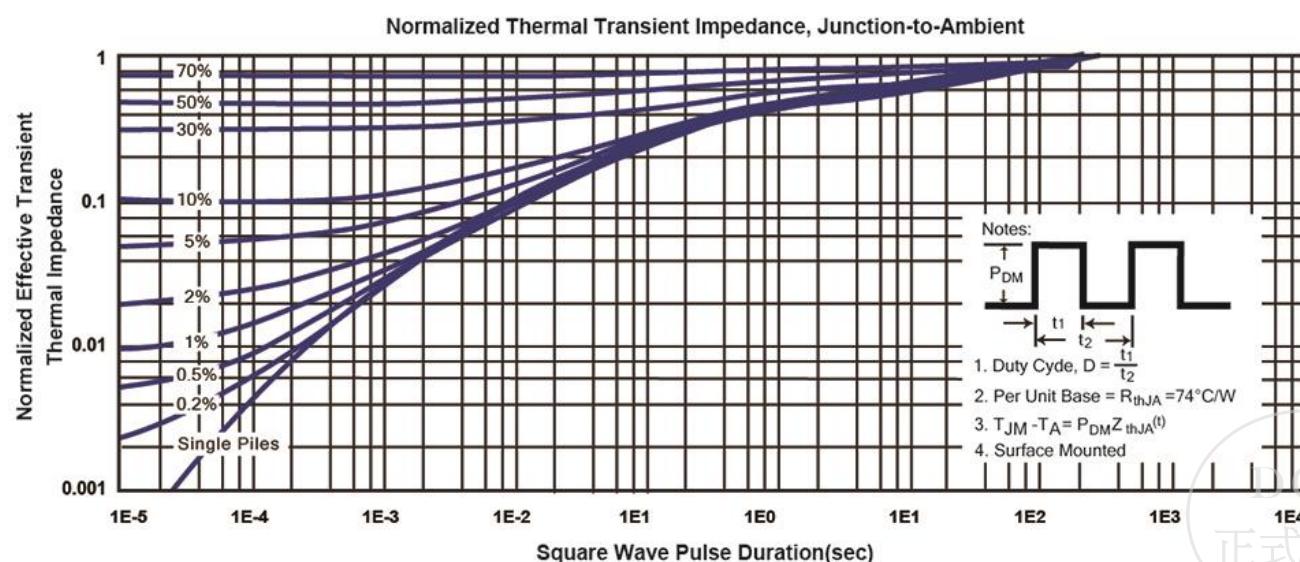
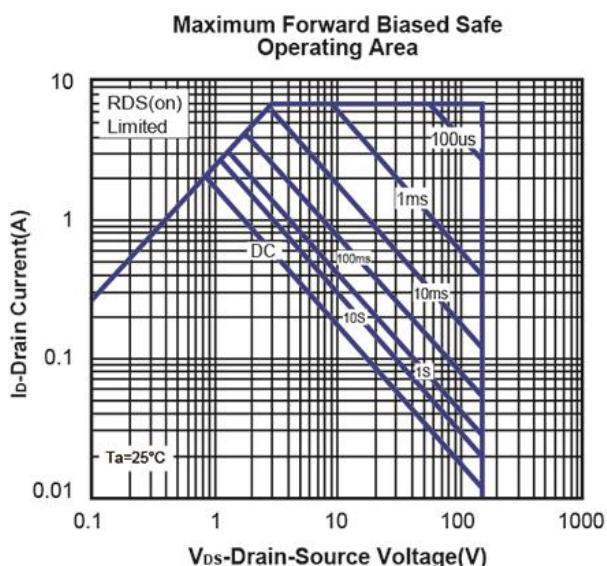
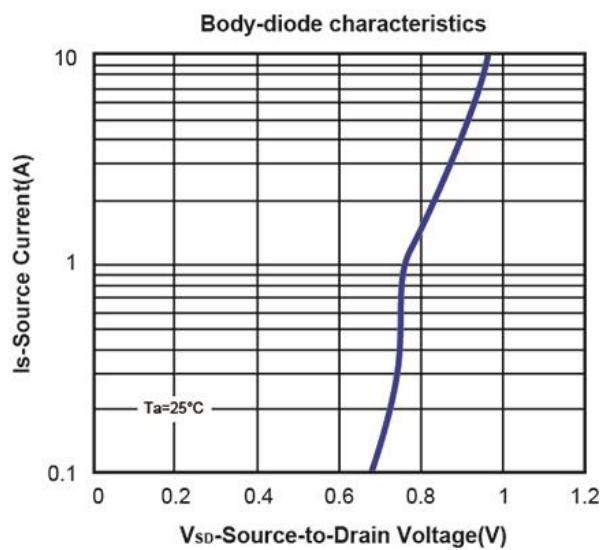
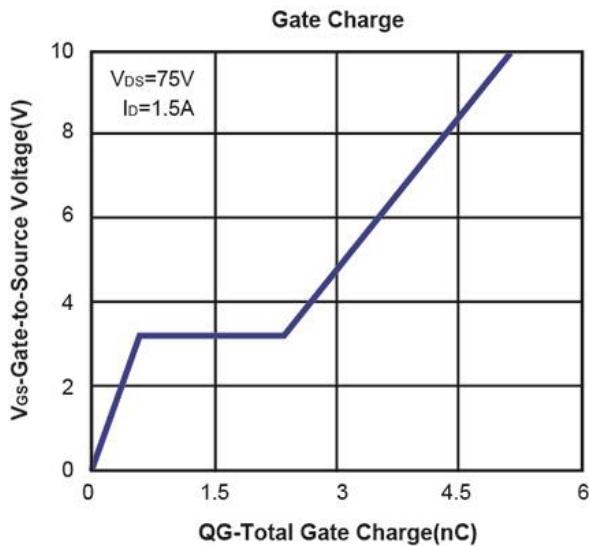
N-Channel 150V(D-S) MOSFET

Typical Characteristics (T_J =25°C Noted)

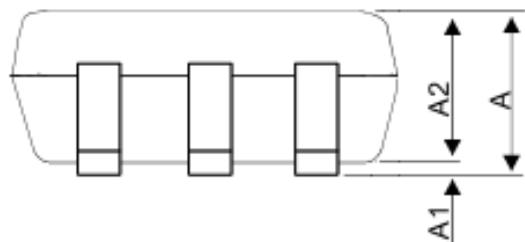
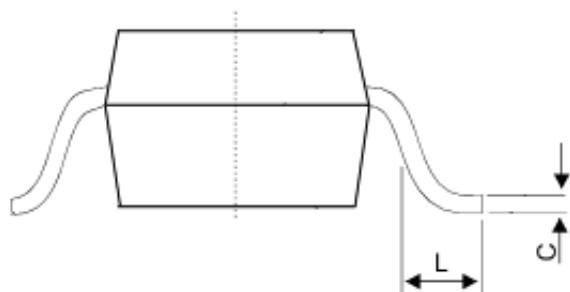
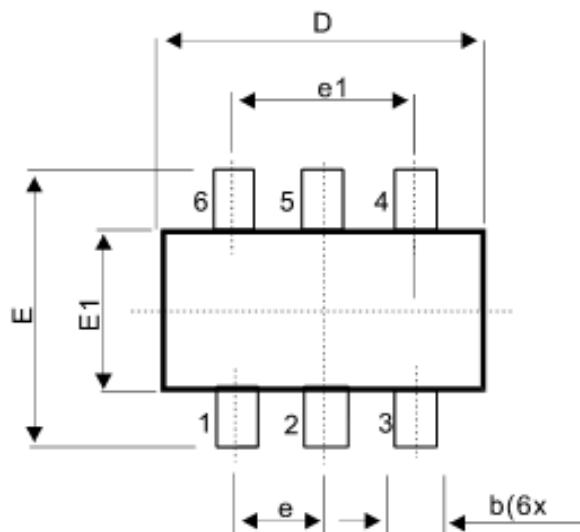


N-Channel 150V(D-S) MOSFET

Typical Characteristics (T_J = 25°C Noted)



TSOP-6 Package Outline



SYMBOL	MILLIMETERS (mm)	
	MIN	MAX
A	0.90	1.20
A1	0.01	0.10
A2	0.90	1.15
b	0.25	0.50
C	0.10	0.20
D	2.80	3.10
E	2.60	3.00
E1	1.50	1.70
e	0.95 BSC	
e1	1.90 BSC	
L	0.30	0.60

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