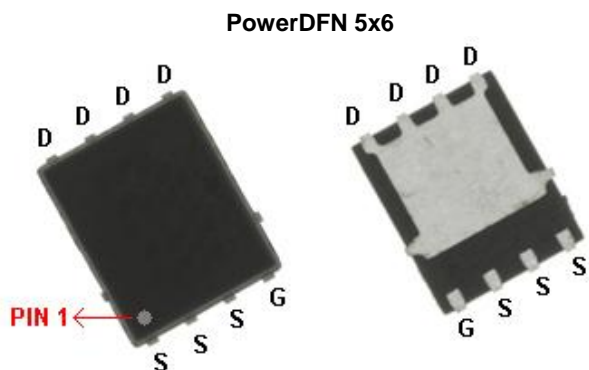


P-Channel 30V(D-S) Enhancement MOSFET

GENERAL DESCRIPTION

The ME7639 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as notebook computer power management and other battery powered circuits where Low-side switching , and low in-line power loss are needed in a very small outline surface mount package.

PIN CONFIGURATION

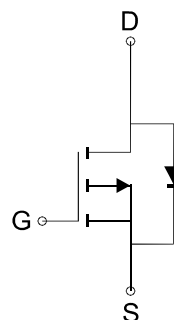


FEATURES

- $R_{DS(ON)} \leq 11m\Omega @ V_{GS} = -10V$
- $R_{DS(ON)} \leq 14m\Omega @ V_{GS} = -6V$
- $R_{DS(ON)} \leq 18m\Omega @ V_{GS} = -4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter



P-Channel MOSFET

Ordering Information: ME7639 (Pb-free)

ME7639-G (Green product-Halogen free)

Absolute Maximum Ratings (TA=25°C Unless Otherwise Noted)

Parameter	Symbol	Maximum Ratings	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current	I_D	$T_A = 25^\circ C$	-12.5
		$T_A = 70^\circ C$	-10
Pulsed Drain Current	I_{DM}	-50	A
Maximum Power Dissipation	P_D	$T_A = 25^\circ C$	2.7
		$T_A = 70^\circ C$	1.7
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ C$
Thermal Resistance-Junction to Ambient*	$R_{\theta JA}$	45	$^\circ C/W$

*The device mounted on 1in² FR4 board with 2 oz copper



P-Channel 30V(D-S) Enhancement MOSFET
Electrical Characteristics ($T_J = 25^\circ\text{C}$ Unless Otherwise Specified)

Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.5		-2.5	V
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 25V$			± 100	nA
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-24V, V_{GS}=0V$			-1	μA
$R_{DS(ON)}$	Drain-Source On-State Resistance ^a	$V_{GS}=-10V, I_D=-15A$		7	11	m Ω
		$V_{GS}=-6V, I_D=-10A$		9	14	
		$V_{GS}=-4.5V, I_D=-5A$		11	18	
V_{SD}	Diode Forward Voltage	$I_S=-1A, V_{GS}=0V$		-0.7	-1	V
DYNAMIC						
Q_g	Total Gate Charge	$V_{DS}=-15V, V_{GS}=-10V, I_D=-15A$		53.8		nC
Q_{gs}	Gate-Source Charge			11		
Q_{gd}	Gate-Drain Charge			12.6		
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, F=1MHz$		1224		pF
C_{oss}	Output Capacitance			314		
C_{rss}	Reverse Transfer Capacitance			242		
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V, R_L=15\Omega$ $I_D=-1A, V_{GEN}=-10V$ $R_G=6\Omega$		44		ns
t_r	Turn-On Rise Time			19.3		
$t_{d(off)}$	Turn-Off Delay Time			158		
t_f	Turn-Off Fall Time			43.7		

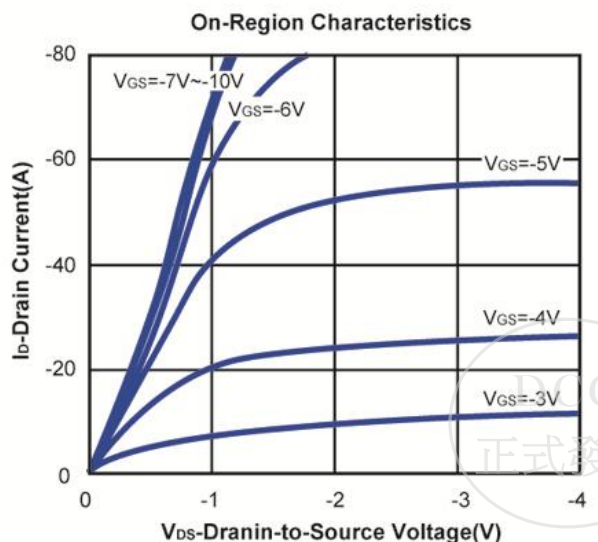
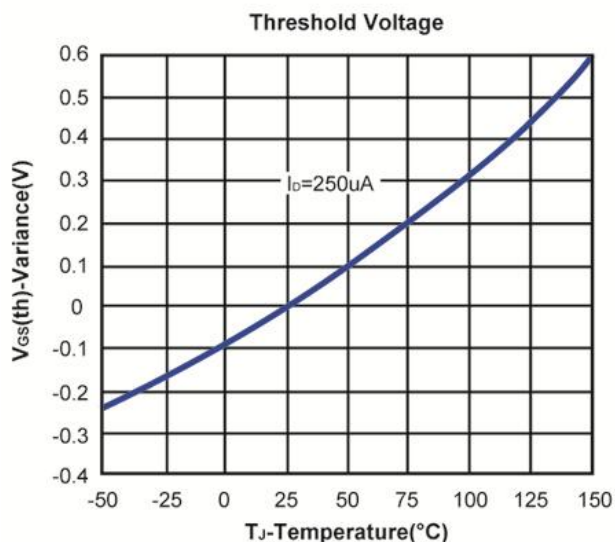
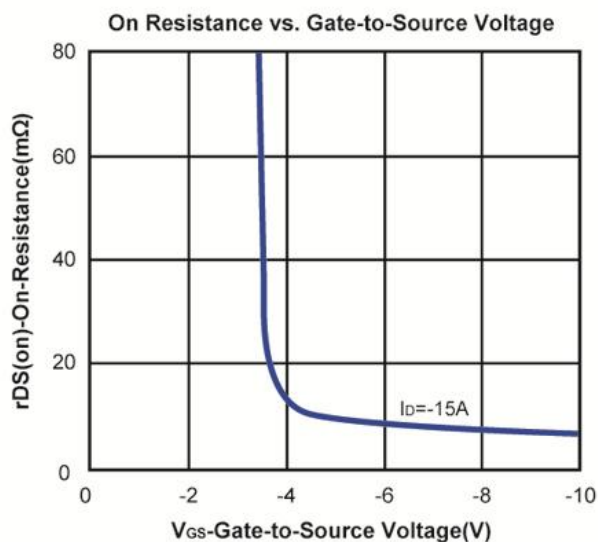
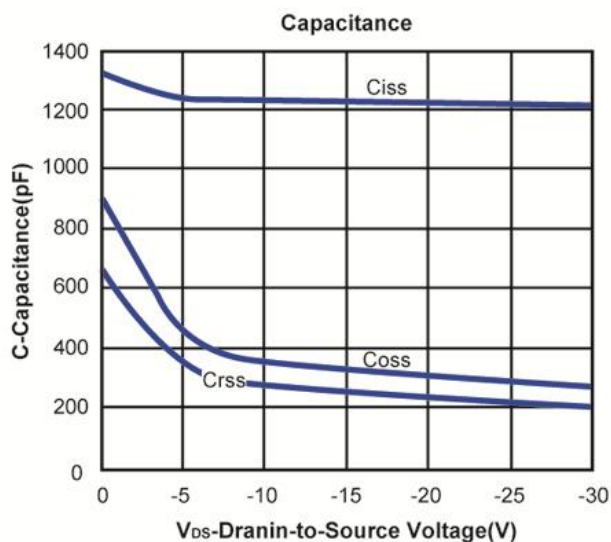
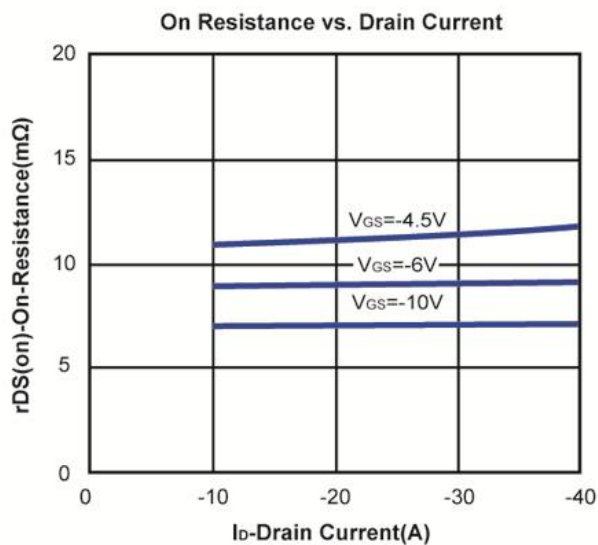
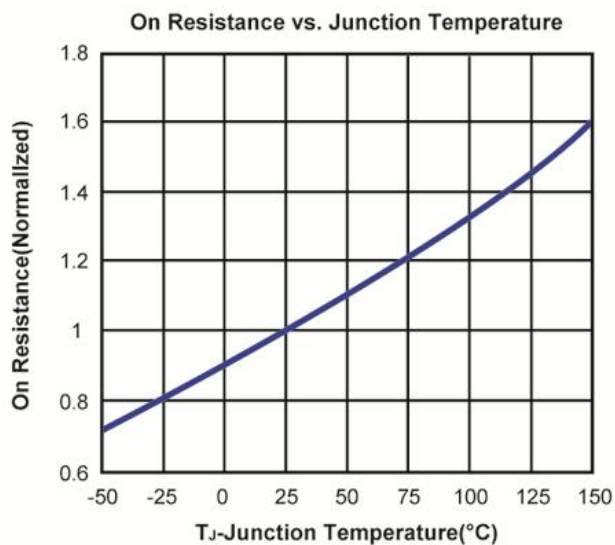
 Notes: a. Pulse test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.

 DCC
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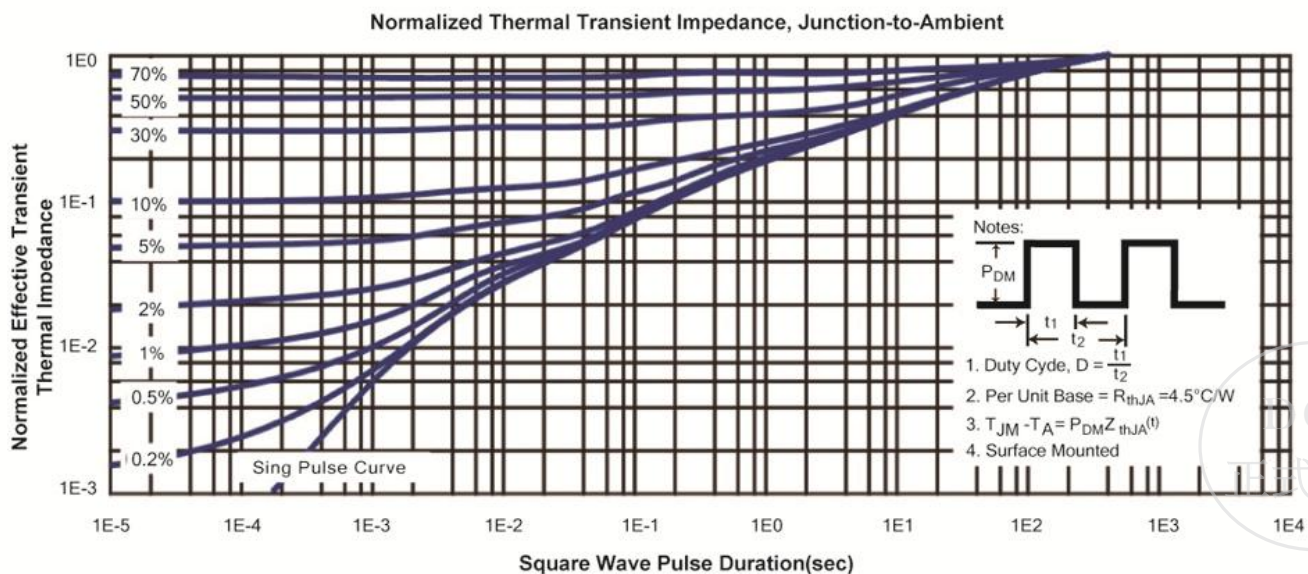
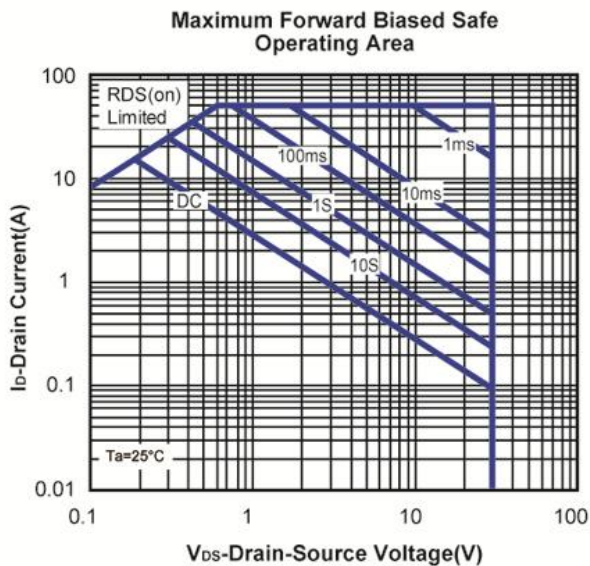
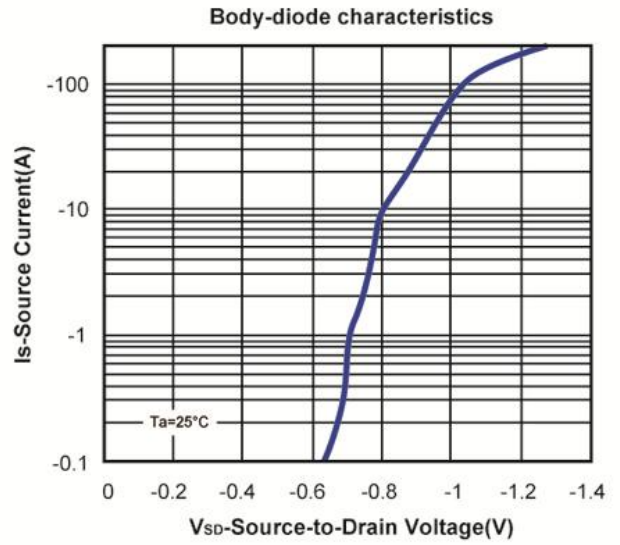
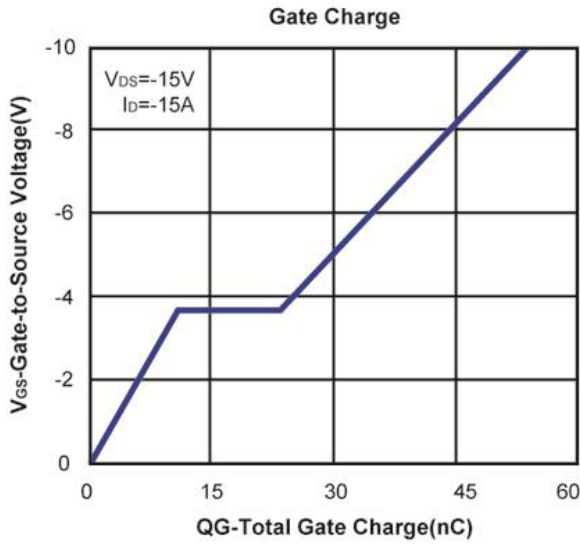
P-Channel 30V(D-S) Enhancement MOSFET

Typical Characteristics (T_J =25°C Noted)

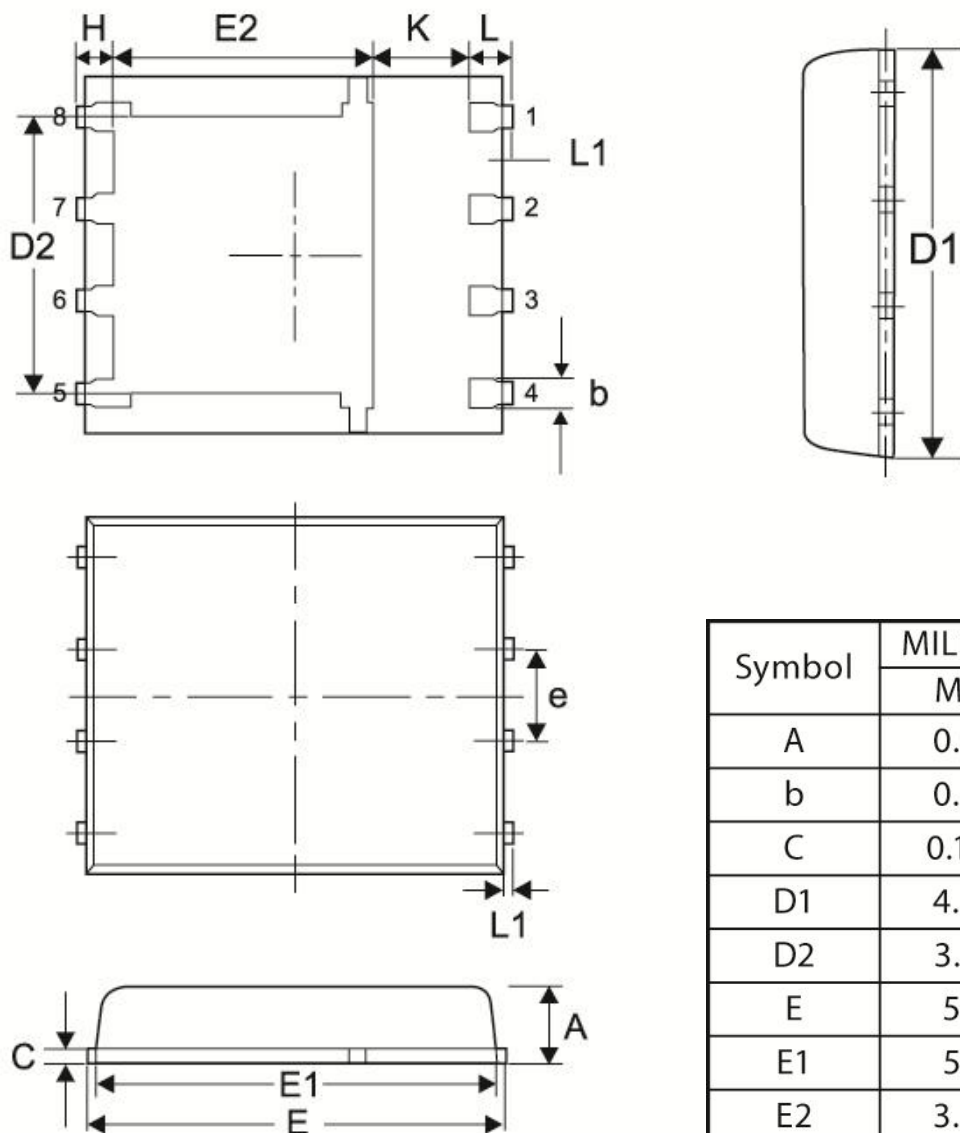


P-Channel 30V(D-S) Enhancement MOSFET

Typical Characteristics (T_J =25°C Noted)



PowerDFN 5x6 Package Outline



Symbol	MILLIMETERS (mm)	
	MIN	MAX
A	0.90	1.25
b	0.33	0.51
C	0.155	0.30
D1	4.80	5.00
D2	3.61	3.96
E	5.8	6.20
E1	5.6	5.90
E2	3.35	4.31
e	1.27 BSC	
H	0.35	0.61
K	1.60	-
L	0.35	0.71
L1	0.05	0.20

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