

N-Channel 30V (D-S) MOSFET

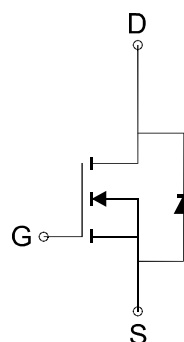
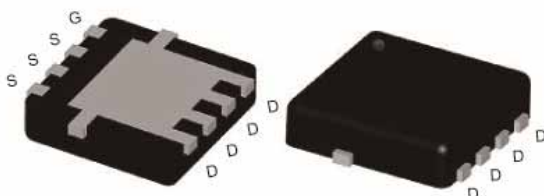
GENERAL DESCRIPTION

The ME7802S-G is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits where Low-side switching , and low in-line power loss are needed in a very small outline surface mount package.

PIN CONFIGURATION

(DFN 3.3x3.3)

Top View



N-Channel MOSFET

Ordering Information: ME7802S-G (Green product-Halogen free)

Absolute Maximum Ratings (TA=25 Unless Otherwise Noted)

Parameter		Symbol	Maximum Ratings			Unit
Drain-Source Voltage		V _{DS}	30			V
Gate-Source Voltage		V _{GS}	±20			V
Continuous Drain Current*	T _C =25	I _D	84			A
	T _C =70		67			
	T _A =25		22.7			
	T _A =70		18.2			
Pulsed Drain Current		I _{DM}	91			A
Maximum Power Dissipation*	T _C =25	P _D	52			W
	T _C =70		33			
	T _A =25		3.8			
	T _A =70		2.4			
Operating Junction Temperature		T _J	150			
Storage Temperature Range		T _{stg}	-55 to 150			
Thermal Resistance-Junction to Ambient*		R _{θJA}	Typ	26	Max	33
Thermal Resistance-Junction to Case*		R _{θJC}	Typ	1.9	Max	2.4

*The device mounted on 1in² FR4 board with 2 oz copper

FEATURES

R_{DS(ON)} 4.6mΩ@V_{GS}=10V

R_{DS(ON)} 6.8mΩ@V_{GS}=4.5V

Super high density cell design for extremely low R_{DS(ON)}

Exceptional on-resistance and maximum DC current capability

APPLICATIONS

- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch

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Electrical Characteristics (TA=25 Unless Otherwise Specified)

Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	30			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250 μA	1.0		3.0	V
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V			1	μA
R _{DS(ON)}	Drain-Source On-State Resistance ^a	V _{GS} =10V, I _D =20A		3.7	4.6	m
		V _{GS} =4.5V, I _D =16A		5.4	6.8	
V _{SD}	Diode Forward Voltage	I _S =1.0A, V _{GS} =0V		0.7	1.2	V
DYNAMIC						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _D =20A		54		nC
Q _g	Total Gate Charge			27		
Q _{gs}	Gate-Source Charge	V _{DS} =15V, V _{GS} =4.5V, I _D =20A		9.5		
Q _{gd}	Gate-Drain Charge			11		
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, F=1MHz		2450		pF
C _{oss}	Output Capacitance			393		
C _{rss}	Reverse Transfer Capacitance			129		
R _g	Gate-Resistance	V _{DS} =0V, V _{GS} =0V, F=1MHz		1.8		
t _{d(on)}	Turn-On Delay Time	V _{DD} =15V, R _L =15 V _{GEN} =10V, R _G =3		23		ns
t _r	Turn-On Rise Time			16		
t _{d(off)}	Turn-Off Delay Time			73		
t _f	Turn-Off Fall Time			12		

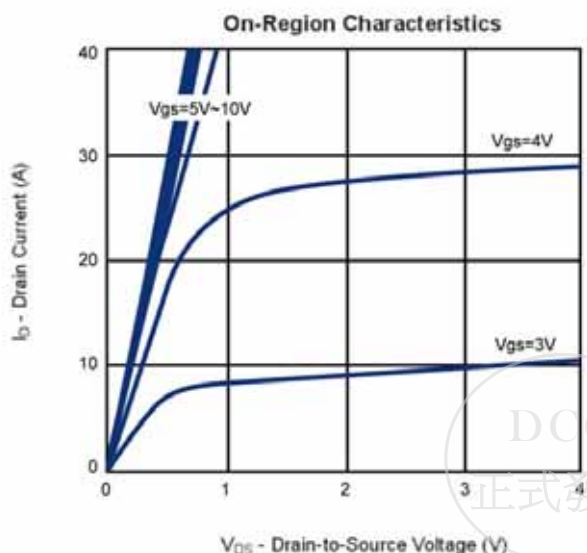
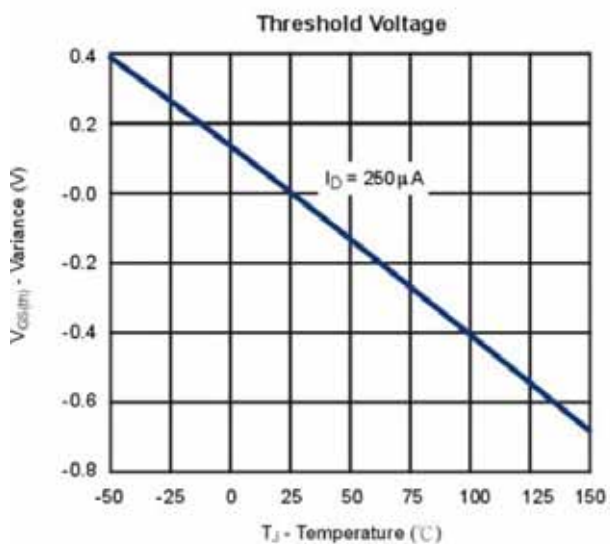
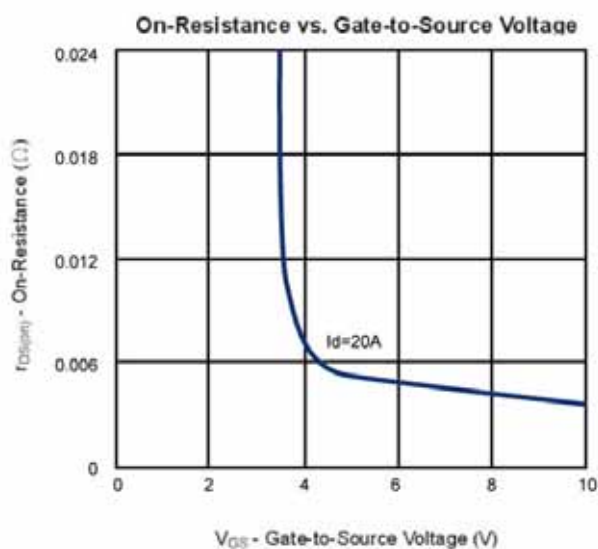
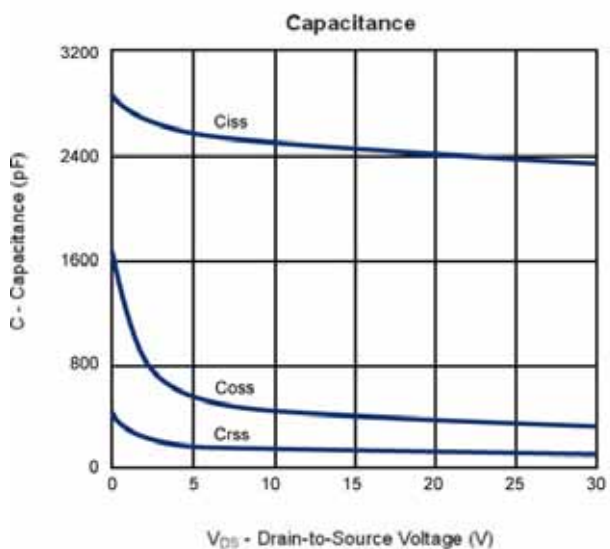
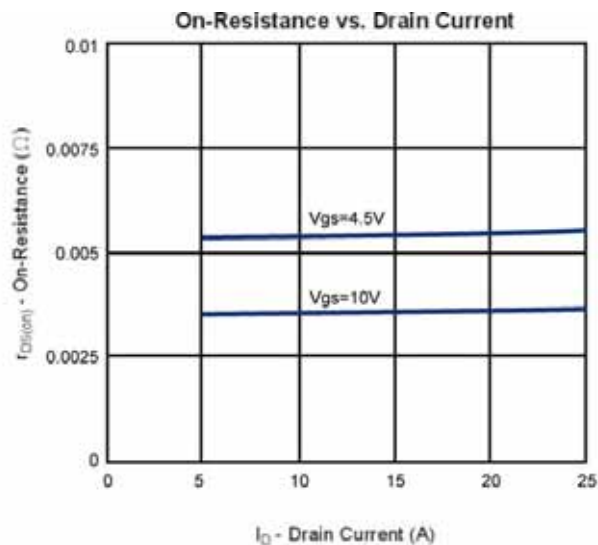
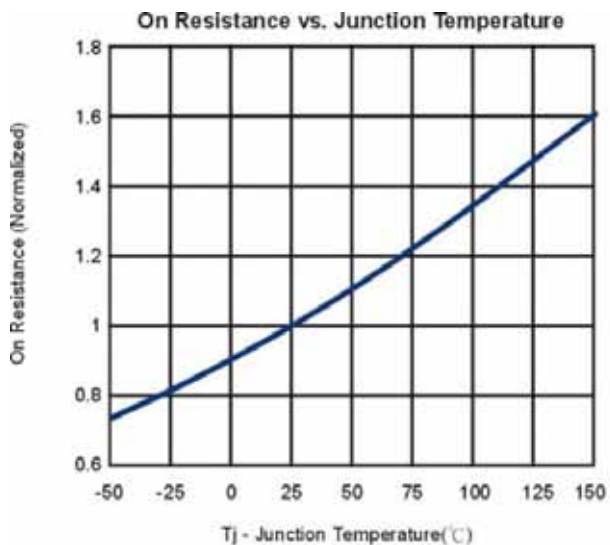
Note: a. Pulse test: pulse width 300us, duty cycle 2%, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.



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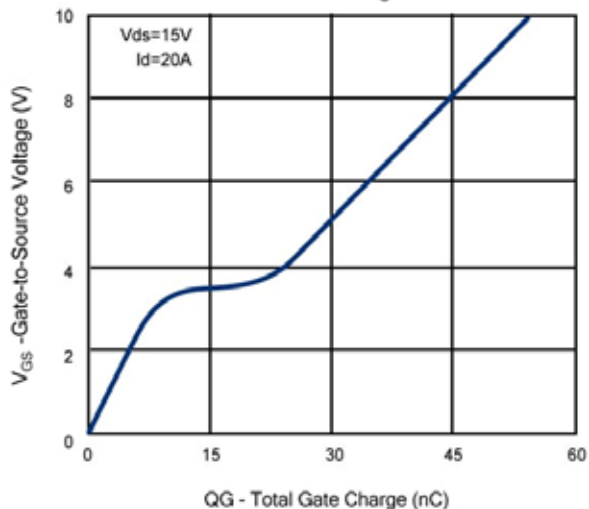
Typical Characteristics (T_J =25 Noted)



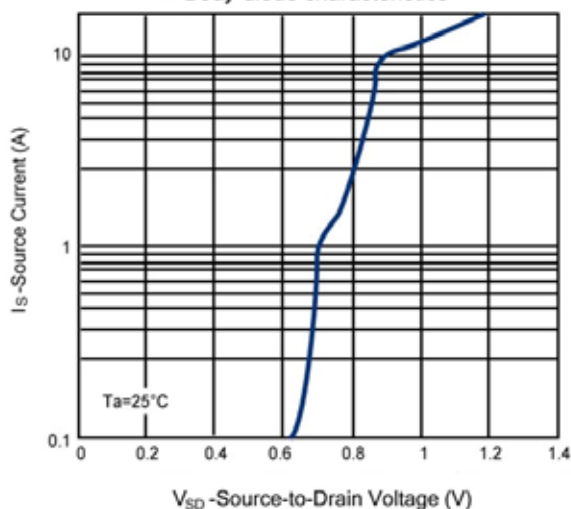
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Typical Characteristics (T_J =25 Noted)

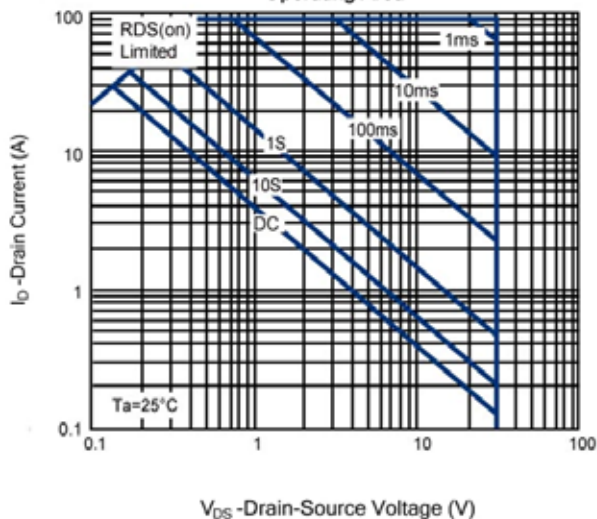
Gate Charge



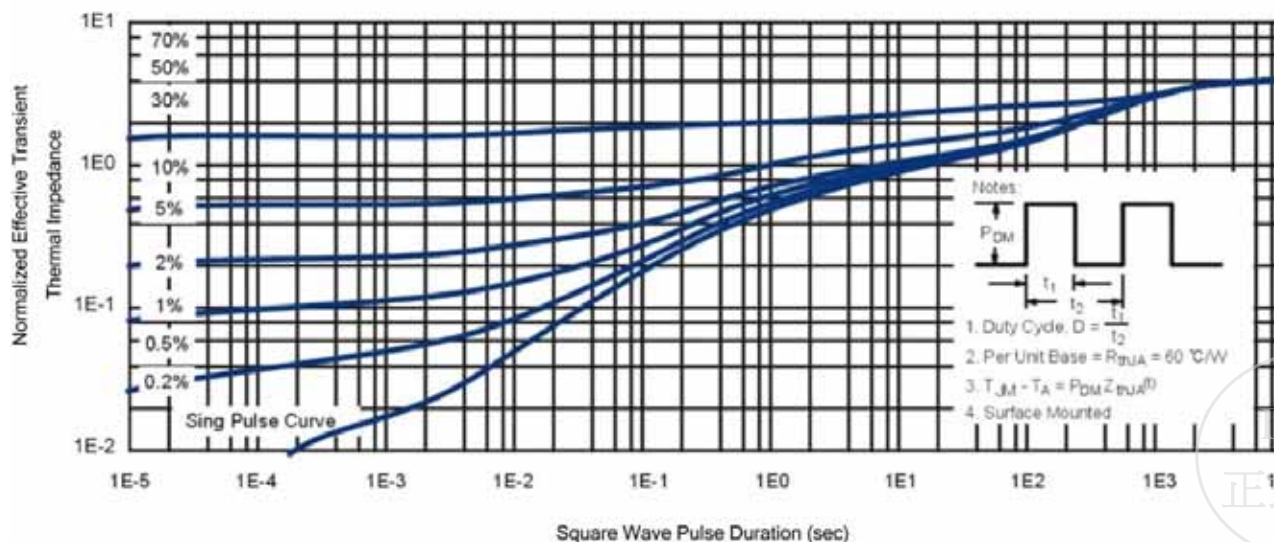
Body-diode characteristics



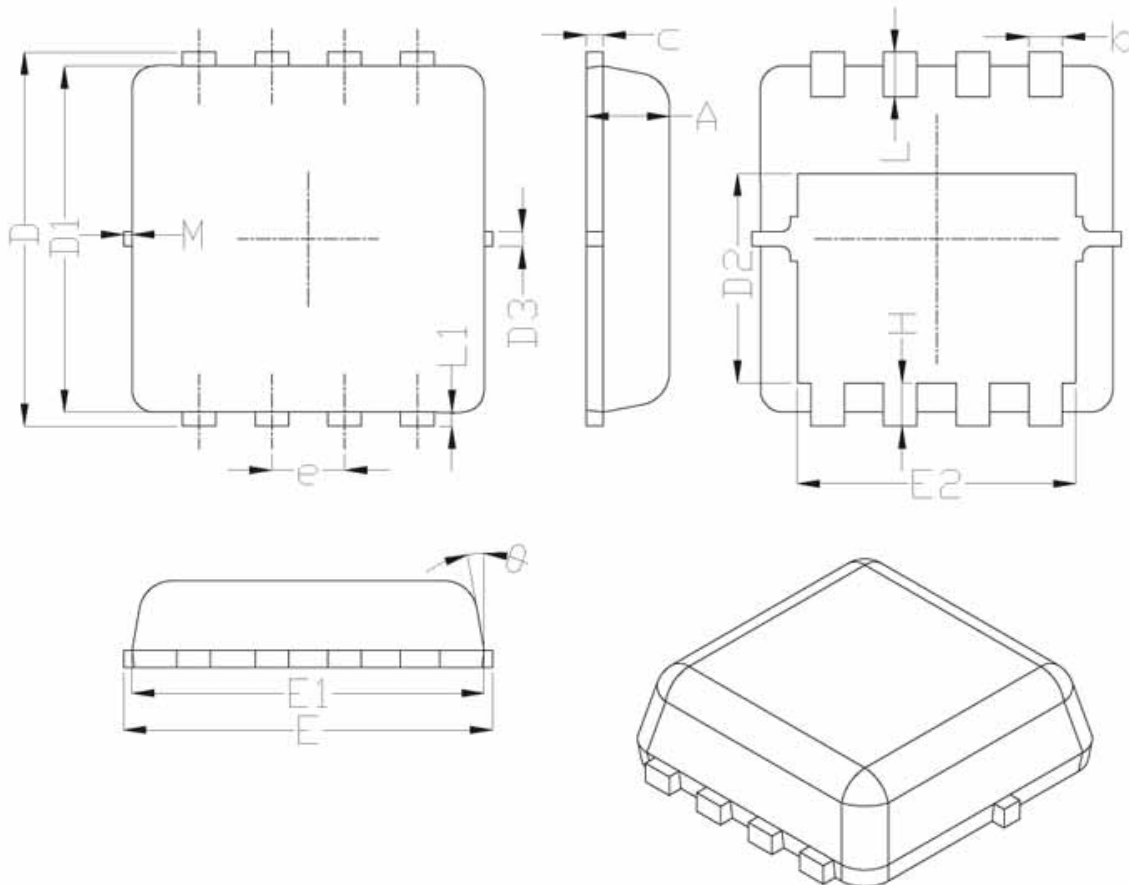
Maximum Forward Biased Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



DFN(S) 3.3x3.3 Package Outline



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
θ	---	10°	12°
M	*	*	0.15

* Not specified

