

N-Channel Logic Level Enhancement Mode Power MOSFET

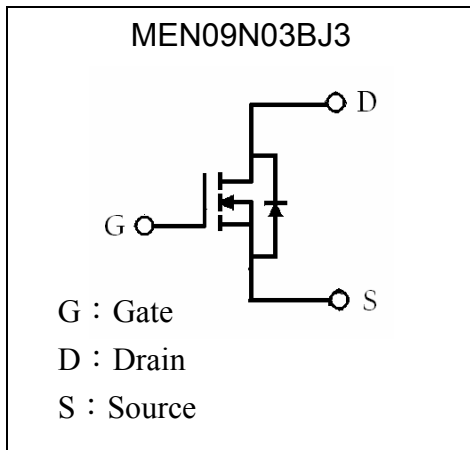
MEN09N03BJ3

BV_{DSS}	30V
I_D	50A
R_{DSON}	9m Ω

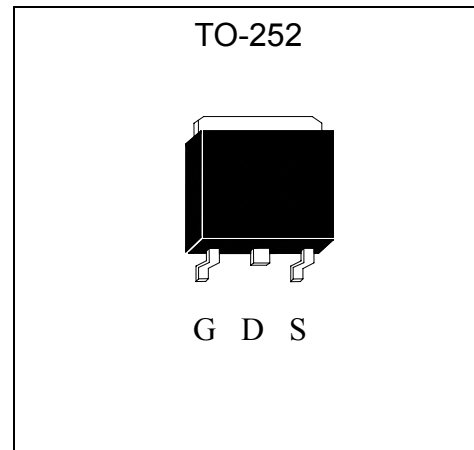
Features

- $V_{DS}=30V, I_D=50A, R_{DS(ON)}=9m\Omega$
- Low Gate Charge
- Simple Drive Requirement
- RoHS compliant package
- Repetitive Avalanche Rated
- Fast Switching Characteristic

Symbol



Outline



Absolute Maximum Ratings (T_C=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current @ T _C =25°C	I_D	50	A
Continuous Drain Current @ T _C =100°C	I_D	35	A
Pulsed Drain Current	I_{DM}	140 *1	A
Avalanche Current	I_{AS}	37.5	A
Avalanche Energy @ L=0.1mH, I _D =37.5A, R _g =25 Ω	E_{AS}	70	mJ
Repetitive Avalanche Energy @ L=0.05mH	E_{AR}	15 *2	mJ
Power Dissipation (T _C =25°C)	P_D	60	W
Power Dissipation (T _C =100°C)		32	W
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+175	°C

100% UIS testing in condition of V_D=15V, L=0.1mH, V_G=10V, I_L=25A, Rated V_{DS}=25V N-CH

Note : *1. Pulse width limited by maximum junction temperature
*2. Duty cycle ≤ 1%



Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	2.5	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	75	°C/W

Characteristics (T_c=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	30	-	-	V	V _{GS} =0, I _D =250μA
V _{GS(th)}	1.0	1.7	3.0	V	V _{DS} = V _{GS} , I _D =250μA
G _{FS}	-	20	-	S	V _{DS} =5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20
I _{DSS}	-	-	1	μA	V _{DS} =24V, V _{GS} =0
	-	-	25		V _{DS} =20V, V _{GS} =0, T _j =125°C
I _{D(ON)}	50	-	-	A	V _{GS} =10V, V _{DS} =10V
*R _{DS(ON)}	-	7.5	9	mΩ	V _{GS} =10V, I _D =25A
	-	12	15		V _{GS} =5V, I _D =20A
Dynamic					
R _g	-	1.7	-	Ω	V _{GS} =15mV, V _{DS} =0, f=1MHz
*Q _g (V _{GS} =10V)	-	23	-	nC	I _D =25A, V _{DS} =15V, V _{GS} =10V
*Q _g (V _{GS} =5V)	-	13	-		
*Q _{gs}	-	4.7	-		
*Q _{gd}	-	7.4	-		
*t _{d(ON)}	-	10	-	ns	V _{DS} =15V, I _D =25A, V _{GS} =10V, R _G =2.7Ω, R _D =0.6Ω
*t _r	-	8	-		
*t _{d(OFF)}	-	30	-		
*t _f	-	5	-		
C _{iss}	-	2020	-	pF	V _{GS} =0V, V _{DS} =15V, f=1MHz
C _{oss}	-	275	-		
C _{rss}	-	160	-		
Source-Drain Diode					
*I _S	-	-	50	A	
*I _{SM}	-	-	140		
*V _{SD}	-	-	1.3	V	I _F =I _S , V _{GS} =0
*t _{rr}	-	22	-	ns	I _F =I _S , dI _F /dt=100A/μs
*I _{RM(REC)}	-	180	-	A	
*Q _{rr}	-	12	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

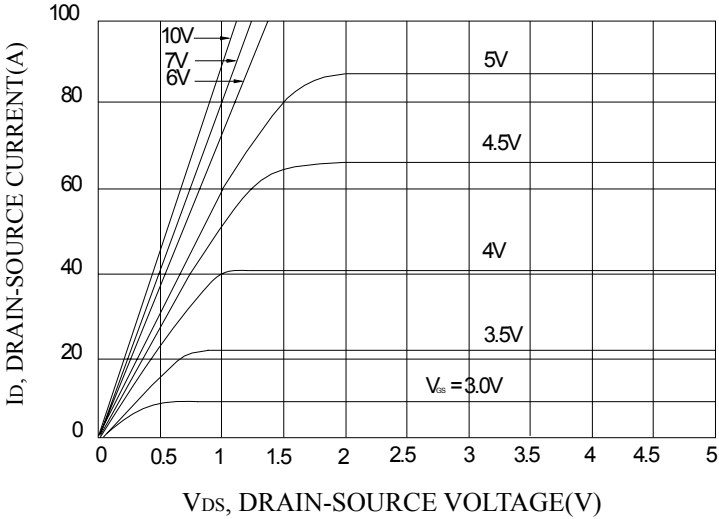
Ordering Information

Device	Package	Shipping	Marking
MEN09N03BJ3	TO-252 (RoHS compliant)	2500 pcs / Tape & Reel	09N03

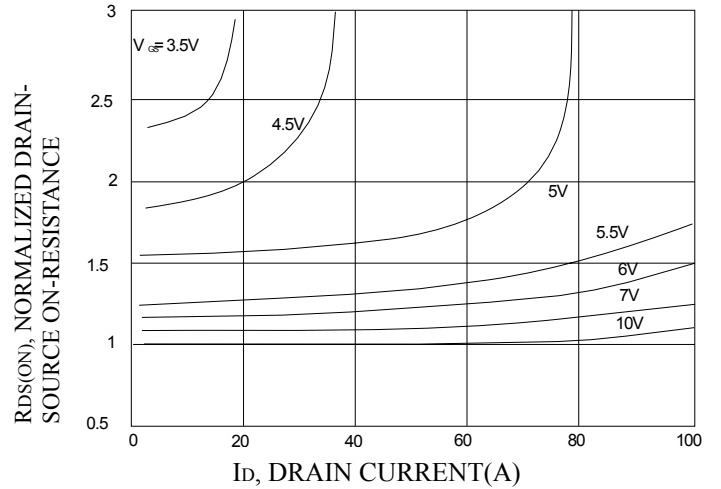


Characteristic Curves

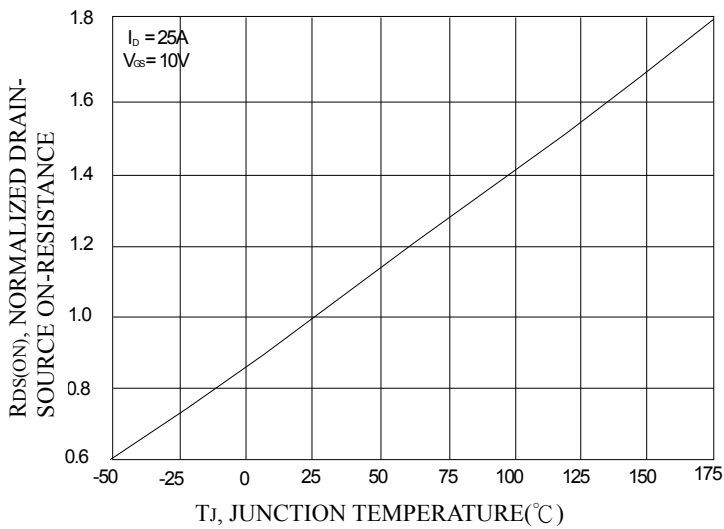
ON-REGION CHARACTERISTIC



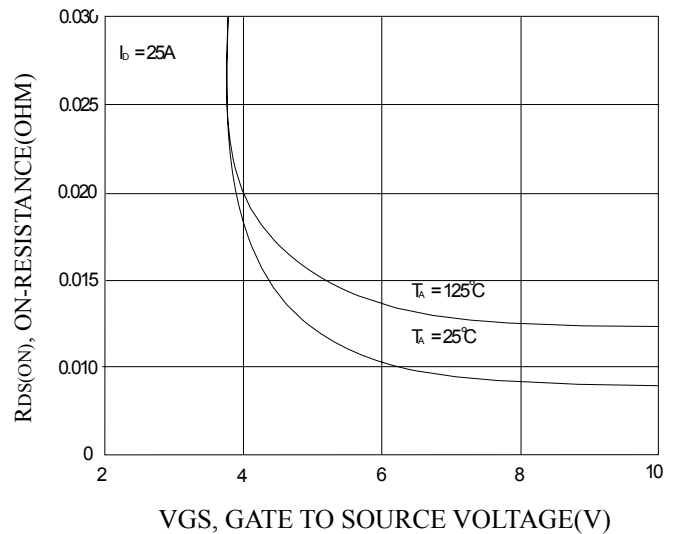
ON-RESISTANCE VARIATION WITH DRAIN CURRENT AND GATE VOLTAGE



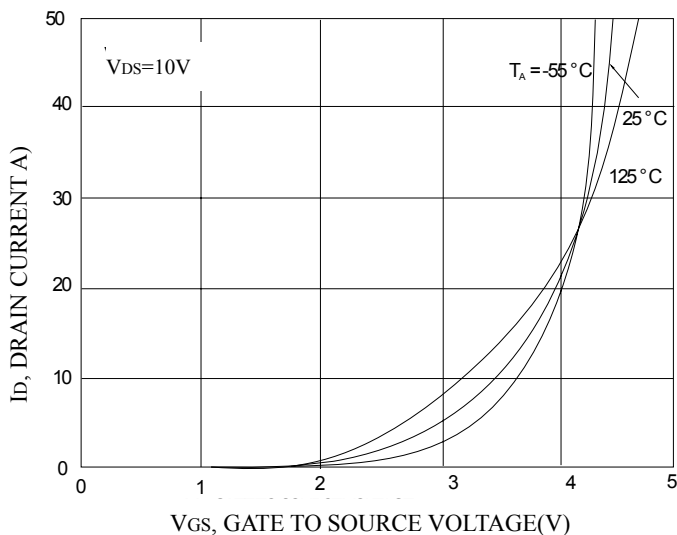
ON-RESISTANCE VARIATION WITH TEMPERATURE



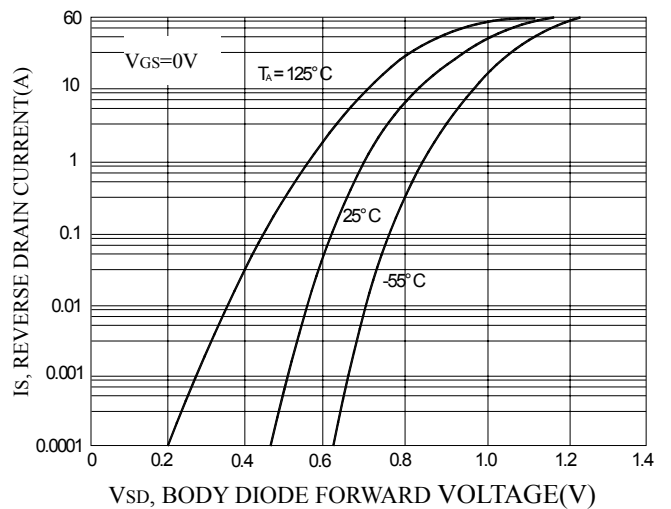
ON-RESISTANCE VARIATION WITH GATE TO SOURCE VOLTAGE



TRANSFER CHARACTERISTICS



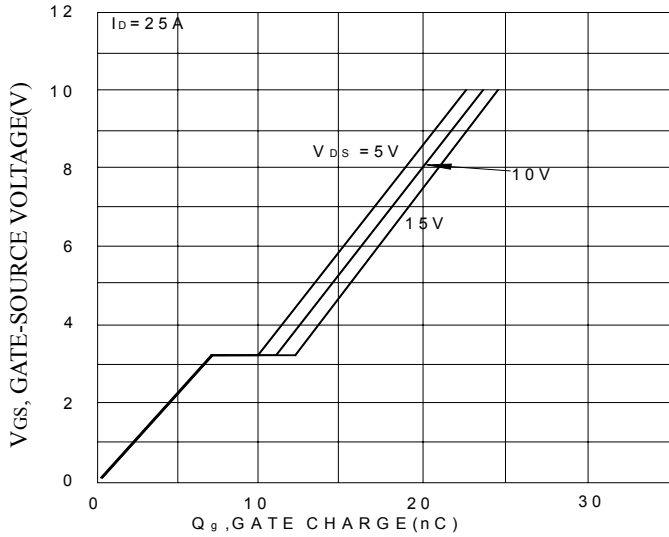
BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE



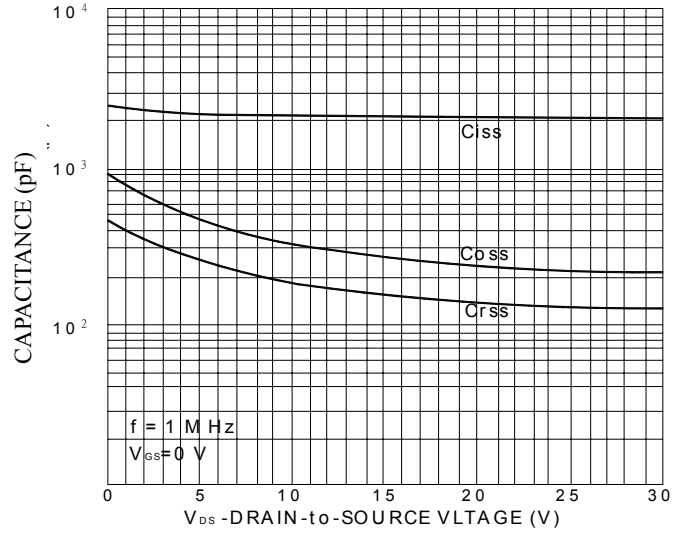


Characteristic Curves(Cont.)

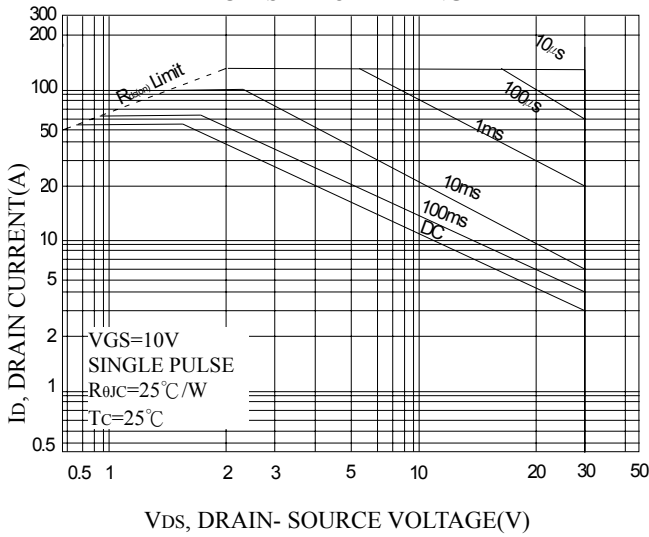
GATE CHARGE CHARACTERISTICS



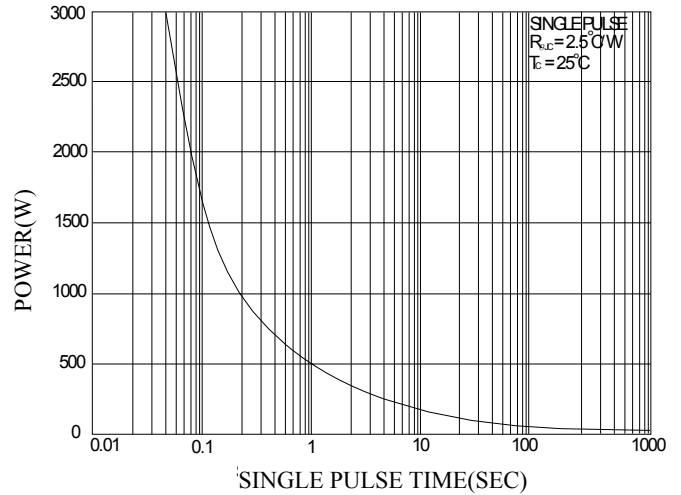
CAPACITANCE CHARACTERISTICS



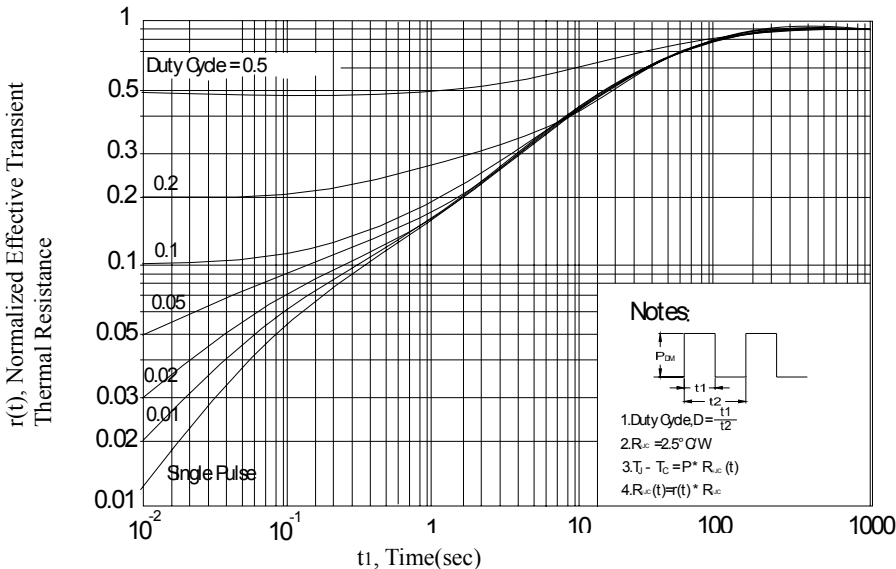
MAXIMUM SAFE OPERATING AREA



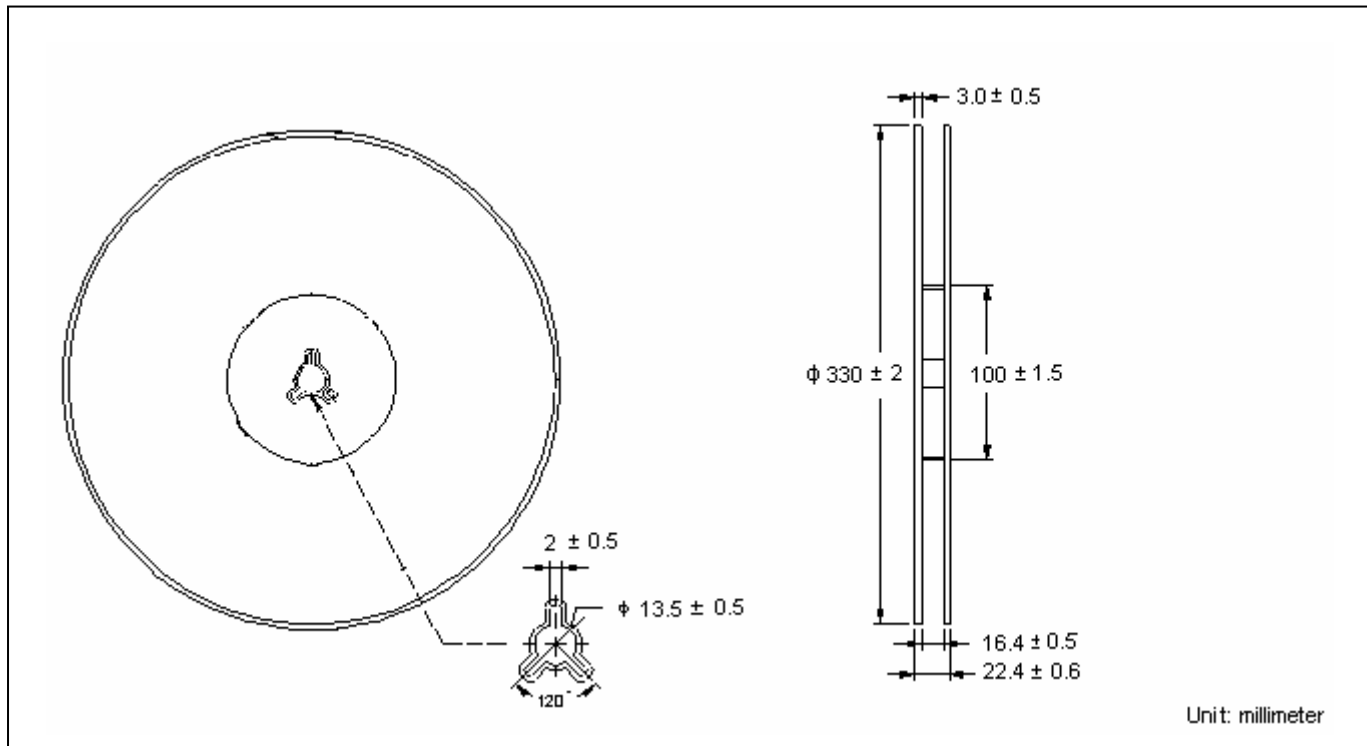
SINGLE PULSE MAXIMUM POWER DISSIPATION



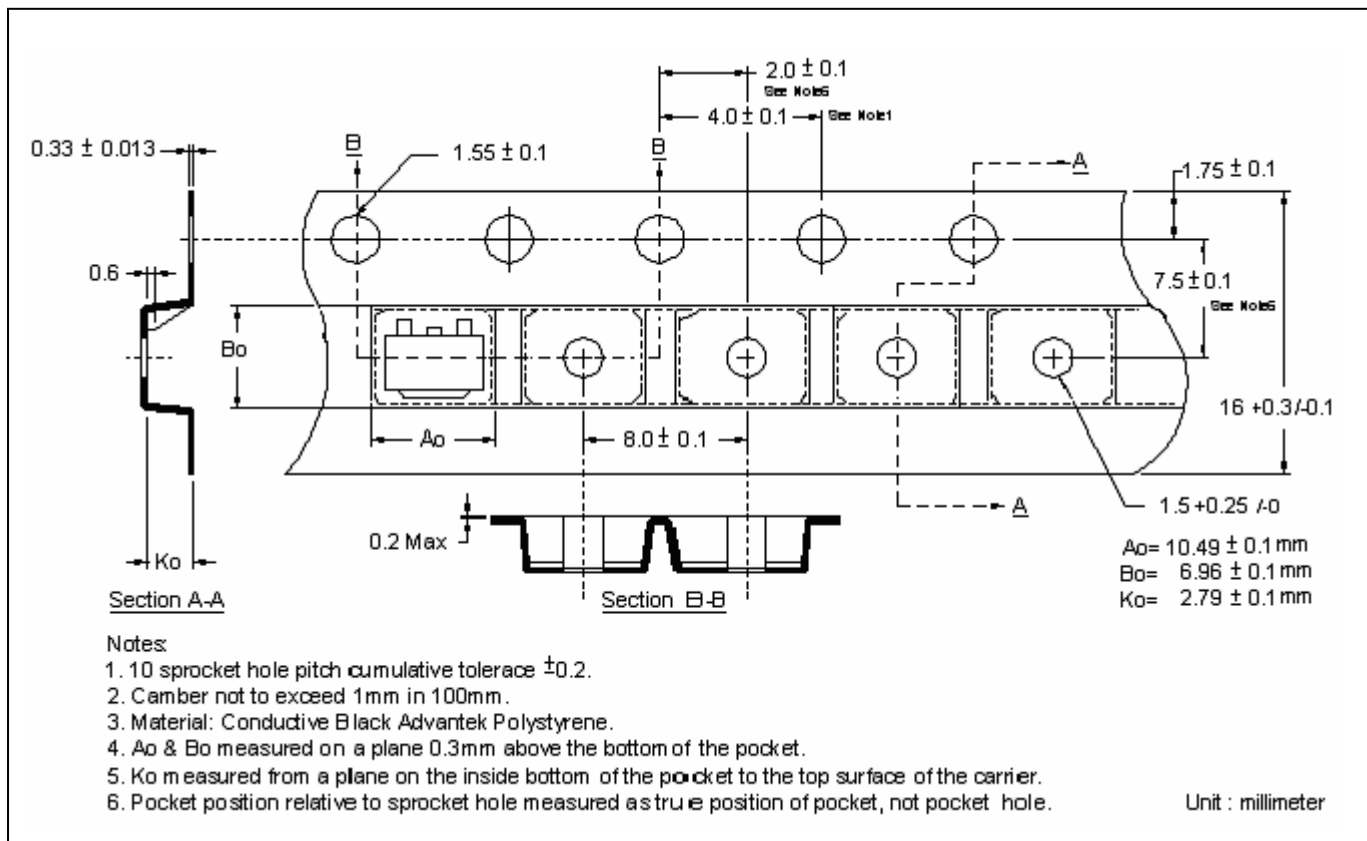
TRANSIENT THERMAL RESISTANCE



Reel Dimension



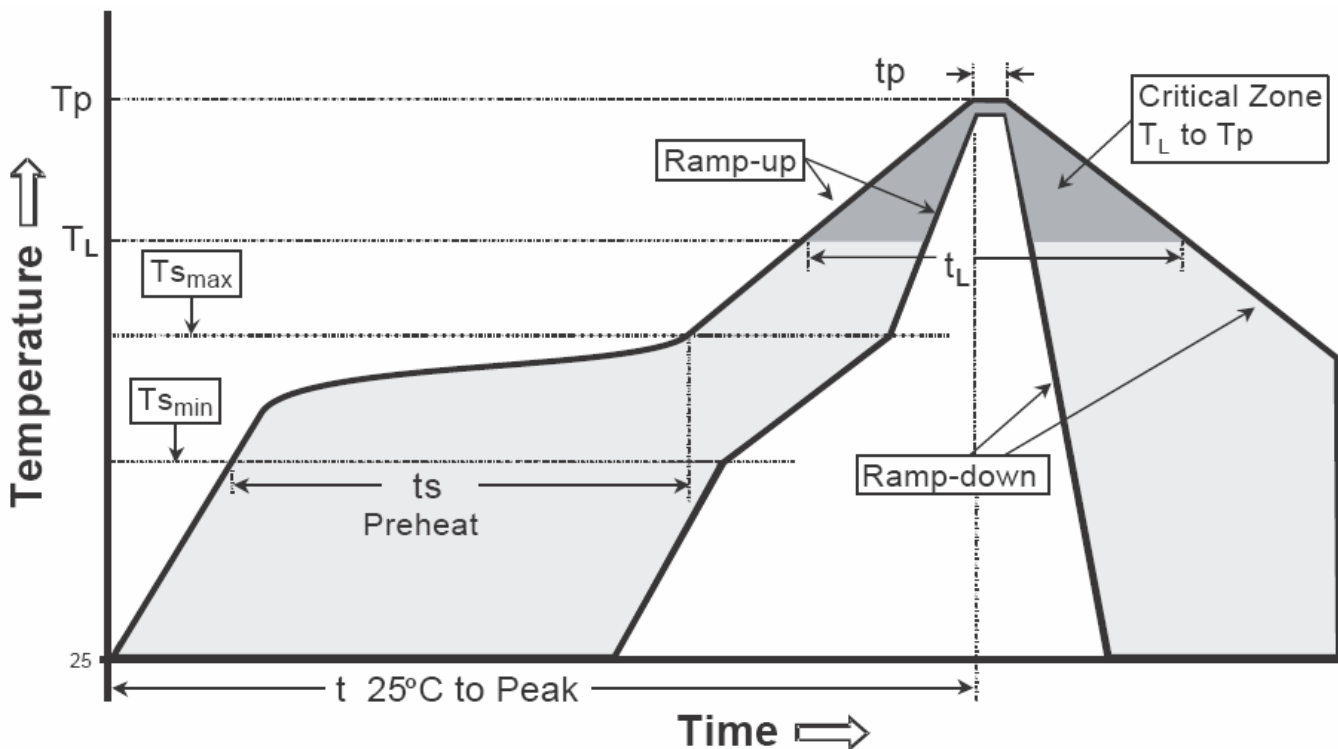
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

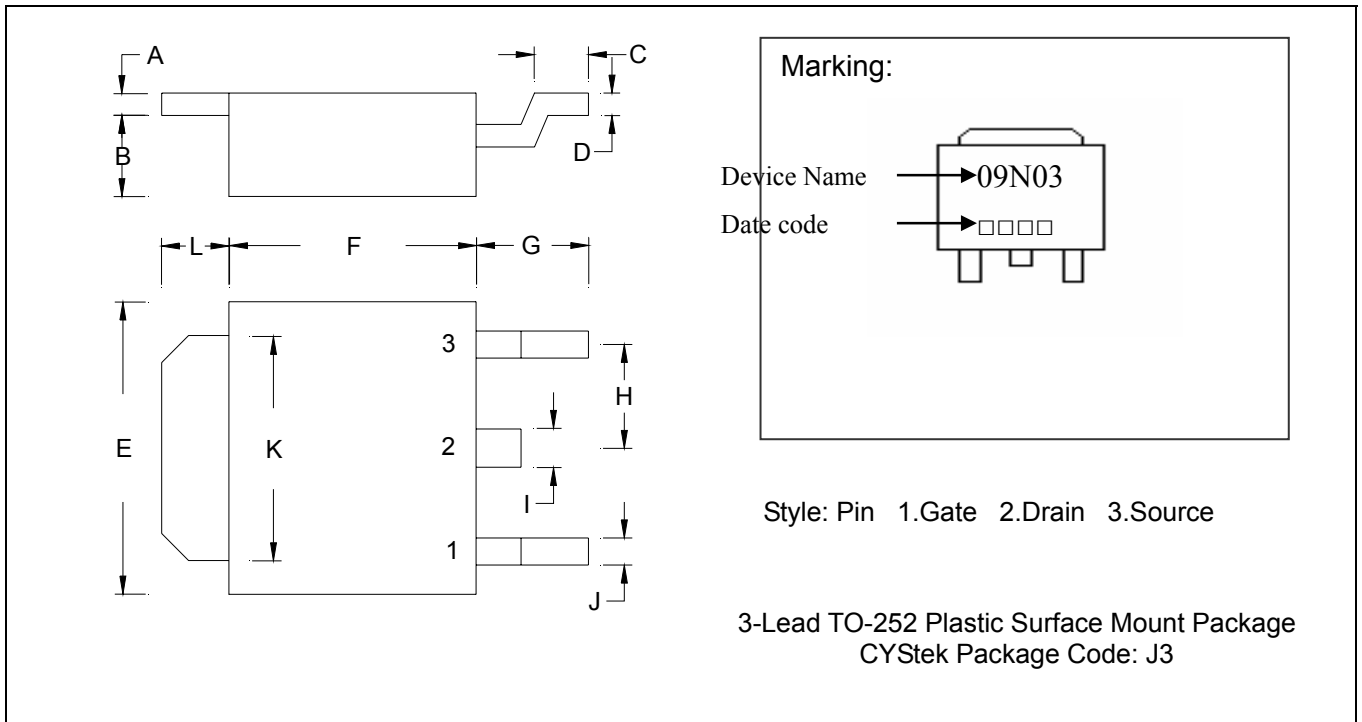
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0177	0.0217	0.45	0.55	G	0.0866	0.1102	2.20	2.80
B	0.0650	0.0768	1.65	1.95	H	-	*0.0906	-	*2.30
C	0.0354	0.0591	0.90	1.50	I	-	0.0449	-	1.14
D	0.0177	0.0236	0.45	0.60	J	-	0.0346	-	0.88
E	0.2441	0.2677	6.20	6.80	K	0.2047	0.2165	5.20	5.50
F	0.2125	0.2283	5.40	5.80	L	0.0551	0.0630	1.40	1.60

- Notes:**
- 1.Controlling dimension: millimeters.
 - 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 - 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : KFC; tin plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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