

**MFE120
MFE121
MFE122**

**CASE 20-03, STYLE 9
TO-72 (TO-206AF)**

**DUAL-GATE MOSFET
VHF AMPLIFIER**

N-CHANNEL — DEPLETION

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	+ 25	Vdc
Drain Current	I_D	30	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300 1.7	mW mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	- 65 to + 175	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Drain-Source Breakdown Voltage ($I_D = 100 \mu\text{Adc}$, $V_S = 0$, $V_{G1S} = - 4.0 \text{ V}$, $V_{G2S} = + 4.0 \text{ V}$)	$V_{(BR)DSX}$	25	—	—	Vdc
Gate 1-Source Breakdown Voltage ($I_{G1} = \pm 10 \mu\text{Adc}$, $V_{G2S} = 0$)	$V_{(BR)G1SO}$	± 7.0	—	± 20	Vdc
Gate 2-Source Breakdown Voltage ($I_{G2} = \pm 10 \mu\text{Adc}$, $V_{G2S} = 0$)	$V_{(BR)G2SO}$	± 7.0	—	± 20	Vdc
Gate 1 Leakage Current ($V_{G1S} = + 6.0 \text{ Vdc}$, $V_{G2S} = 0$, $V_{DS} = 0$)	I_{G1SS}	—	—	20	nAdc
Gate 2 Leakage Current ($V_{G2S} = + 6.0 \text{ Vdc}$, $V_{G1S} = 0$, $V_{DS} = 0$)	I_{G2SS}	—	—	20	nAdc
Gate 1 to Source Cutoff Voltage ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = 200 \mu\text{Adc}$)	$V_{G1S(\text{off})}$	—	—	- 4.0	Vdc
Gate 2 to Source Cutoff Voltage ($V_{DS} = 15 \text{ Vdc}$, $V_{G1S} = 0$, $I_D = 200 \mu\text{Adc}$)	$V_{G2S(\text{off})}$	—	—	- 4.0	Vdc
ON CHARACTERISTICS					
Zero-Gate-Voltage Drain Current ($V_{DS} = 15 \text{ Vdc}$, $V_{G1S} = 0$, $V_{G2S} = 4.0 \text{ Vdc}$)	I_{DSS}	2.0 5.0 2.0	7.0 10 9.0	18 30 20	mAdc

SMALL-SIGNAL CHARACTERISTICS

Forward Transfer Admittance (Gate 1 to Drain) ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = 10 \text{ mAdc}$, $f = 1.0 \text{ kHz}$)	MFE120,22 MFE121	$ Y_{fs} $	8000 10,000	— —	18,000 20,000	μmhos
Input Capacitance ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = I_{DSS}$, $f = 1.0 \text{ MHz}$)	MFE120,22 MFE121	C_{iss}	— —	4.5 4.5	7.0 6.0	pF
Reverse Transfer Capacitance ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = 6.0 \text{ mAdc}$, $f = 1.0 \text{ MHz}$)		C_{rss}	—	0.023	—	pF
Output Capacitance ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = I_{DSS}$, $f = 1.0 \text{ MHz}$)	MFE120,22 MFE121	C_{oss}	— —	2.5 2.5	4.0 3.5	pF

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ELECTRICAL CHARACTERISTICS (continued) ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
FUNCTIONAL CHARACTERISTICS					
Noise Figure ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = 6.0 \text{ mA dc}$, Z_S is optimized for NF) (f = 105 MHz — Figure 1) (f = 60 MHz — Figure 3) (f = 200 MHz — Figure 3)	MFE120 MFE121 MFE121	NF	— — —	2.9 2.6 2.6	5.0 5.0 5.0
Common Source Power Gain ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = 6.0 \text{ mA dc}$, Z_S is optimized for NF) (f = 105 MHz — Figure 1) (f = 60 MHz — Figure 3) (f = 200 MHz — Figure 3)	MFE120 MFE121 MFE121	G _{ps}	17 20 17	19.6 27.8 18.6	— — —
Level of Unwanted Signal for 1.0% Cross Modulation ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, $I_D = 6.0 \text{ mA dc}$)		—	—	100	—
Common-Source Conversion Power Gain (Gate 1 Injection, Figure 2) ($V_{DS} = 15 \text{ Vdc}$, $V_{G2S} = 4.0 \text{ Vdc}$, Local Oscillator Voltage = 925 mVrms) (Signal Frequency = 60 MHz, Local Oscillator Frequency = 104 MHz) (Signal Frequency = 200 MHz, Local Oscillator Frequency = 244 MHz)	MFE122 MFE122	G _c	15 12	16.5 13.3	— —

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FIGURE 1 – 105 MHz TEST CIRCUIT

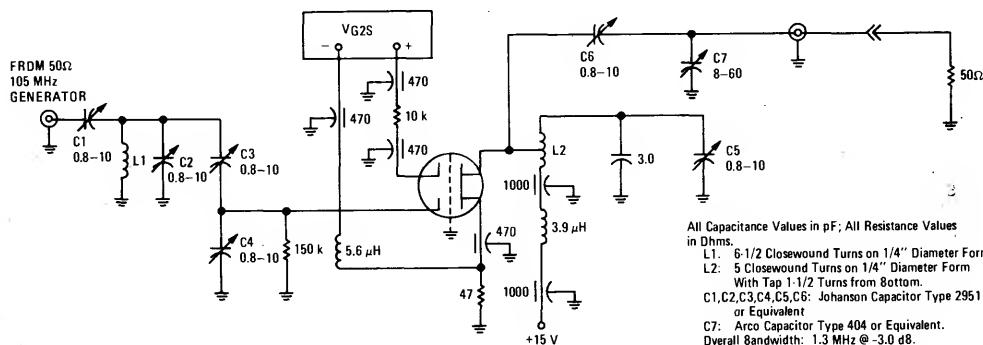
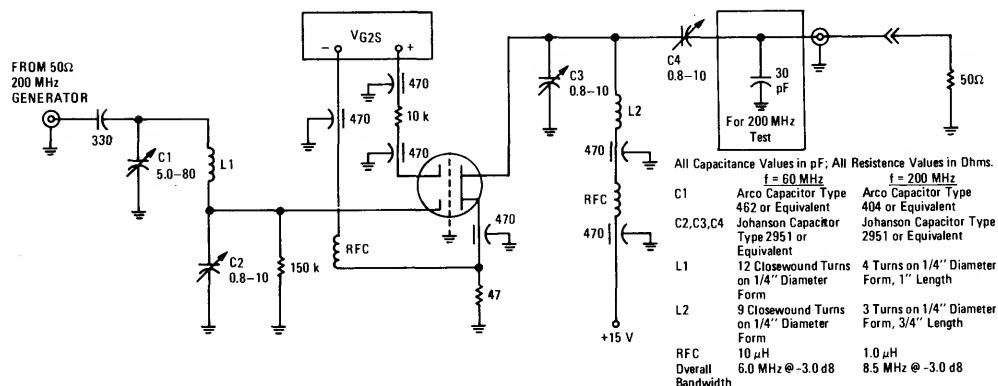
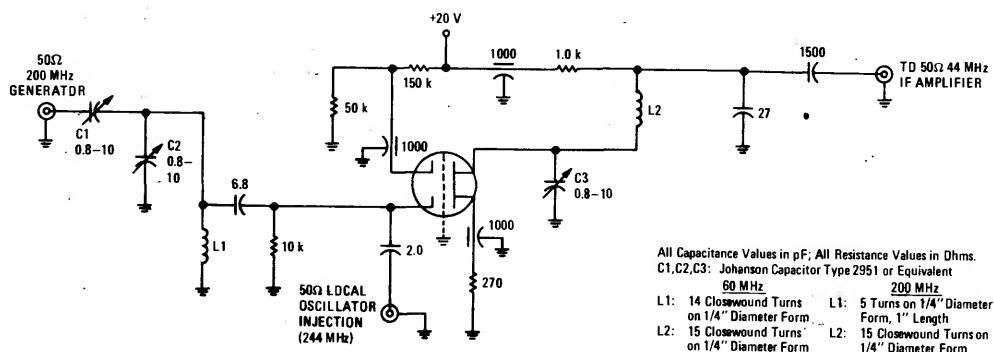


FIGURE 2 – 60 AND 200 MHZ TEST CIRCUIT



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FIGURE 3 – 60 AND 200 MHZ CONVERSION POWER GAIN



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COMMON-SOURCE ADMITTANCE PARAMETERS
($V_{DS} = 15$ Vdc, $V_{G2S} = 4.0$ Vdc, $I_D = 6.0$ mAdc)

FIGURE 4 – INPUT ADMITTANCE

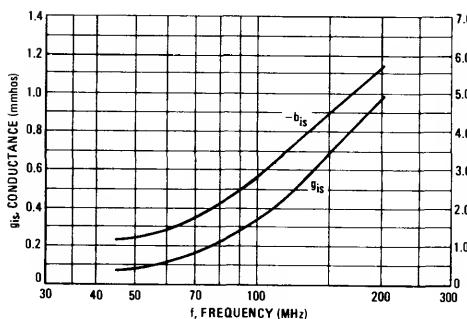


FIGURE 5 – REVERSE TRANSFER ADMITTANCE

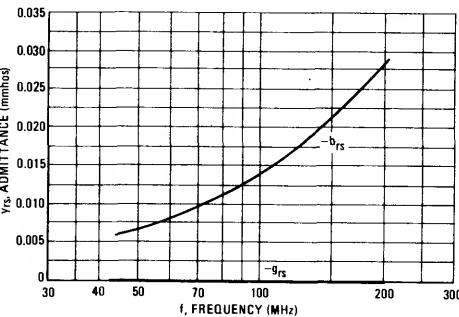


FIGURE 6 – FORWARD TRANSFER ADMITTANCE

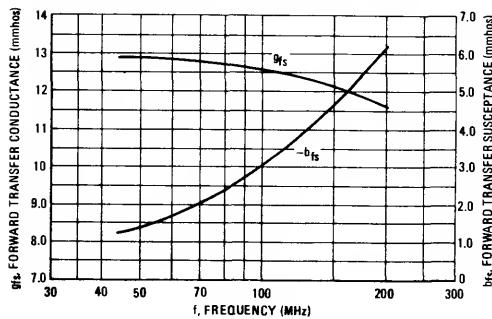
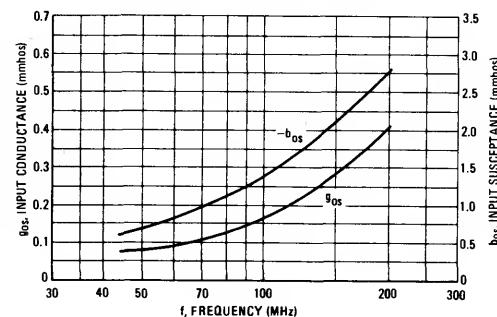


FIGURE 7 – OUTPUT ADMITTANCE



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FIGURE 8 – GAIN REDUCTION

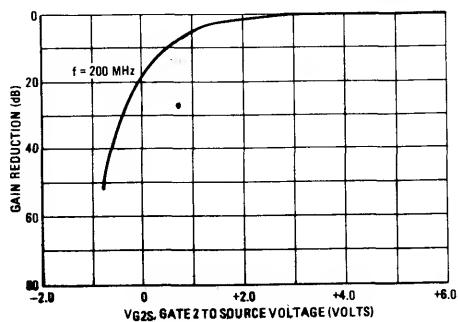


FIGURE 9 – CONVERSION POWER GAIN

