

# MG048B100006A

## 3 phase Inverter Module

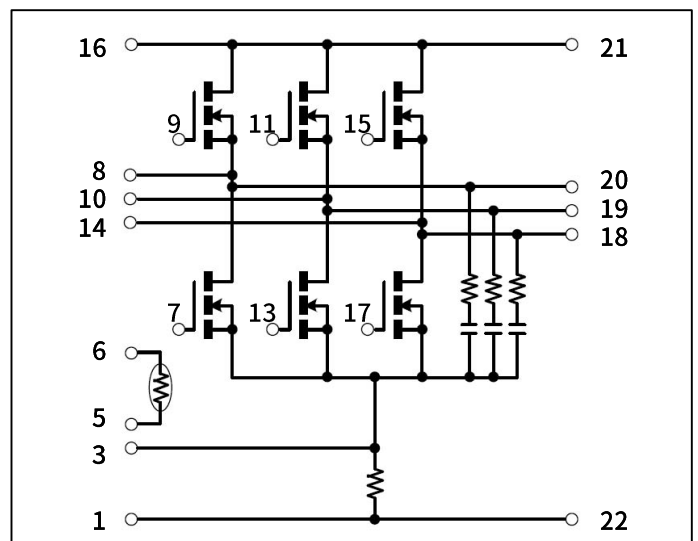
### Feature

- 3 phase Inverter
- MOSFET(N-channel)
- High current capacity
- Isolated package
- Low Ron
- Halogen free
- Pb free terminal
- RoHS:Yes

### Outline



### Equivalent circuit



## Absolute maximum ratings (Tc = 25°C unless otherwise specified)

### MOSFET

Item	Symbol	Conditions	Ratings	Unit
Channel temperature	T <sub>ch</sub>		150	°C
Drain-source voltage	V <sub>DSS</sub>		60	V
Gate-source voltage	V <sub>GSS</sub>		±20	V
Continuous drain current (DC)	I <sub>D</sub>		100	A
Continuous drain current (Peak)	I <sub>DP</sub>	Pulse width 10μs, Duty = 1/100	400	A
Total power dissipation	P <sub>T</sub>		147	W
Single avalanche current	I <sub>AS</sub>	Starting T <sub>ch</sub> =25°C T <sub>ch</sub> ≤150°C	58	A
Single avalanche energy	E <sub>AS</sub>	Starting T <sub>ch</sub> =25°C T <sub>ch</sub> ≤150°C	408	mJ

### Module

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T <sub>stg</sub>		-40~125	°C
Mounting torque	TOR	Fixing screw M3	1.2	N · m

## Electrical and thermal characteristics (Tc=25°C unless otherwise specified.)

These are characteristics of the 1 chip unless otherwise specified.

### MOSFET

Item	Symbol	Conditions	Ratings			Unit	
			Min.	Typ.	Max.		
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	60	—	—	V	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	—	—	1.0	μA	
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	—	—	±0.1	μA	
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	Chip	I <sub>D</sub> =50A, V <sub>GS</sub> =10V	—	1.84	—	mΩ
		Terminal	I <sub>D</sub> =50A, V <sub>GS</sub> =10V (Electrical characteristics of Q6)	—	2.92	3.65	mΩ
Gate threshold voltage	V <sub>TH</sub>	I <sub>D</sub> =1mA, V <sub>DS</sub> =10V	2.0	3.0	4.0	V	
Source-drain diode forward voltage	V <sub>SD</sub>	I <sub>S</sub> =100A, V <sub>GS</sub> =0V	—	—	1.5	V	
Total gate charge	Q <sub>g</sub>	V <sub>DD</sub> =48V, V <sub>GS</sub> =10V, I <sub>D</sub> =100A	—	129	—	nC	
Gate to source charge	Q <sub>gs</sub>		—	39	—		
Gate to drain charge	Q <sub>gd</sub>		—	63	—		
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz (Electrical characteristics of discrete MOSFET device)	—	8100	—	pF	
Reverse transfer capacitance	C <sub>rss</sub>		—	370	—		
Output capacitance	C <sub>oss</sub>		—	830	—		

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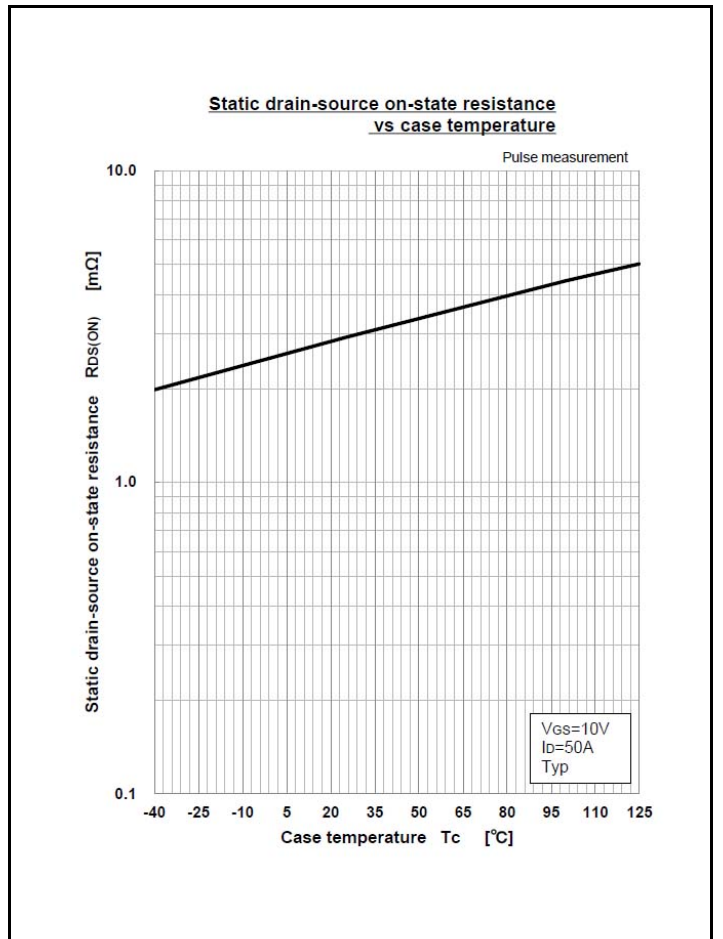
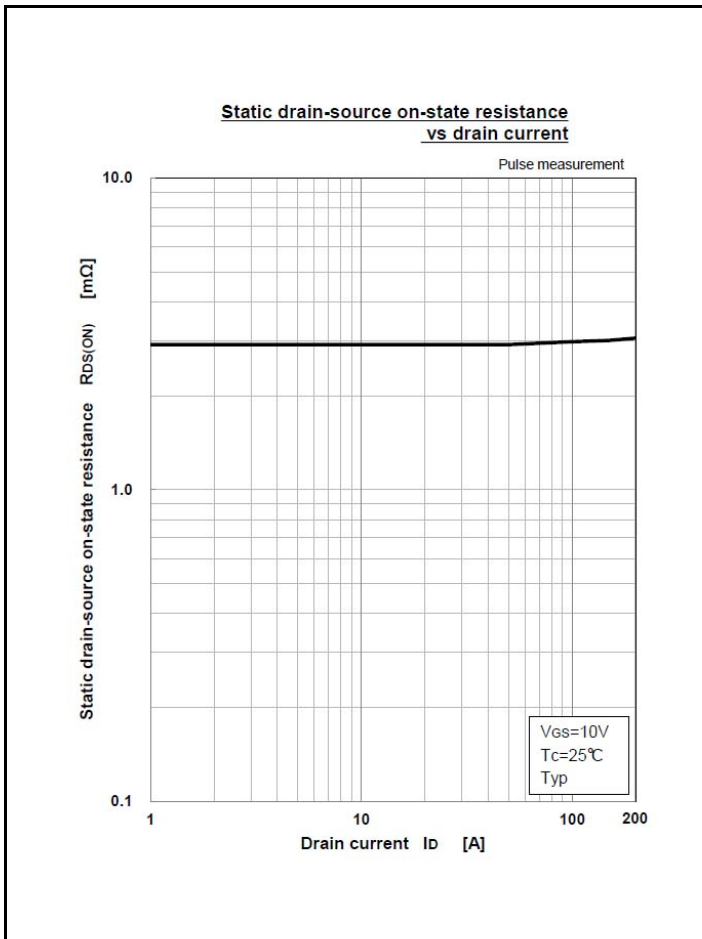
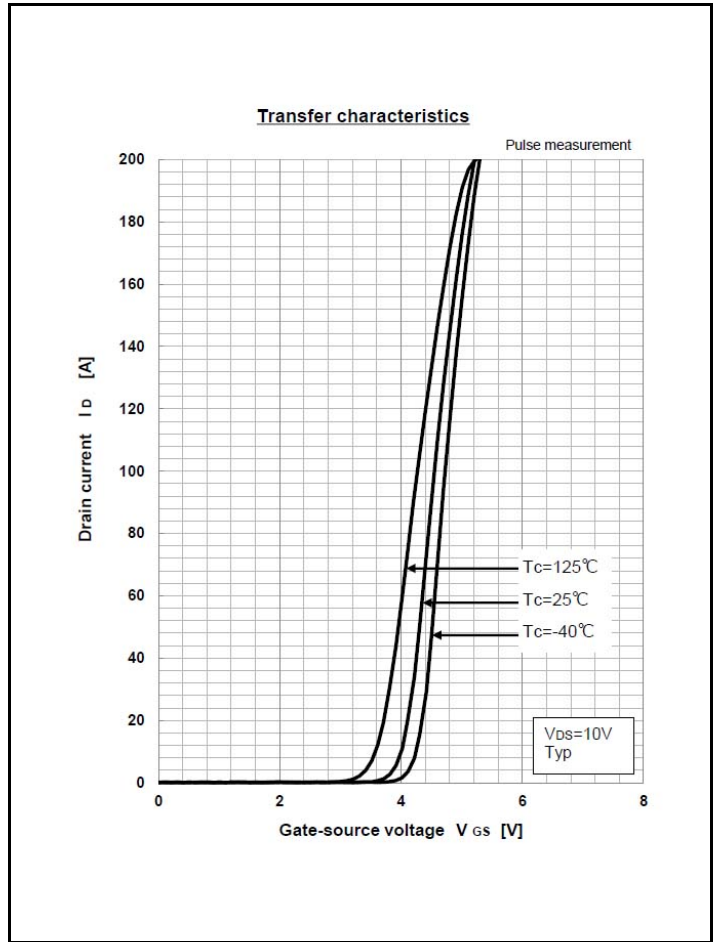
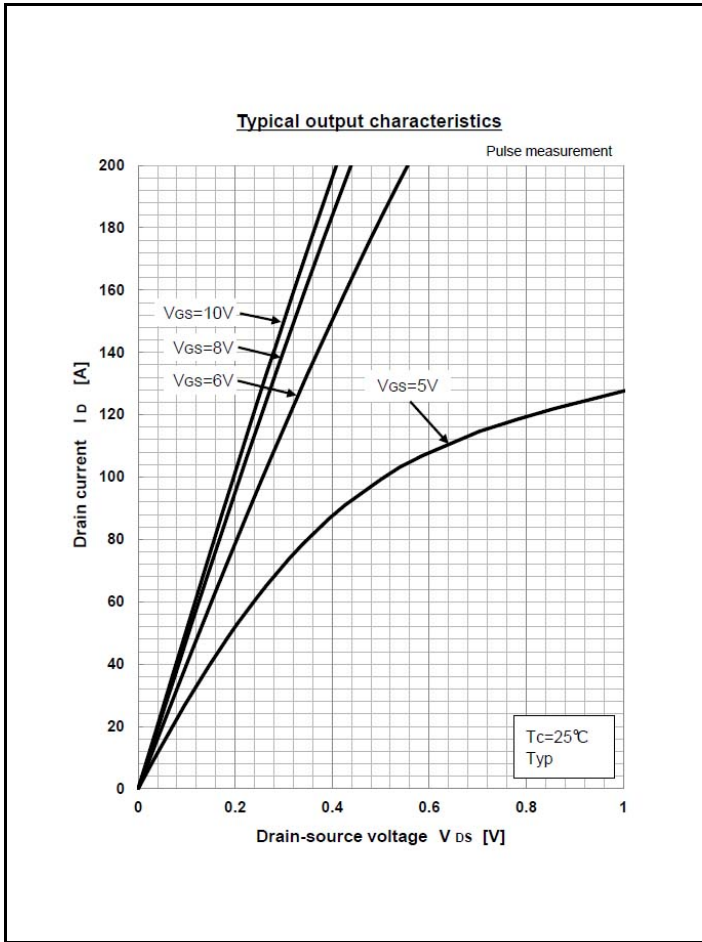
Turn—on delay time	td(on)	I <sub>D</sub> =50A, R <sub>L</sub> =0.6Ω, V <sub>DD</sub> =30V, R <sub>g</sub> =0Ω V <sub>GS(+)</sub> =10V, V <sub>GS(-)</sub> =0V, (Electrical characteristics of discrete MOSFET device)	—	10	—	ns
Rise time	tr		—	42	—	
Turn—off delay time	td(off)		—	100	—	
Fall time	tf		—	50	—	
Diode reverse recovery time	trr	I <sub>F</sub> =100A, V <sub>GS</sub> =0V, di/dt=100A/μs	—	93	—	ns
Diode reverse recovery charge	Qrr		—	115	—	nC

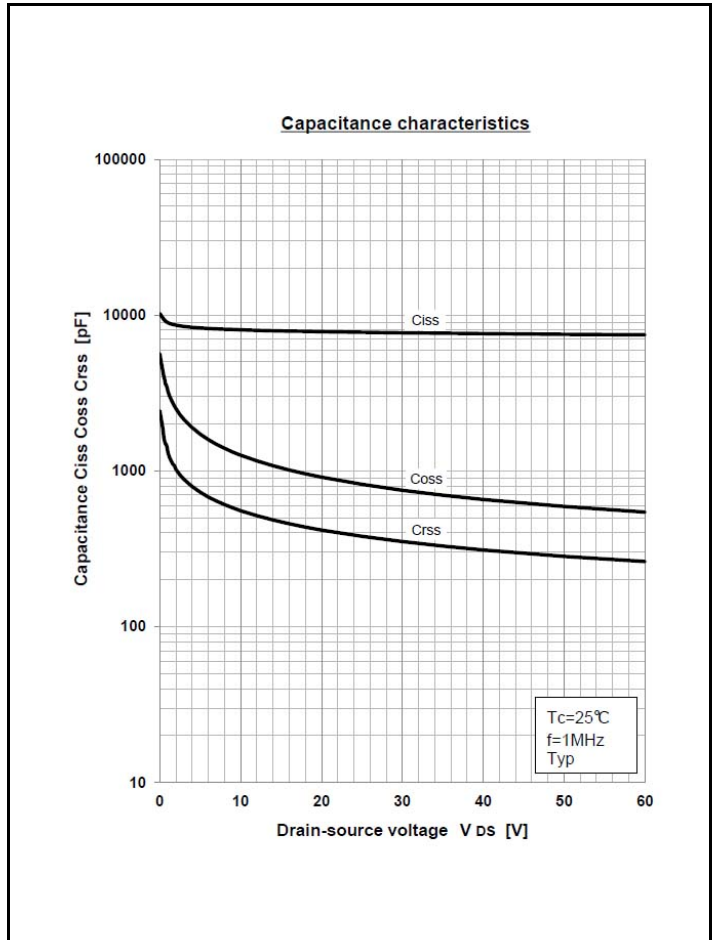
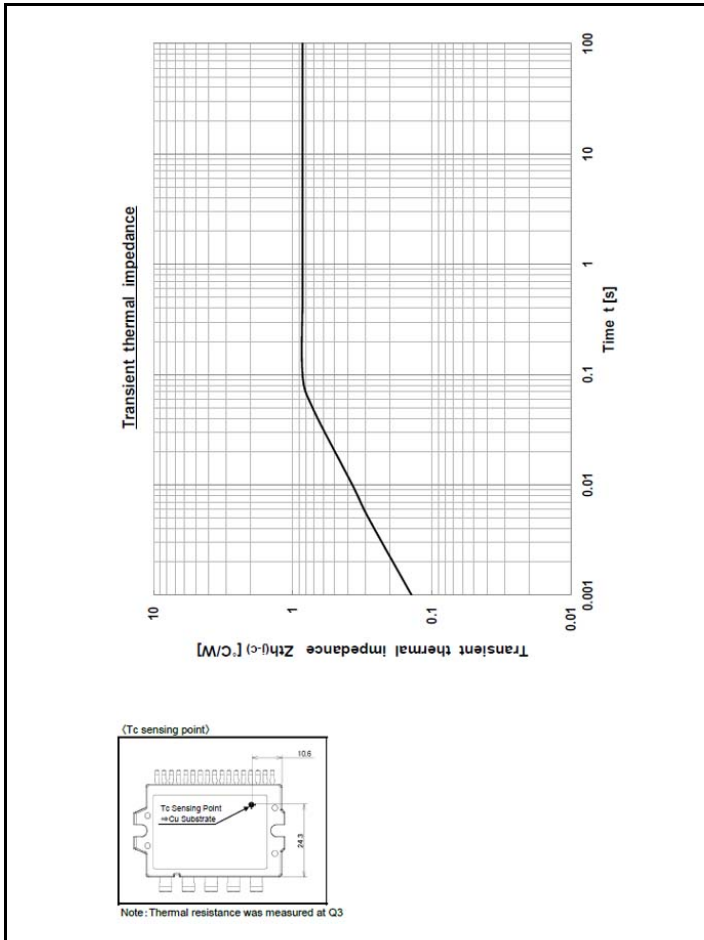
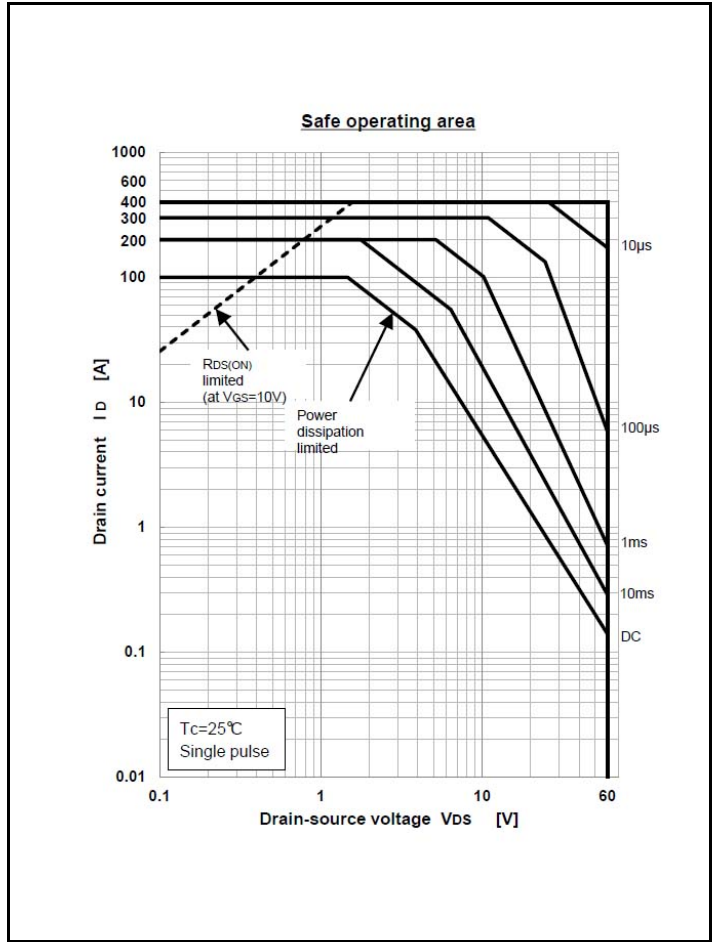
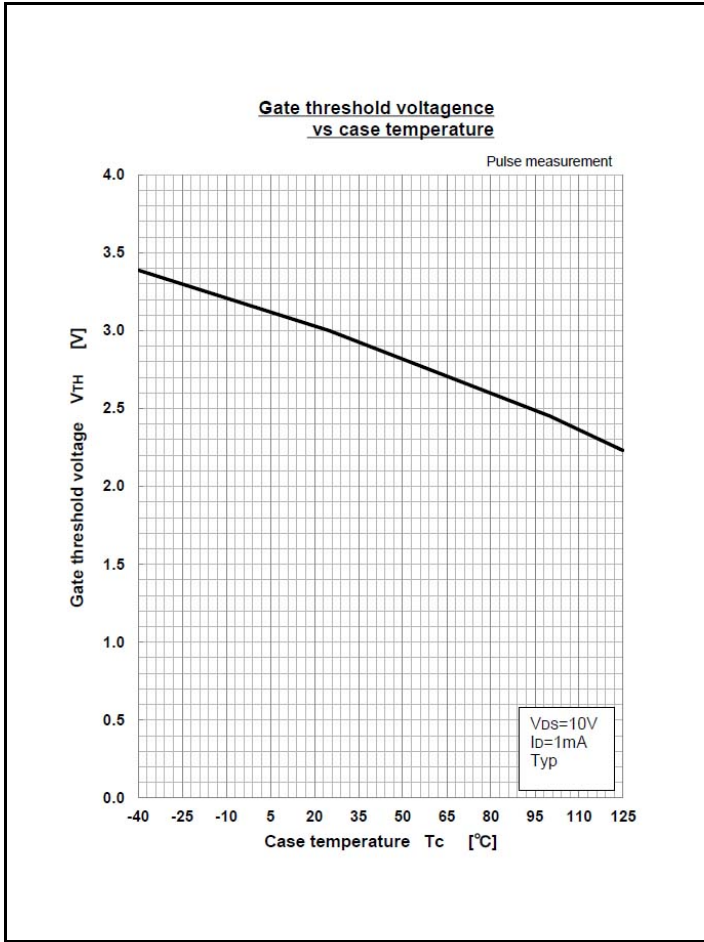
#### Module

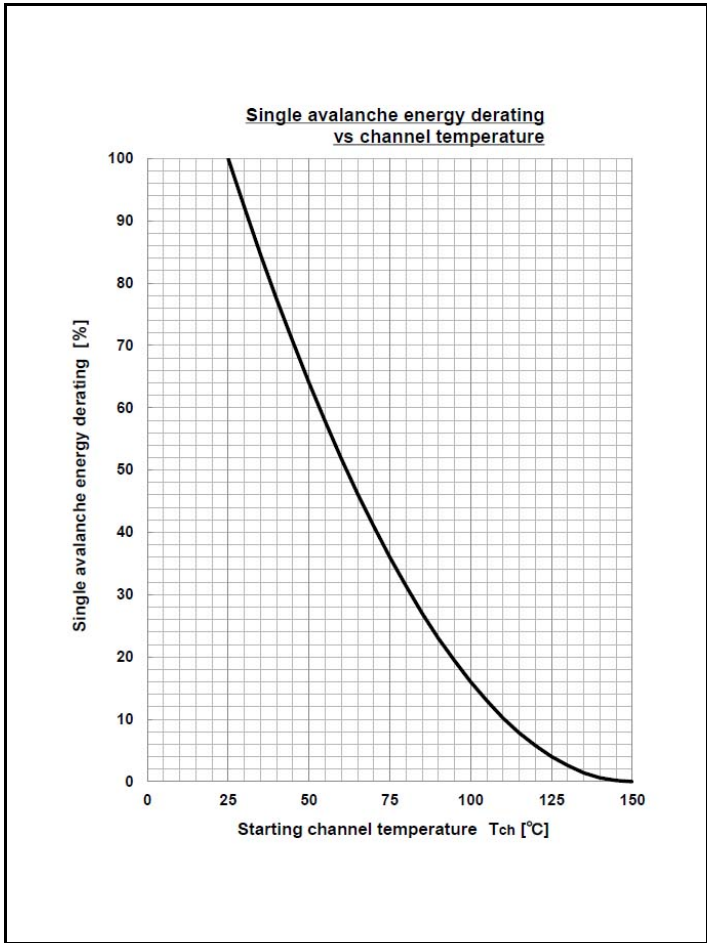
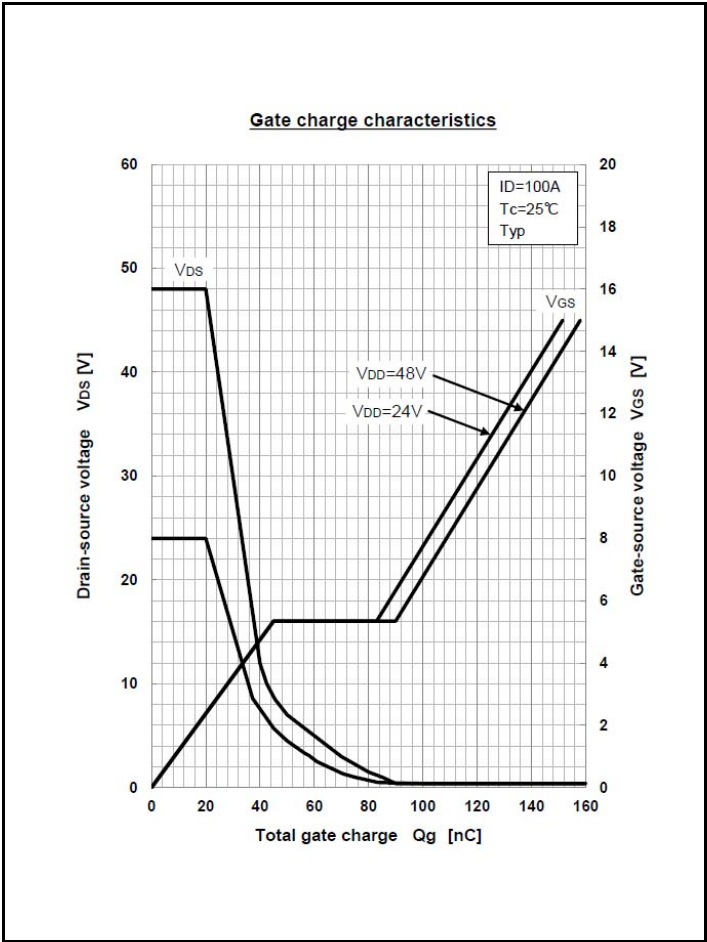
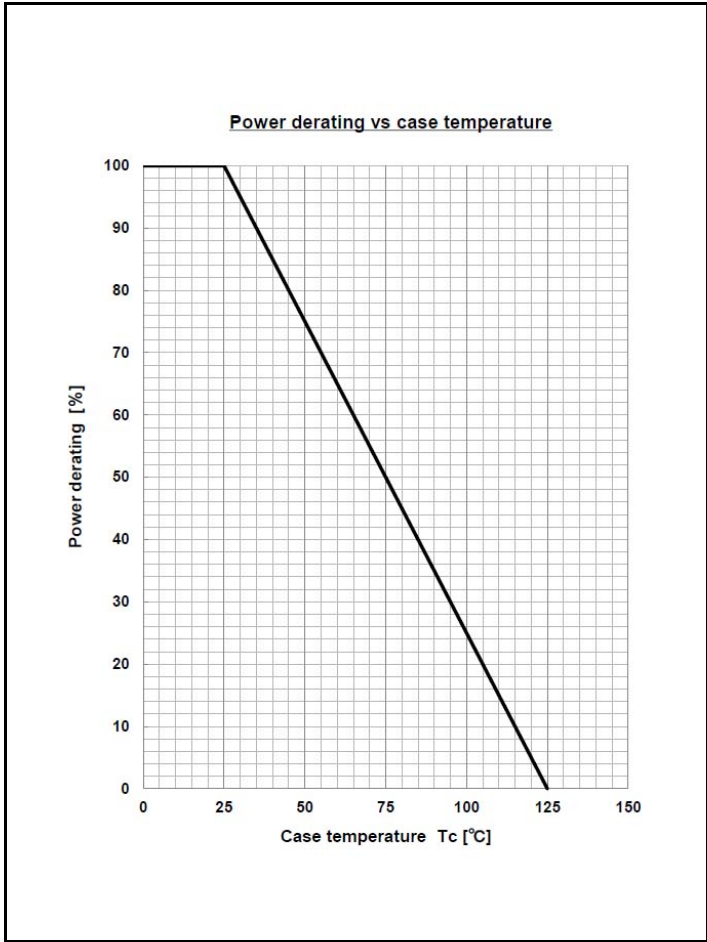
Item	Symbol	Conditions	Ratings			Unit
			Min.	Typ.	Max.	
Thermal resistance	R <sub>th(j-c)</sub>	Junction to case, with heatsink	—	—	0.85	°C/W

Note : Thermal resistance was measured at Q3

# CHARACTERISTIC DIAGRAMS





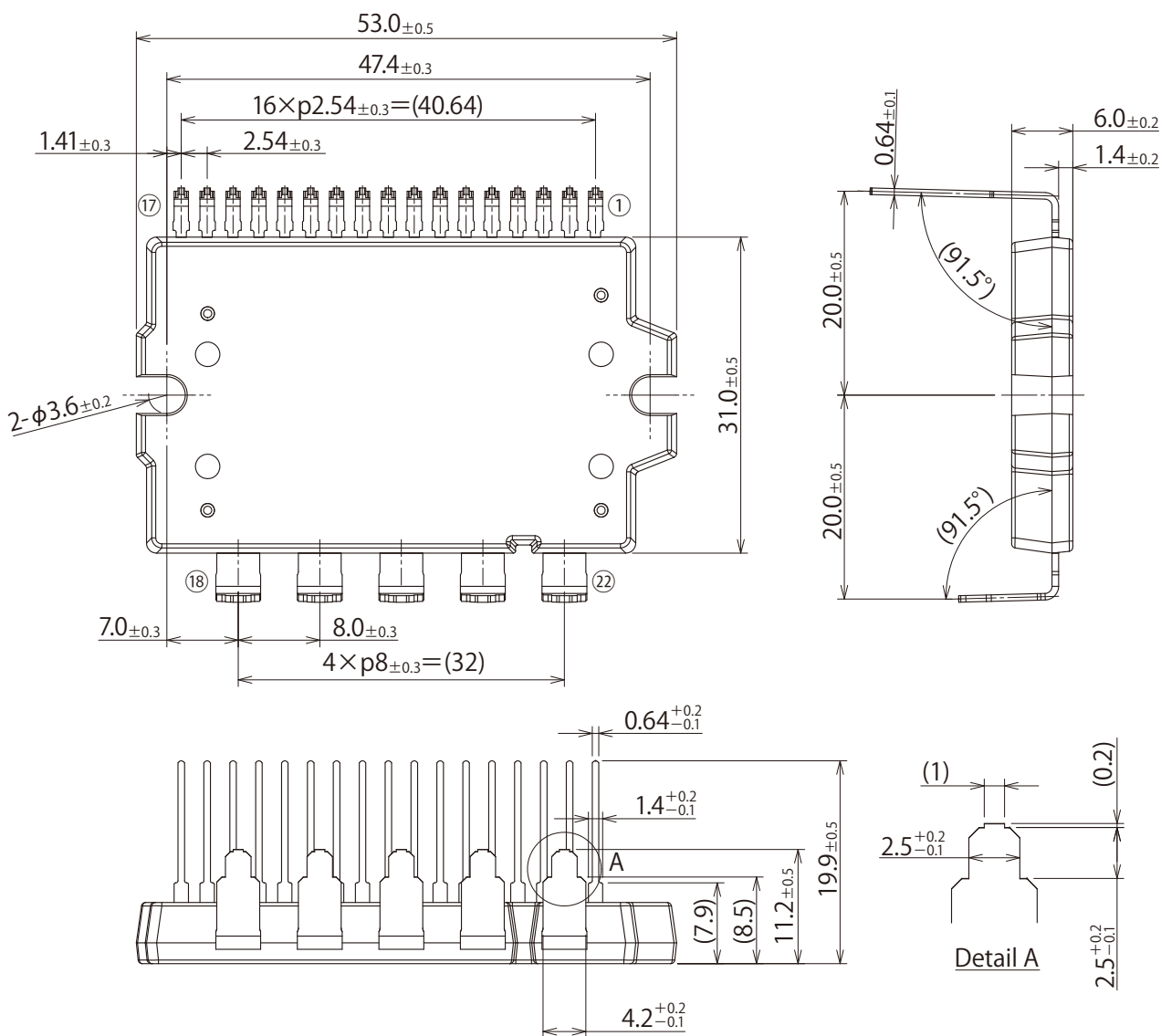


# Package Outline-Dimensions

unit : mm

F8

JEDEC Code	-
JEITA Code	-
House Name	MG048



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