

UNISONIC TECHNOLOGIES CO., LTD

MGBR10L120

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

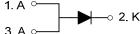
DESCRIPTION

The UTC **MGBR10L120** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed



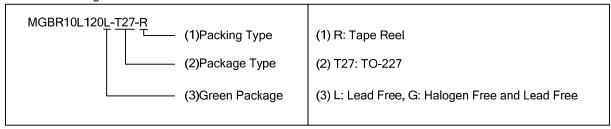


3. A ○──

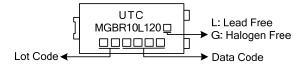
■ ORDERING INFORMATION

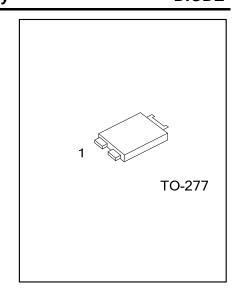
Ordering Number Package Pin Assignment Packing Lead Free Halogen Free 1 2 3 MGBR10L120L-T27-R MGBR10L120G-T27-R TO-277 A K A Tape Reel

Note: Pin Assignment: A: Anode K: Common Cathode



MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT	
DC Blocking Voltage	V_{RM}	120	V	
Working Peak Reverse Voltage	V_{RWM}	120	V	
Peak Repetitive Reverse Voltage	V_{RRM}	120	V	
Average Rectified Output Current T _C =125°C	Io	10	Α	
Non-Repetitive Peak Forward Surge Current 8.3ms	I _{FSM}	160	Α	
Single Half Sine-Wave Superimposed on Rated Load	. 5			
Operating Junction Temperature	TJ	-65~+150	°C	
Storage Temperature	T _{STG}	-65~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	73	°C/W
Junction to Case	$\theta_{ m JC}$	13	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A =25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	120			V
Forward Voltage Drop	VEM	I _F =10A, T _J =25°C			0.82	V
		I _F =10A, T _J =125°C			0.70	V
Leakage Current (Note 1)	I PM	V _R =120V, T _J =25°C			400	μΑ
		V _R =120V, T _J =125°C			30	mΑ

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

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