

UNISONIC TECHNOLOGIES CO., LTD

MGBR20L40C

Preliminary

DIODE

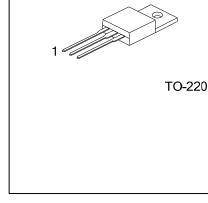
DUAL MOS GATED BARRIER RECTIFIERS

DESCRIPTION

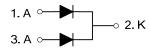
The UTC MGBR20L40C is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with high current capability, low forward voltage and high switching speed, etc.

■ FEATURES

- * Low forward voltage
- * High switching speed
- * High current capability



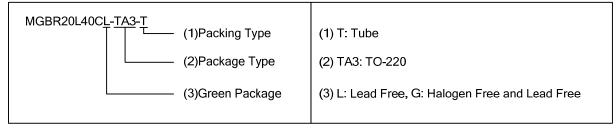
■ SYMBOL



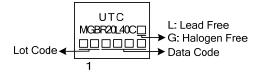
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing	
Lead Free	Halogen Free	Fackage	1	2	3	Facking	
MGBR20L40CL-TA3-T	MGBR20L40CG-TA3-T	TO-220	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	40	V
Working Peak Reverse Voltage		V_{RWM}	40	V
Peak Repetitive Reverse Voltage		V_{RRM}	40	V
Average Rectified Forward Current	Per Leg		10	Α
(Rated VR-20KHz Square Wave) – 50% duty cycle	Total	I _O	20	Α
Peak Forward Surge Current - 1/2 60Hz		I_{FSM}	120	Α
Peak Repetitive Reverse Surge Current (2µS-1KHz)		I_{RRM}	2	Α
Operating Junction Temperature		T _J	-65~+150	°C
Storage Temperature		T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ _{JC}	2	°C/W

■ ELECTRICAL CHARACTERISTICS-(PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	40			٧
Instantaneous Forward Voltage	V_{FM}	I _F =20A, T _J =25°C			0.55	V
		I _F =20A, T _J =125°C			0.53	V
Instantaneous Reverse Current (Note 1)	I _{RM}	V _R =40V, T _J =25°C			500	μΑ
		V _R =40V, T _J =125°C			100	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} Thermal resistance junction to case mounted on heatsink.

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