MGBR30L150C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

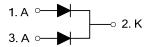
■ DESCRIPTION

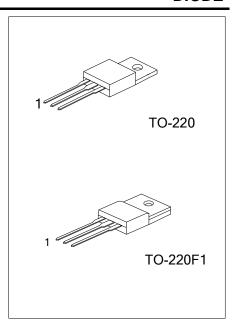
The UTC MGBR30L150C is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers withlow forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed

SYMBOL

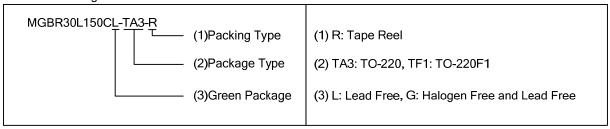




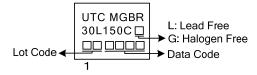
■ ORDERING INFORMATION

=	Ordering Number		Dookogo	Pin Assignment			Dooking
	Lead Free	Halogen Free	Package	1	2	3	Packing
	MGBR30L150CL-TA3-T	MGBR30L150CG-TA3-T	TO-220	Α	K	Α	Tube
	MGBR30L150CL-TF1-T	MGBR30L150CG-TF1-T	TO-220F1	Α	K	Α	Tube

Note: Pin Assignment: A: Anode K: Common Cathode



MARKING



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■ ABSOLUTE MAXIMUM RATINGS(T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
OC Blocking Voltage		V_{RM}	150	V
VorkingPeak Reverse Voltage		V_{RWM}	150	V
Peak Repetitive Reverse Voltage	Peak Repetitive Reverse Voltage		150	V
Average Rectified Output Current	Per Leg ,		15	Α
(T _C =140°C)	Total	lo	30	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load Repetitive Peak Avalanche Power (1µs, 25°C) Operating Junction Temperature Storage Temperature		I _{FSM}	200	А
		P _{ARM}	5000	W
		TJ	-65~+150	°C
		T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETE	PARAMETER		RATINGS	UNIT
Junction to Ambient		θ_{JA}	62.5	°C/W
lunction to Coop	TO-220	θ_{JC}	2	°C/M
Junction to Case	TO-220F1		3.31	°C/W

■ ELECTRICAL CHARACTERISTICS(Per Leg) (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.5mA	150			V
		I _F =15A, T _J =25°C			0.90	V
		I _F =15A, T _J =125°C			0.85	V
akage Current (Note 1)	I DM	V _R =150V, T _J =25°C			100	μA
		V _R =150V, T _J =125°C			10	mΑ

Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} Thermal resistance junction to case mounted on heatsink.

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