

UNISONIC TECHNOLOGIES CO., LTD

MGBR5U45

MOS GATED BARRIER RECTIFIER

DESCRIPTION

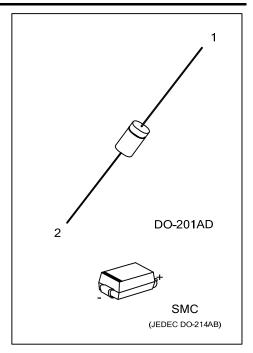
The UTC MGBR5U45 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Ultra low forward voltage drop
- * High switching speed



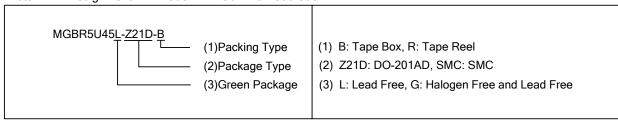




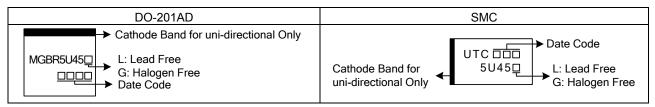
■ ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment		Dooking	
Lead Free	Halogen Free	Package	1	2	Packing	
MGBR5U45L-Z21D-R	MGBR5U45G-Z21D-R	DO-201AD	K	Α	Tape Box	
MGBR5U45L-SMC-R	MGBR5U45G-SMC-R	SMC	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	45	V
Working Peak Reverse Voltage		V_{RWM}	45	V
Repetitive Peak Reverse Voltage		V_{RRM}	45	V
RMS Reverse Voltage		$V_{R(RMS)}$	35	V
Average Rectified Output Current	T _C =140°C	Ιο	5	Α
Non-Repetitive Peak Forward Surge Curren Single Half Sine-Wave Superimposed on Ra		I _{FSM}	200	Α
Operating Junction Temperature		T_J	-65~+150	°C
Storage Temperature		T_{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	75	°C/W
DO-20	1AD	15	°C/W
Junction to Case SMC	θ _{JC}	35	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A =25°C, unless otherwise specified.)

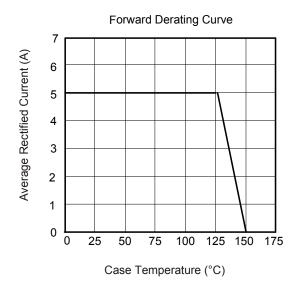
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.5mA	45			V
Instantaneous Forward Voltage	V _{FM}	I _F =5A, T _J =25°C			0.43	V
Instantaneous Forward Voltage		I _F =5A, T _J =125°C			0.40	٧
Lookaga Cumant (Note 4)	I _{RM}	V _R =45V, T _J =25°C			500	μA
Leakage Current (Note 1)		V _R =45V, T _J =125°C			100	mΑ

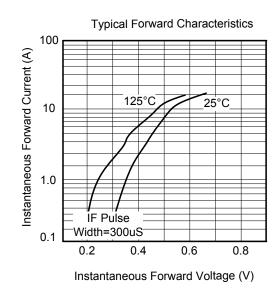
Notes: 1. Short duration pulse test used to minimize self-heating effect.

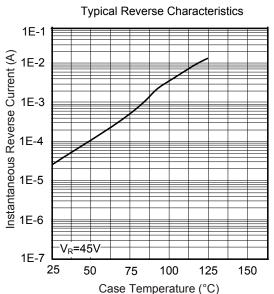
^{2.} Thermal resistance junction to case mounted on heatsink.

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■ TYPICAL CHARACTERISTICS







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