

**PRELIMINARY**

Notice: This is not a final specification.  
Some parametric limits are subject to change.

MITSUBISHI SEMICONDUCTOR <GaAs FET>

# MGFC42V3742A

**3.7~4.2GHz BAND 16W INTERNALLY MATCHED GaAs FET**

## DESCRIPTION

The MGFC42V3742A is an internally impedance-matched GaAs power FET especially designed for use in 3.7 ~ 4.2 GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

## FEATURES

- Class A operation
- Internally matched to 50Ω system
- High output power  
 $P_{1dB} = 18\text{ W (TYP)} @ 3.7 \sim 4.2\text{ GHz}$
- High power gain  
 $G_{LP} = 10\text{ dB (TYP)} @ 3.7 \sim 4.2\text{ GHz}$
- High power added efficiency  
 $\eta_{add} = 34\% \text{ (TYP)} @ 3.7 \sim 4.2\text{ GHz}, P_{1dB}$
- Hermetically sealed metal-ceramic package
- Low distortion [Item: -51]  
 $IM3 = -45\text{ dBc (TYP)} @ P_o = 31\text{ (dBm) S.C.L.}$
- Low thermal resistance  $R_{th(ch-c)} \leq 1.6 \text{ (}^\circ\text{C/W)}$

## APPLICATION

Item-01: 3.7~4.2 GHz band power amplifiers.  
Item-51: Digital radio communication.

## QUALITY GRADE

- IG

## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Symbol	Parameter	Ratings	Unit
V <sub>GD0</sub>	Gate to drain voltage	-15	V
V <sub>GSO</sub>	Gate to source voltage	-15	V
I <sub>D</sub>	Drain current	12	A
I <sub>GR</sub>	Reverse gate current	-40	mA
I <sub>GF</sub>	Forward gate current	+84	mA
P <sub>T</sub>	Total power dissipation *1	93.7	W
T <sub>ch</sub>	Channel temperature	175	°C
T <sub>stg</sub>	Storage temperature	-65 ~ +175	°C

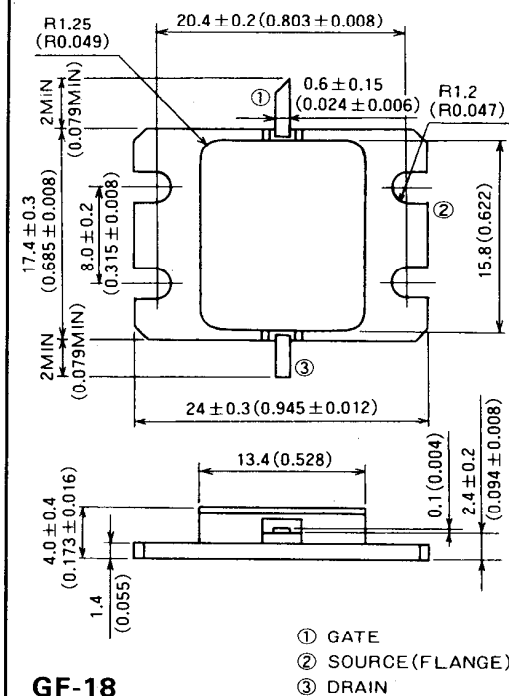
\*1: T<sub>c</sub> = 25°C

## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I <sub>DSS</sub>	Saturated drain current	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0V	—	9	12	A
g <sub>m</sub>	Transconductance	V <sub>DS</sub> = 3V, I <sub>D</sub> = 4.4A	—	4	—	S
V <sub>GS(off)</sub>	Gate to source cut-off voltage	V <sub>DS</sub> = 3V, I <sub>D</sub> = 80mA	-2	-3	-4	V
P <sub>1dB</sub>	Output power at 1dB gain compression	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.5A, f = 3.7~4.2GHz	41.5	42.5	—	dBm
G <sub>LP</sub>	Linear power gain		9	10	—	dB
I <sub>D</sub>	Drain current		—	4.5	—	A
η <sub>add</sub>	Power added efficiency		—	34	—	%
IM <sub>3</sub>	3rd order IM distortion *1		-42	-45	—	dBc
R <sub>th(ch-c)</sub>	Thermal resistance *2		ΔV <sub>f</sub> method	—	—	1.6

\*1: Item-51, 2-tone test P<sub>o</sub> = 31dBm Single Carrier Level f = 4.2GHz Δf = 10MHz, \*2: Channel to case

## OUTLINE DRAWING Unit: millimeters (inches)



GF-18

## RECOMMENDED BIAS CONDITIONS

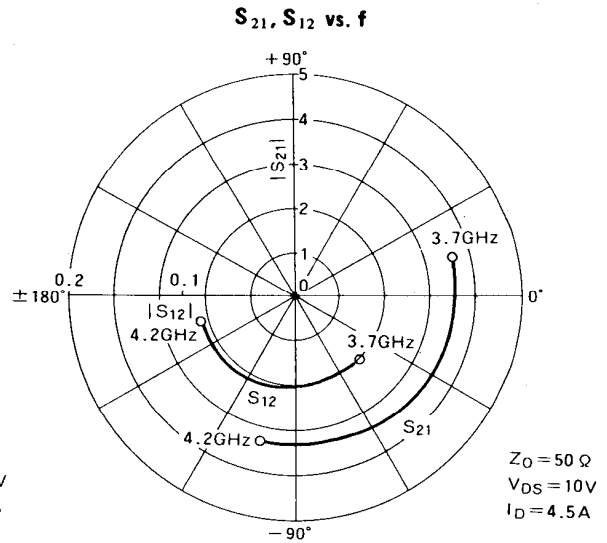
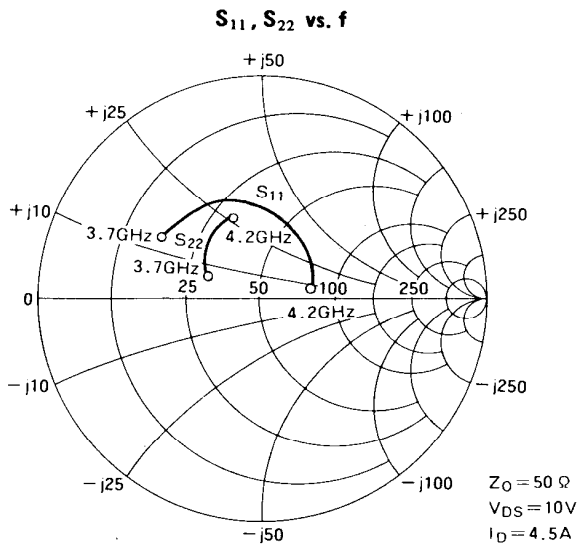
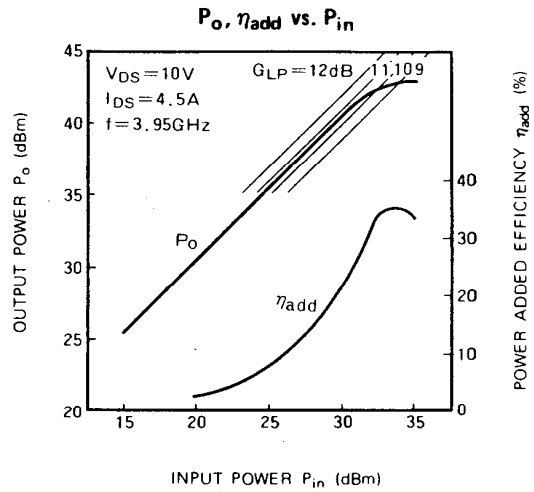
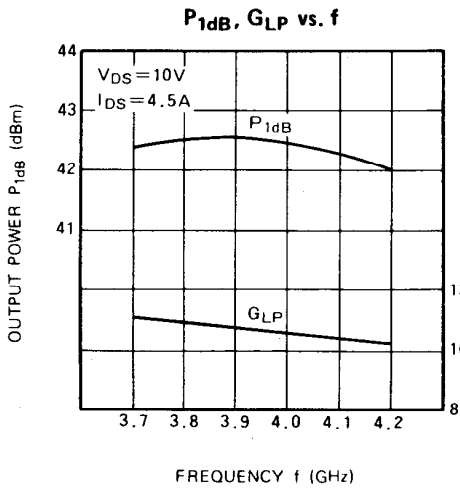
- V<sub>DS</sub> = 10V
- I<sub>D</sub> = 4.5A
- R<sub>g</sub> = 25Ω
- Refer to Bias Procedure

NOV. '97

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**TYPICAL CHARACTERISTICS (Ta=25°C)**



**S PARAMETERS (Ta=25°C, V<sub>DS</sub>=10V, I<sub>DS</sub>=4.5A)**

f (GHz)	S Parameters (TYP.)							
	S <sub>11</sub>		S <sub>21</sub>		S <sub>12</sub>		S <sub>22</sub>	
	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)
3.7	0.51	149	3.65	13	0.080	48	0.26	159
3.8	0.49	129	3.55	-9	0.080	72	0.32	148
3.9	0.47	110	3.50	-34	0.083	97	0.34	138
4.0	0.41	90	3.48	-54	0.086	117	0.37	129
4.1	0.33	54	3.39	-77	0.084	139	0.38	119
4.2	0.24	11	3.31	-103	0.086	163	0.38	108