

PRELIMINARY

Notice: This is not a final specification.
Some parametric limits are subject to change.

MITSUBISHI SEMICONDUCTOR <GaAs FET>

MGFC42V4450A

4.4~5.0GHz BAND 16W INTERNALLY MATCHED GaAs FET

DESCRIPTION

The MGFC42V4450A is an internally impedance-matched GaAs power FET especially designed for use in 4.4~5.0 GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

FEATURES

- Class A operation
- Internally matched to 50Ω system
- High output power
 $P_{1dB} = 18\text{ W (TYP) @ 4.4~5.0 GHz}$
- High power gain
 $G_{LP} = 9\text{ dB (TYP) @ 4.4~5.0 GHz}$
- High power added efficiency
 $\eta_{add} = 33\% \text{ (TYP) @ 4.4~5.0 GHz, } P_{1dB}$
- Hermetically sealed metal-ceramic package
- Low distortion [Item: -51]
 $IM_3 = -45\text{ dBc (TYP) @ } P_o = 31\text{ (dBm) S.C.L.}$
- Low thermal resistance $R_{th(ch-c)} \leq 1.6\text{ (}^\circ\text{C/W)}$

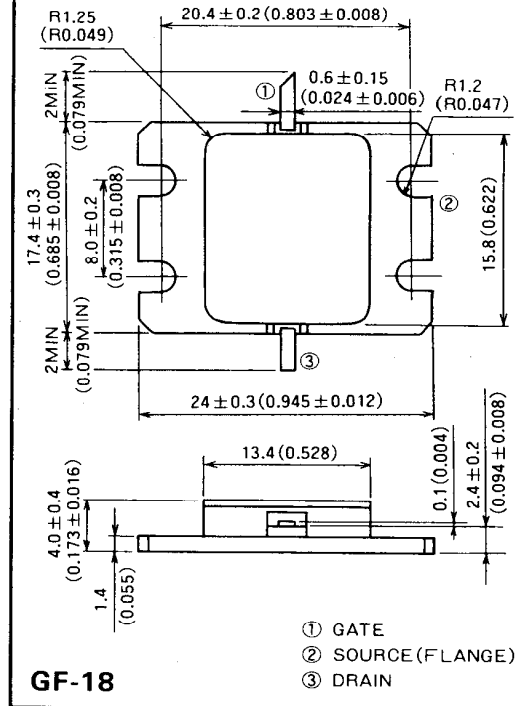
APPLICATION

- Item-01: 4.4~5.0 GHz band power amplifiers.
- Item-51: Digital radio communication

QUALITY GRADE

- IG

OUTLINE DRAWING Unit: millimeters (inches)



ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Symbol	Parameter	Ratings	Unit
V _{GD0}	Gate to drain voltage	-15	V
V _{GSO}	Gate to source voltage	-15	V
I _D	Drain current	12	A
I _{GR}	Reverse gate current	-40	mA
I _{GF}	Forward gate current	+84	mA
P _T	Total power dissipation *1	93.7	W
T _{ch}	Channel temperature	175	°C
T _{stg}	Storage temperature	-65 ~ +175	°C

*1: T_c = 25°C

RECOMMENDED BIAS CONDITIONS

- V_{DS} = 10V
- I_D = 4.5A
- R_g = 25Ω
- Refer to Bias Procedure

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I _{DSS}	Saturated drain current	V _{DS} = 3V, V _{GS} = 0V	—	9	12	A
g _m	Transconductance	V _{DS} = 3V, I _D = 4.4A	—	4	—	S
V _{GS(off)}	Gate to source cut-off voltage	V _{DS} = 3V, I _D = 80mA	-2	-3	-4	V
P _{1dB}	Output power at 1dB gain compression	V _{DS} = 10V, I _D = 4.5A, f = 3.7~4.2GHz	41.5	42.5	—	dBm
G _{LP}	Linear power gain		8	9	—	dB
I _D	Drain current		—	4.5	—	A
η _{add}	Power added efficiency		—	33	—	%
IM ₃	3rd order IM distortion *1		-42	-45	—	dBc
R _{th(ch-c)}	Thermal resistance *2		ΔV _f method	—	—	1.6

*1: Item-51, 2-tone test P_o = 31dBm Single Carrier Level f = 5.0 Δf = 10MHz. *2: Channel to case

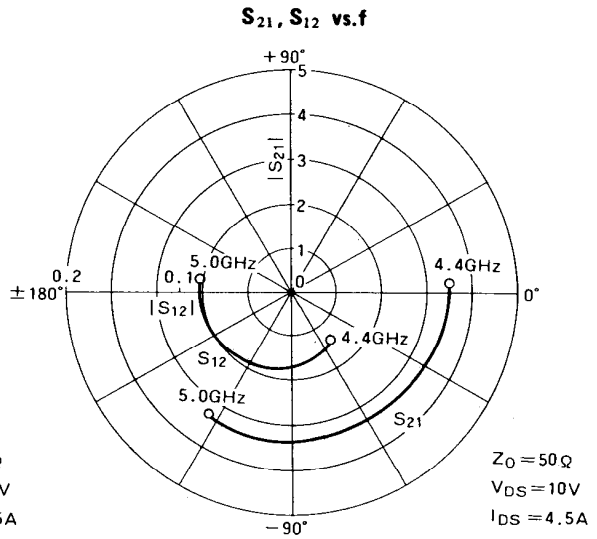
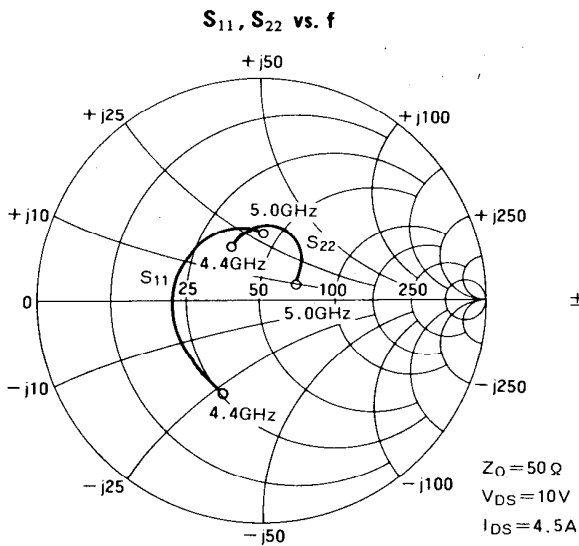
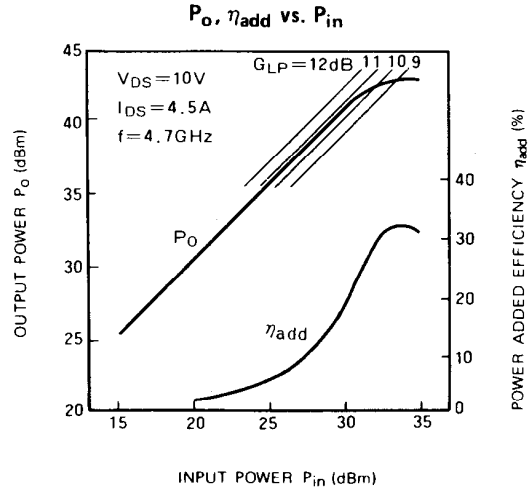
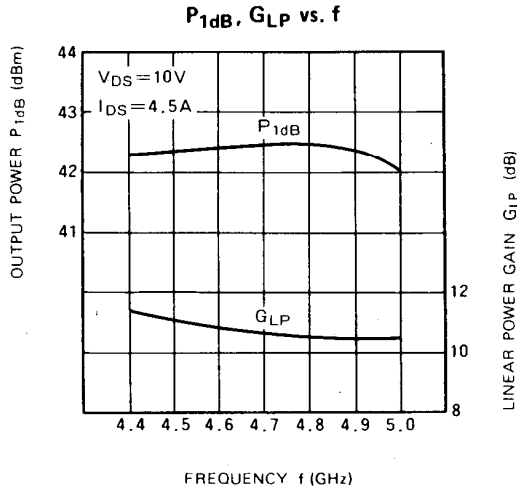
NOV. '97

PRELIMINARY

Notice: This is not a final specification.
Some parametric limits are subject to change.

4.4~5.0GHz BAND 16W INTERNALLY MATCHED GaAs FET

TYPICAL CHARACTERISTICS (Ta=25°C)



S PARAMETERS (Ta=25°C, VDS=10V, IDS=4.5A)

f (GHz)	S Parameters (TYP.)							
	S_{11}		S_{21}		S_{12}		S_{22}	
	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)
4.4	0.46	-113	3.63	3	0.054	-49	0.31	121
4.5	0.41	-156	3.50	-20	0.063	-78	0.33	101
4.6	0.40	-179	3.45	-38	0.068	-93	0.33	90
4.7	0.38	154	3.42	-61	0.072	-123	0.33	79
4.8	0.37	135	3.38	-77	0.075	-140	0.31	71
4.9	0.33	109	3.34	-101	0.078	-167	0.25	51
5.0	0.29	89	3.27	-122	0.079	172	0.17	22