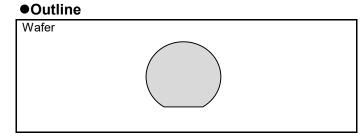
MH2205WZ

1200V 50A Fast Recovery Diode

Datasheet

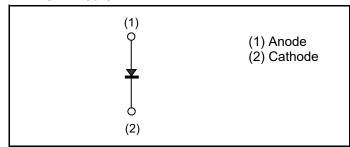
| V_{RM} | 1200V |
|-------------------------------|--------|
| I _{F (Nominal)} | 50A |
| $V_{F (Typ.)}$ | 1.65V |
| Max. Possible Chips per Wafer | 558pcs |



Features

- 1) Light Punch Through Type
- 2) Low Forward Voltage
- 3) Very Fast & Soft Recovery
- 4) Low Recovery Loss

●Inner Circuit



Application

Free Wheeling

for Industrial Use

Absolute Maximum Ratings

| Parameter | Symbol | Value | Unit |
|--|--------------------|-------------|------|
| Repetitive Peak Reverse Voltage, T _j = 25°C | V_{RM} | 1200 | V |
| Forward Current | I _F *1 | *1) | А |
| Pulsed Forward Current | I _{FP} *2 | 150 | А |
| Operating Junction Temperature | T _j | -40 to +175 | °C |

^{*1} Depending on thermal properties of assembly

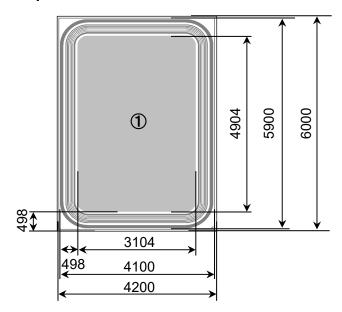
• Electrical Characteristics (at T_i = 25°C unless otherwise specified)

| Parameter | Symbol | Conditions | Values | | | Unit |
|-------------------|----------------|---|--------|------|------|-------|
| | | | Min. | Тур. | Max. | Offic |
| Breakdown Voltage | BV | I _R = 10μA | 1200 | ı | ı | V |
| Reverse Current | I _R | V _R = 1200V | 1 | 1 | 10 | μΑ |
| | | $I_{F} = 50A,$ | | | | |
| Forward Voltage | V_F^{*3} | $I_F = 50A,$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$ | - | 1.65 | 2.1 | V |
| | | T _j = 175°C | - | 1.85 | - | |

^{*3} Design assurance without measurement

^{*2} Pulse width limited by $T_{jmax.}$

●Chip Information

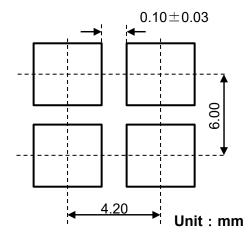


Unit: µm

: Pad Area

1 : Anode Bonding Pad

Backside: Cathode



| Wafer Size | 150mm |
|-------------------------|-----------------------|
| Wafer Thickness | 0.12±0.01mm |
| Chip Size | 4.20mm×6.00mm |
| Cut Line Width | 0.10±0.03mm |
| Top Side Metallization | AlSiCu:5.0µm |
| Back Side Metallization | Ti/Ni:0.4μm/Au:0.05μm |
| Passivation | Polyimide |
| | - |

•Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

| This chip data sheet refers to the device data sheet | - |
|--|---|
|--|---|

Technology qualified in TO-247N package.

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