

MULTI-INNO TECHNOLOGY CO., LTD

LCD MODULE SPECIFICATION

Model: MI12864P-G-2

Revision	
Engineering	
Date	
Our Reference	

MULTI-INNO TECHNOLOGY CO., LTD MI12864P-G-2



REVISION RECORD

Date	Ver.	Ref. Page	Revision No.	Revision Items
2003-3-29	Ver.1.0			



1 General Specifications:

1.1 Display type: STN

1.2 Display color*¹:

Display color: White Background*²: Blue

1.3 Polarizer mode: Transmissive/Negative

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/65 Duty 1/7 Bias

1.6 Logic Voltage: 5.0V

1.7 LCD Operating Voltage: 8.9V

1.8 Backlight: LED(White,5.0V)
1.9 Controller: S1D15605D11D

1.10 Data Transfer: 8 Bit Parallel

1.11 Operating Temperature: -20----+70°C Storage Temperature: -30----+80°C

1.12 Outline Dimensions: Refer to outline drawing on next page

1.13 Dot Matrix: 128 X 64 Dots 1.14 Dot Size: 0.48X0.48(mm)

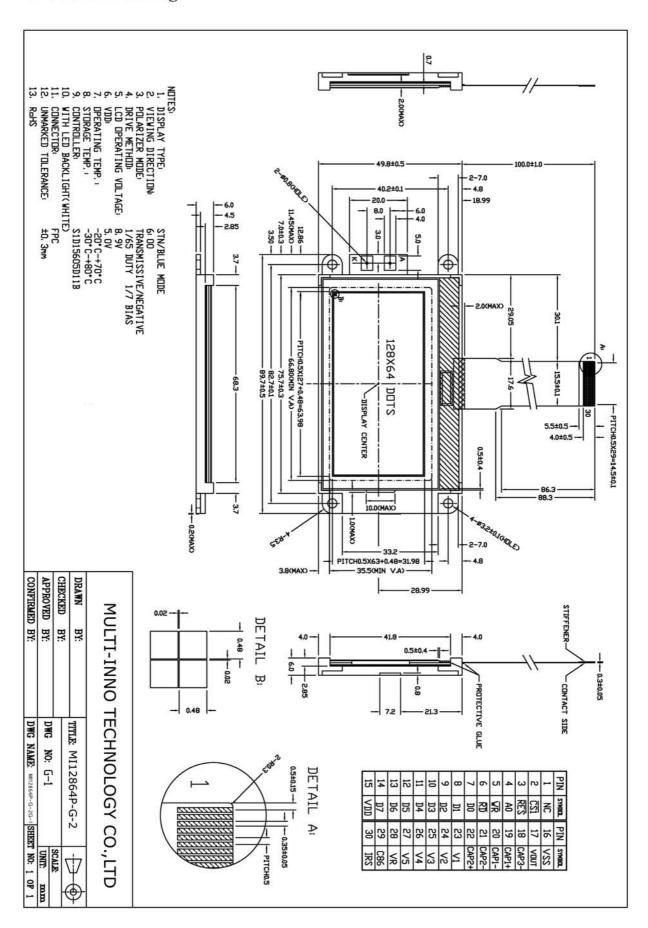
1.15 Dot Pitch: 0.5X0.5 (mm)
1.16 Weight: 45g(approx.)

^{*1} Color tone is slightly changed by temperature and driving voltage.

^{*2} Color tone will be changed by backlight.



2 Outline Drawing



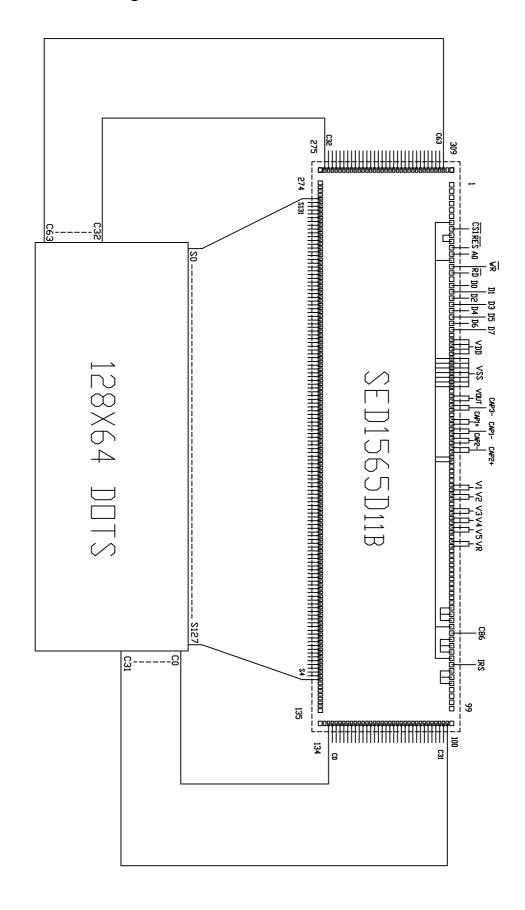


3 LCD Module Part Numbering System



4 Electronic Character

4.1 Circuit Block Diagram

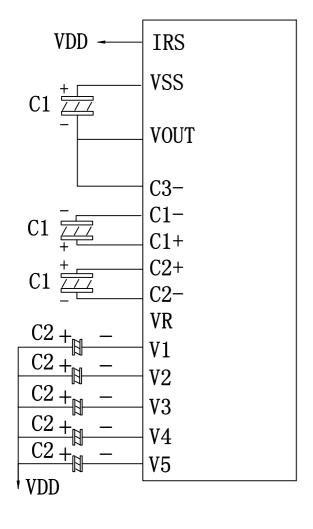


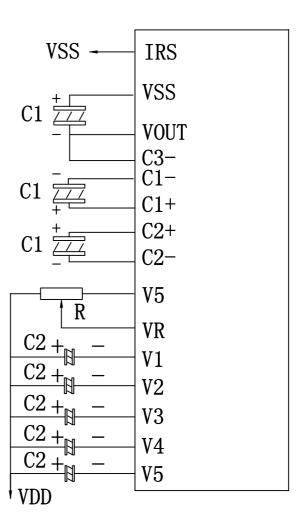


4.2 Power Supply Circuit

When the voltage regulator internal resistor is used

When the voltage regulator internal resistor is not used





VALUE OF EXTERNAL CAPACITANCE

ITEM	VALUE	UNIT
C1	1.0 to 4.7	E
C2	0.01 to 1.0	ur





5 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	V _{DD} -V _{SS}	-0.3	6.0	V	
LCD Driving Voltage	VLCD	-0.3	25.0	V	
Operating Temperature Range	Тор	-20	+70	°C	No
Storage Temperature Range	Tst	-30	+80		Condensation



6 Electrical Specifications and Instruction Code

6.1 Electrical characteristics

Iter	n	Symbol	Min.	Тур.	Max.	Unit
Supply V (Log	•	V _{DD} -V _{SS}	4.5	5.0	5.5	V
Supply V (LCD I	•	VLCD	-	8.9	25.0	V
Input	High	V_{IH} $(V_{\text{DD}}=5.0)$	$0.8 \mathrm{V}_\mathrm{DD}$	-	V _{DD} +0.3	V
Signal Voltage	Low	V_{IL} $(V_{\text{DD}}=5.0)$	0	-	0.2 V _{DD}	V
Supply (Log		I_{DD} $(V_{DD}-V_{SS}=3.0V)$	-	-	300	uA
Supply (I_{DD} $(V_{DD}-V_{SS}=3.0V)$	-	-	500	uA
Supply current (LED)		$I_{\scriptscriptstyle m LED}$	-	-	90	mA
Supply V (LE	•	VLCD	-	5.0	-	V





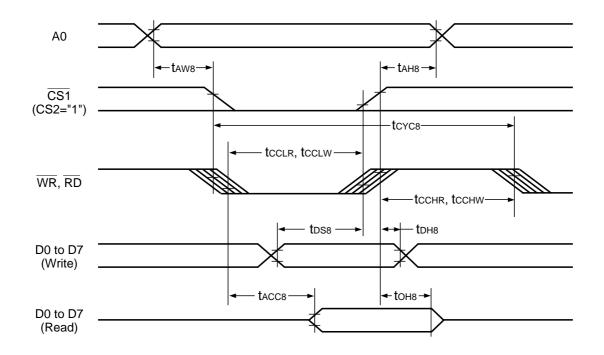
6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	NC	-	No connection
2	/CS1	H/L	Chip select input pin (active at low)
3	/RES	H/L	Reset input pin (active at low)
4	A0	H/L	Register select input pin (H: Data, L: control)
5	/WR	H/L	Write enable clock input pin
6	/RD	H/L	Read enable clock input pin
7	DB0	H/L	
8	DB1	H/L	
9	DB2	H/L	
10	DB3	H/L	8-bit bi-directional data bus that is connected
11	DB4	H/L	to the standard 8-bit microprocessor data bus.
12	DB5	H/L	
13	DB6	H/L	
14	DB7	H/L	
15	VDD	5.0V	Power supply
16	VSS	0V	Ground
17	VOUT	-	Voltage converter input / output pin
18	C3-	-	
19	C1+	-	
20	C1-	-	Capacitor connection pin for voltage converter
21	C2+	-	
22	C2-	-	
23	V1	-	
24	V2	-	LCD driver supply voltages
25	V3	-	$V1 = (1/7) \times V5$ $V2 = (2/7) \times V5$
26	V4	-	$V3 = (5/7) \times V5$ $V4 = (6/7) \times V5$
27	V5	-	
28	VR	-	Output voltage regulator terminal
29	C86	H/L	MPU interface switch terminal (H: 6800 Series MPU interface. L: 8080 MPU interface)
30	IRS	H/L	Terminal selects the resistors for the V5 voltage level adjustment



6.3 Interface Timing Chart

(1) System Bus Read/Write Characteristics 1 (For the 8080 Series MPU)



Figure

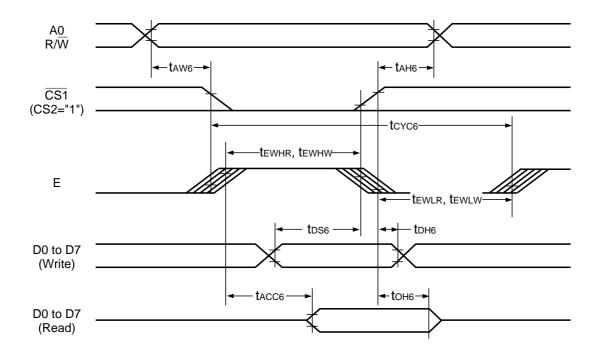
Table

 $(VDD = 4.5 \text{ V to } 5.5 \text{ V}, Ta = -40 \text{ to } 85^{\circ}\text{C})$

Item	Signal	Symbol	Condition	Rat	ing	Units
item	Signai	Syllibol	Condition	Min.	Ullits	
Address hold time Address setup time	A0	tah8 taw8		0 0	_	ns ns
System cycle time	A0	tcyc8		166	_	ns
Control LOW pulse width (WR) Control LOW pulse width (RD) Control HIGH pulse width (WR) Control HIGH pulse width (RD)	WR RD WR RD	tcclw tcclr tcchw tcchr		30 70 30 30	_ _ _ _	ns ns ns ns
Data setup time Address hold time	D0 to D7	tds8 tdh8		30 10	_	ns ns
RD access time Output disable time		tACC8 tOH8	CL = 100 pF	 5	70 50	ns ns



(2) System Bus Read/Write Characteristics 2 (6800 Series MPU)



Figure

Table

 $(VDD = 4.5 \text{ V to } 5.5 \text{ V}, Ta = -40 \text{ to } 85^{\circ}\text{C})$

				\		,	/
Item		Signal	Symbol	Condition	Rat	ing	Units
		Signal	Syllibol	Condition	Min.	Max.	Oilles
Address hold time Address setup time		A0	tah6 taw6		0 0	_	ns ns
System cycle time		A0	tcyc6		166	_	ns
Data setup time Data hold time		D0 to D7	tDS6 tDH6		30 10	_	ns ns
Access time Output disable time			tACC6 tOH6	CL = 100 pF	 10	70 50	ns ns
Enable HIGH pulse time	Read Write	E	tewhr tewhw		70 30	_	ns ns
Enable LOW pulse time	Read Write	E	tewlr tewlw		30 30	_	ns ns





6.4 Instruction code

Table of SED1565 Series Commands

						Com	mand	Code					
	Command	A0	\overline{RD}	\overline{WR}	D7	D6	D5	D4	D3	D2	D1	D0	Function
(1)	Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2)	Display start line set	0	1	0	0	1		Disp	lay sta	ırt add	Iress		Sets the display RAM display start line address
(3)	Page address set	0	1	0	1	0	1	1	F	Page a	addres	6	Sets the display RAM page address
(4)	Column address set upper bit	0	1	0	0	0	0	1	M cc	lost sig olumn	gnifica addres	nt ss	Sets the most significant 4 bits of the display RAM column address.
	Column address set lower bit	0	1	0	0	0	0	0			gnifica addres		Sets the least significant 4 bits o the display RAM column address
(5)	Status read	0	0	1		Sta	atus		0	0	0	0	Reads the status data
(6)	Display data write	1	1	0				Write	data				Writes to the display RAM
(7)	Display data read	1	0	1				Read	data				Reads from the display RAM
(8)	ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9)	Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10)	Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0	Display all points 0: normal display 1: all points ON
(11)	LCD bias set	0	1	0	1	0	1	0	0	0	1	0	Sets the LCD drive voltage bias ratio SED1565*** 0: 1/9, 1: 1/7 SED1566*** /SED1568*** /SED1569*** 0: 1/8, 1: 1/6 SED1567*** 0: 1/6, 1: 1/5
(12)	Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13)	End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14)	Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15)	Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 0: normal direction, 1: reverse direction
(16)	Power control set	0	1	0	0	0	1	0	1	0	peratir mode	ng	Select internal power supply operating mode
(17)	V5 voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Res	sistor r	atio	Select internal resistor ratio (Rb/Ra) mode
(18)	Electronic volume	0	1	0	1	0	0	0	0	0	0	1	
	mode set Electronic volume register set	0	1	0	*	*		Electi	onic v	olume	value		Set the V ₅ output voltage electronic volume register
(19)	Static indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0	0: OFF, 1: ON
	Static indicator register set	0	1	0	*	*	*	*	*	*	Мо	de	Set the flashing mode
(20)	Power saver												Display OFF and display all points ON compound command
(21)	NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(22)	Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

12/23

(Note) *: disabled data

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7 Optical Characteristics

7.1 Optical Characteristics Ta=25 ℃

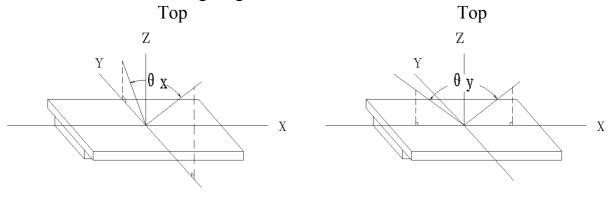
Item		Symbol	Condition		Min.	Тур.	Max.	Unit
Viewing	A a.l.a	$\theta_{\!\scriptscriptstyle \mathbf{X}}$	C > 2	θ _y =0°	-30)	20	Dag
Viewing A	Angle	θу	Cr≥2	θ _x =0°	-30)	30	Deg
Contrast 1	Ratio	Cr		$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		-	-	
Response	Turn on	Ton	θ_{X} =	=0°	-	-	300	mg
Time	Turn off	Toff	$\theta_{y} =$	=0°	-	-	300	ms

Bottom



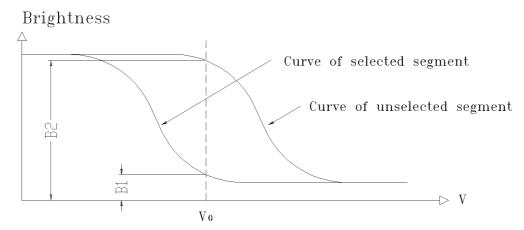
7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle



7.2.2 Definition of Contrast Ratio

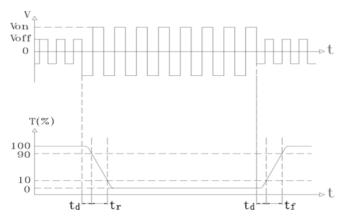
Bottom



Contrast Ratio =
$$B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

1) Ambient Temperature: 25°C; 2) Frame frequency: 84Hz 7.2.3 Definition of Response time



Turn on time: $t_{on} = t_d + t_r$

Turn off time: $t_{off} = t_d + t_f$

Measuring Condition:

1) Operating Voltage: 8.9V 2) Frame frequency: 84Hz

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8 Reliability

8.1 Content of Reliability Test

Ta=25°C

No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	60°C 96H Restore 4H at 25°C
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-20°C 96H Restore 4H at 25°C
3	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	40°C 90%RH 96H Restore 4H at 25°C
4	Temperature Cycle	Endurance test applying the low and high temperature cycle $-20^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C} \longleftrightarrow 60^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C}$ 30min 5min 30min 5min $\leftarrow 1 \text{ cycle}$	-20°C/60°C 10 cycles Restore 4H at 25°C
5	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 50m/s ² , 40min
6	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 100m/s ² , 11ms
7	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40kPa 16H Restore 2H





8.2 Failure Judgment Criterion

Criterion		-	Γest	Iten	n No	٠.		Failure Judgement Criterion
Item	1	2	3	4	5	6	7	randre Judgement Citterion
Basic Specification	1	V	1	1	1	1	√	Out of the basic Specification
Electrical specification	1	1	√					Out of the electrical specification
Mechanical Specification					1	1		Out of the mechanical specification
Optical Characteristic	1	1	V	1			√	Out of the optical specification
Note	For test item refer to 8.1							
Remark	Basic specification = Optical specification + Mechanical specification							





9 QUALITY LEVEL

Examination	At T _a =25°C (unless otherwise stated)		Inspection				
or Test			Min.	Max.	Unit	IL	AQL
External Visual Inspection	Under illumi-nation eyesight condition dis-tance between and LCD is 25cm	en eyes	See Appendix A			II	Major 0.65 Minor 1.5
Display Defects	Under illumi-nation eyesight co display on inspec	normal and ndition, tion.	See Appendix B		II	Major 0.65 Minor 1.5	

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828



10 Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.





- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.





Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria				
Leakage		Not permitted				
Rainbow		According to the limit specimen				
	Wrong polarizer attachment	Not permitted				
	Bubble between	Not counted		Max. 3 defects allowed		
Polarizer	polarizer and glass	φ<0.3mm	0.3mm≤¢≤0.5ı		mm	
	Scratches of polarizer	According to the limit specimen				
Black spot	a a	Not counted	Max. 3 spots allowed		Max. 3	
(in viewing area)		X<0.2mm	0.2mm≤X≤0.5mm			
		X=(a+b)/2			spots (lines)	
Black line (in viewing	b b	Not counted	Max. 3 lines allowed		allowed	
area)		a<0.02mm	0.02mm≤a≤0.05mm b≤2.0mm			
Progressive cracks		Not permitted	[





Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents	Criteria					
	Cracks on pads	a	b)	c	Max. 2	
		≤3mm	≪W/5		≪T/2	cracks	
		≤2mm	≪V	V/5	T/2 <c<t< td=""><td>allowed</td><td></td></c<t<>	allowed	
	Cracks on contact side	a		b			
		≤3m	m	≪T/2			
		≤2m	m	T/2 <b<t< td=""></b<t<>			
Glass Cracks		C shall be not reach the seal area				Max. 2 cracks	Max. 5 cracks allowed
	Cracks on non-contact side	a		b		allowed	
	O SW -	≤3m	m		≤T/2		
		≤2mm		T/2 <b<t< td=""><td></td><td></td></b<t<>			
		C≤0.5mm					
		d≤SW/3					
	Corner cracks	e<2.0mm ²				Max. 3	
	f-#	f<2.0mm	n^2			cracks allowed	
	_						





Appendix B

Inspection items and criteria for display defects

Items		Contents	Criteria					
Open segment or open common			Not permitted					
Short			Not permitted					
Wrong view	Wrong viewing angle			Not permitted				
Contrast radio uneven			According to the limit specimen					
Crosstalk			According to the limit specimen					
		Not counted	Max.3 dots allowed					
		X<0.1mm	0.1mm≤X≤0.2mm					
Pin holes and cracks in segment (DOT)		X=(a+b)/2	Max.3 dots					
		Not counted	Max.2 dots allowed	allowed				
		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm					
Black spot (in viewing area)		Not counted	Max.3 spots allowed					
		X<0.1mm	0.1mm≤X≤0.2mm					
		X=(a+b)/2	5					
Black line	b b	Not counted	Max.3 lines allowed	(lines) allowed				
(in viewing area)		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm					



Appendix B Inspection items and criteria for display defects (continued)

Items	Content	Criteria				
	1	Not counted	Max. 2 defects allowed			
		x<0.1mm	0.1mm≤x≤0.2mm			
		x=(a+b)/2				
				Max.3		
		Not counted	Max. 1 defects allowed	defects allowed		
Transformation of segment		a<0.1mm	0.1mm≤a≤0.2mm D>0			
		Max.2 defects 0.8W≤a≤1.2 a=measured va W=nominal va				