# Panasonic \_\_\_\_\_

### MIP2F40MTSCF

種別。	種別/Type シリコン MOS形集積回路/Silicon MOSFET type Integrated Circuit									
用途/Application スイッチング電源制行		「電源制御用/For S	御用/For Switching Power Supply Control							
構造	構造/Structure CMOS形/CMOS type									
等価	等価回路/Equivalent Circuit 添付図/See Figure. 6									
外形。	∕Out Line	DIP7-A	1-B		マーク記号/マーキング/Marl	king	MIP2F4			
A. 絶	対最大定格/A	BSOLUTE MAX	KIMUM RATINGS (T	a=25℃	±3°C)					
NO.					定格/Ratings	単位/ Unit		備考/N	ote	
1	ドレイン電圧 DRAIN Voltag	VD		−0.3 ~ 700	V		下記パルス幅以内での			
2	VCC電圧 VCC Voltage	vcc		<b>−</b> 0. 3 <b>~</b> 45	V	保証とする (It is guaranteed within				
3	VDD電圧 VDD Voltage		VDD		-0.3 <b>~</b> 8	V	the pulse as below.)			
4	フィードバック FEEDBACK V	oltage	VFB		-0.3 <b>~</b> 8	V		オン時ブランキング幅		
5	フィードバック FEEDBACK C	<del>-</del>	IFB		500	uA	+過電流保護遅れ時間 Leading Edge Blanking Pulse + Current Limit Delay			
6	CL端子電圧 CL Voltage		VCL		-0.3 <b>~</b> 8	V				
7	CL端子電流 CL Current	_	ICL		150	uA	ton(E	3LK)+td((	)+td(OCL)	
8	出力ピーク電流 Output Peak Current		IDP		1. 5 (※1)	А				
9	チャネル部温度 Channel Temperature		Tch		150	°C				
10	保存温度 Storage Temp	Tstg		−55 ~ +150	°C	_				
B. 電	【気的特性/ELE	CTRICAL CHA	RACTERISTICS		測定条件/Measure cond	dition (TC=	:25°C±2	°C)		
No.	項目/Item		記号/ Symbol		件/Measure Condition 図-1 参照/See Figure 1)	Тур.	Limit Min Max		Unit	
【コント	【コントロール機能/CONTROL FUNCTIONS】									
1	出力周波数 Output Frequer	201	fosc	VCC=15 V,	VD=5 V, IFB=20 uA, ICL=50 uA	100	90	110	kHz	
			fosc(L)	VCC=15 V,	VD=5 V, IFB:OPEN, ICL <icl1< td=""><td>12</td><td>9</td><td>15</td><td>kHz</td></icl1<>	12	9	15	kHz	
2	最大デューティサイクル Maximum Duty Cycle		MAXDC	VCC=15 V,	VD=5 V, IFB=20 uA, ICL=50 uA	47.5	45	50	%	
3	VDD基準電圧 VDD Voltage		VDD	VCC=15 V,	VD=5 V, IFB=20 uA, ICL=50 uA	5.9	5.4	6.4	V	
4	VDD停止電圧 UV Lockout Threshold Voltage		yUV	VD=5 V, IF	B=20 uA, ICL=50 uA	5.1	4.6	5.6	V	

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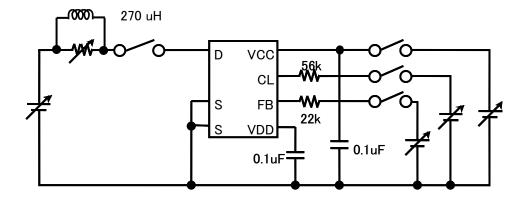
	-T-D -0	記号/	測定条件/Measure Condition	Тур.	Limit				
No.	項目/Item	Symbol	(測定図−1 参照/See Figure 1)		Min	Max	Unit		
【コントロール機能/CONTROL FUNCTIONS】									
	VCC起動電圧								
5	VCC Start Voltage	VCC(ON)	VD=5 V, IFB=20 uA, ICL=50 uA	7.5	6.5	8.5	V		
	VCC充電停止電圧								
6	The strange step timestream testage		VD=40 V, FB:OPEN, CL:OPEN	12	11	13	V		
	フィードバック電流		ON → OFF						
7	Feedback Threshold Current	IFB1	VCC=15 V, VD=5 V, ICL=50 uA	80	50	110	uA		
	フィードバック電流ヒステリシス								
8	Feedback Hysteresis Current IFBHYS		VCC=15 V, VD=5 V, ICL=50 uA	6			uA		
	重負荷時FB端子電流		ICC0 → ICC						
9	FB Pin Current at Heavy Load	IFB0	VCC=15 V, VD=5 V, ICL=50 uA	11	7	15	uA		
	FB端子電圧								
10	FB Pin Voltage	VFB	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA	1.0	0.7	1.3	V		
	回路消費電流								
11	Supply Current	ICC	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA	0.45	0.25	0.55	mA		
	軽負荷時回路消費電流		VCC=15 V, VD=5 V						
12	Supply Current at Light Load	ICC(OFF)	IFB=IFB1+5 uA, ICL=50 uA	0.27	0.2	0.35	mA		
	重負荷時回路消費電流								
13	Supply Current at Heavy Load	ICC0	VCC=15 V, VD=5 V, IFB=OPEN, ICL=50 uA	0.63	0.48	0.78	mA		
	VDD充電電流	Ich1	VDD-0 V VD-40 V ED-ODEN CLODEN	7	5	9	mA		
14	VDD Charging Current	10111	VDD=0 V, VD=40 V, FB:OPEN, CL:OPEN				11171		
14		Ich2	VDD=4 V, VD=40 V, FB:OPEN, CL:OPEN	2.3	1	3.6	mA		
	CL端子電圧								
15	CL Pin Voltage	VCL	VCC=15 V, VD=5 V, FB:OPEN, ICL=15 uA	2.3	2.0	2.6	V		
	fosc 低下時CL端子電流		fosc → fosc(L)						
16	Dropped fosc CL Pin Current	ICL1	VCC=15 V, VD=5 V, FB:OPEN	12	9	15	uA		
	fosc 低下時CL端子電流ヒステリシス	ICLHYS	≫Figure 3						
17	17 CL Pin Hysteresis Current		VCC=15 V, VD=5 V, FB:OPEN	1.0			uA		
【保護	幾能/CIRCUIT PROTECTIONS:*は設言	+保証項目/	Design Guarantee Item]						
İ	過電流保護検出		∭Figure 2/Figure 4						
18	Self Protection Current Limit	ILIMIT	VCC=15 V, FB:OPEN, ICL=50 uA,DUTY=30%	0.70	0.63	0.77	Α		
	ILIMIT 補正係数		≫Figure 2/Figure 4						
19	ILIMIT modified coefficient	R_slope	VCC=15 V, FB:OPEN, ICL=50 uA	40			mA/us		
	最小ILIMIT		Ton=3 usec						
20	Minimum ILIMIT	ILIMITmin	VCC=15 V, FB:OPEN, ICL=0 uA	170	90	250	mA		
*	軽負荷時ドレイン電流		Ton=3 usec						
21	Drain Current at Light Load	ID(OFF)	VCC=15 V, IFB=IFB1+IFBHYS, ICL=50 uA	140	50	230	mA		
*	オン時ブランキング幅								
22	Leading Edge Blanking Delay	ton(BLK)	VCC=15 V, FB:OPEN, ICL=50 uA	240	170	310	ns		
*	過電流保護遅れ時間	.,							
23	Current Limit Delay	td(OCL)		150	100	200	ns		
	過電圧保護検出								
24	Over Voltage Protection	VCC(OV)	VDD=5 V, FB:OPEN, ICL=50 uA	24	21	27	V		
*	過熱保護温度	ТОТР			4.6.5		0 -		
25	25 Thermal Shutdown Temperature			140	130	150	°C		

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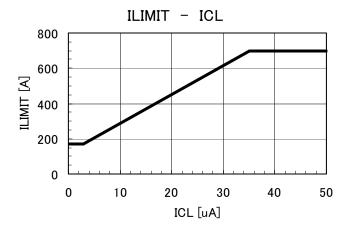
### MIP2F40MTSCF

N		記号/	測定条件/Measure Condition	_	Limit					
No.	項目/Item	Symbol	(測定図−1 参照/See Figure 1)	Тур.	Min	Max	Unit			
【出力	【出力/OUTPUT】									
	ラッチリセット電圧									
26	Power-up Reset Threshold Voltage	VDDreset		2.6	1.8	3.5	V			
	オン抵抗									
27	ON-State Resistance	RDS(ON)	ID=100 mA	7		9.5	Ω			
	オフ時ドレイン端子リーク電流									
28	OFF-State Current	IDSS	VCC=27 V, VD=650 V, FB:OPEN, CL:OPEN	10		20	uA			
	ドレイン耐圧									
29	Breakdown Voltage	VDSS	VCC=27 V, ID=100 uA, FB:OPEN, CL:OPEN		700		V			
	立ち上がり時間		※Figure 5							
30	Rise Time	tr	VCC=15 V, VD=5 V, FB:OPEN, ICL=50 uA	100			ns			
	立ち下がり時間		※Figure 5							
31	Fall Time	tf	VCC=15 V, VD=5 V, FB:OPEN, ICL=50 uA	50			ns			
【電源	電圧/SUPPLY】									
	最小ドレイン電圧									
32	Drain Supply Voltage	VD(MIN)	VCC: OPEN, FB:OPEN, CL:OPEN		50		V			

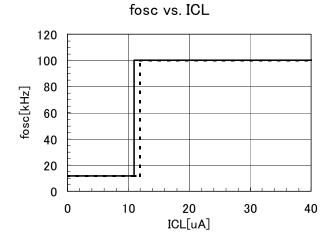
【Fig. 1:測定回路図/Measure Circuit】



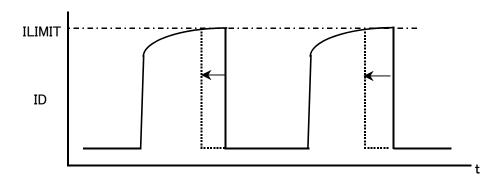
[Fig. 2:ILIMIT vs. ICL Typical Characteristic]



[Fig. 3:fosc vs. ICL Typical Characteristic]

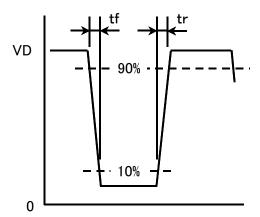


[Fig. 4: ILIMIT Measurement]

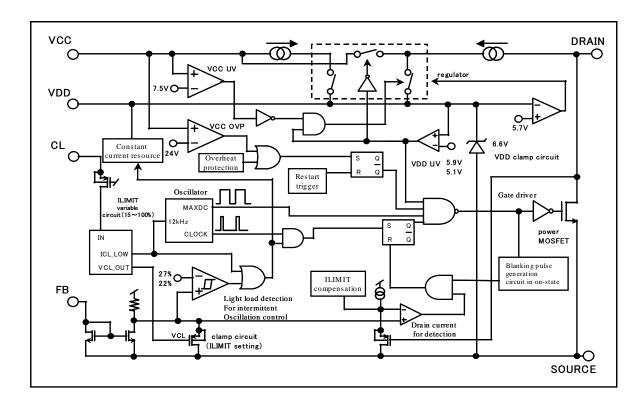


 $R\_slope \; ; \; \{ (ILIMIT \; at \; Duty=30\%) \; - \quad (ILIMIT \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty$ 

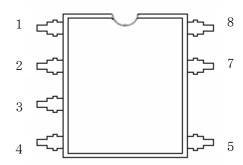
[Fig. 5 : tr, tf Measurement]



[Fig. 6: Block Diagram]



【Fig. 7: 端子配置図/Pin Layout 】



Pin No.	端子名/Terminal Name		
1	VDD		
2	FB		
3	CL		
4	VCC		
5	Drain		
6			
7	Source		
8	Source		

#### 【使用上の注意1/Precautions for Use 1】

VDD 端子-GND間には、O. 1uFのセラミックコンデンサを使用してください。 Connect a 0.1uF ceramic capacitor between VDD pin and GND.

### 【使用上の注意2/Precautions for Use 2】

以下のような条件では破損し、場合によっては破裂、発煙の可能性があります。以下の使用は避けてください。 The IPD has risks for break-down or burst or giving off smoke in following conditions. Avoid the following use.

- DRAIN 端子と VDD 端子を逆にして、電源基板へ挿入する。
   Reverse the DRAIN pin and VDD pin connection to the power supply board.
- (2) DRAIN 端子と VDD 端子をショートする。 DRAIN pin short to VDD pin.
- (3) DRIN端子と FB 端子をショートする。 DRAIN pin short to FB pin.
- (4) DRIN端子とCL端子をショートする。 DRAIN pin short to CL pin.
- (5) DRIN端子と VCC 端子をショートする。 DRAIN pin short to VCC pin.
- (6) VCC 端子と VDD 端子をショートする。 VCC pin short to VDD pin.
- (7) VCC 端子と FB 端子をショートする。 VCC pin short to FB pin.
- (8) VCC 端子と CL 端子をショートする。 VCC pin short to CL pin.

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  Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure
  - mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
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- 1) The sale and/or the export of IPD products to customers located in certain countries is restricted by the Agreement made and executed by and between Power Integrations, Inc. and Panasonic Corporation. For details, refer to the following Attached table "IPD availability by customer."
- 2) IPD products purchased from our company, or its authorized agents, hereinafter referred to as our company, shall be used only for production purposes by those parties who have duly purchased IPD products. Those who have purchased IPD products shall not use such IPD products in unmodified form for re-sale, loan, or sample shipment for evaluation purposes to any other parties.
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- 4) In the event that any actual or threatened breach or violation of any of the above mentioned 2) or 3) has occurred or is about to occur, our company will hold all shipments of IPD products and may request the customer to disclose necessary documentation describing the status of our end-users and/or distribution channels.
  - Note) The products of MIP50\*\*, MIP51\*\*, and MIP7\*\* are excluded from above-mentioned precautions, 1) to 3).

#### Attached table "IPD availability by customer"

	Parts No.		Companies/areas to which products can be sold	Companies/areas to which products cannot be sold	Application
MIP01** MIP2** MIP9A**	MIP02** MIP3** MIP9L**	MIP1** MIP4**	- Japanese companies in Japan - Japanese companies in Asia (50% or more owned)	Companies in European and American countries     Asian companies in Asia     Other local companies	· For power supply · For DC-DC converter
MIP00** MIP55** MIP803/804	MIP52** MIP56** MIP816/826	MIP53** MIP5S** MIP9E**	- Japanese companies in Japan - Japanese companies in Asia (50% or more owned) - Asian companies in Asia	· Companies in European and American countries · Other local companies	· For power supply · For EL driver · For LED lighting driver
MIP50**	MIP51**	MIP7**	· No restrictions in terms of contract	· No restrictions in terms of contract	· For lamp driver/ car electronics accessories

Note) For details, contact our sales division.