

isc Silicon NPN Power Transistor

DESCRIPTION

- · High Switching Speed
- · Wide Area of Safe Operation
- · 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS



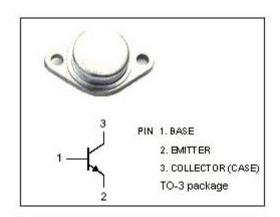
• Designed for use in large screen color deflection circuits.

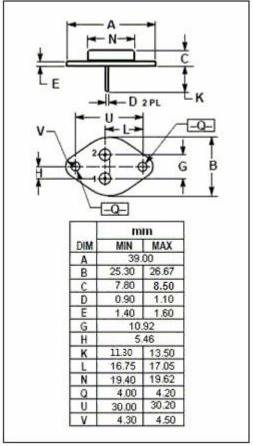
ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector- Base Voltage	1500	V	
V _{CEO(SUS)}	Collector-Emitter Voltage	750	V	
V _{EBO}	Emitter-Base Voltage	5	V	
lE	Emitter Current-Continuous	7	Α	
Pc	Collector Power Dissipation@T _C =25℃	100	W	
TJ	Junction Temperature 1		$^{\circ}\!\mathbb{C}$	
T _{stg}	Storage Temperature	-65~150	$^{\circ}$	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	1.25	°C/W







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MJ12002

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C =10mA ; I _B =0	750			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 1.8A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 1.8A			1.5	V
Ісво	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			1.0	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C =0			1.0	mA
h _{FE}	DC Current Gain	I _C = 0.5A ; V _{CE} = 5V		10.5		
f⊤	Current-Gain—Bandwidth Product	I _C = 0.1A; V _{CE} = 5V; f _{test} =1.0MHz		4		MHz
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} =0.1MHz		50		pF
t _f	Fall Time	I _C = 3A , I _{B1} = 1.2A; L _B = 8 μ H		0.5	1.0	μS

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