

isc Silicon NPN Power Transistor

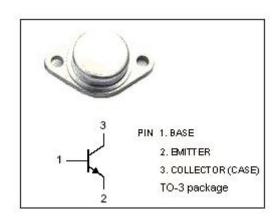
MJ12005

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)} = 750V(Min)
- · High Switching Speed
- · Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

• Designed for use in CRT deflection circuits.

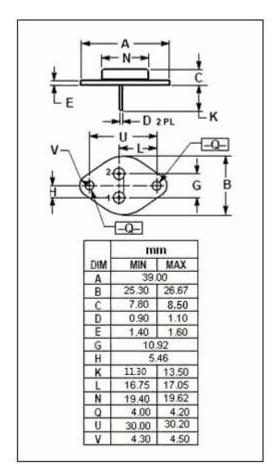


ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector- Base Voltage	1500	V
V _{CEO(SUS)}	Collector-Emitter Voltage	750	V
V_{EBO}	Emitter-Base Voltage	5	V
Ic	Collector Current-Continuous	8	Α
lΒ	Base Current-Continuous	4	А
IE	Emitter Current-Continuous	12	А
Pc	Collector Power Dissipation@T _C =25℃	100	W

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	1.25	°C/W





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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C =10mA ; I _B =0	750			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 1A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 1A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C =0			0.1	mA



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