

isc Silicon NPN Power Transistor

DESCRIPTION

- · High Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)}= 300V(Min.)
- · Collector-Emitter Saturation Voltage-
 - : $V_{CE(sat)} = 0.8 V(Max)@ I_C = 1A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

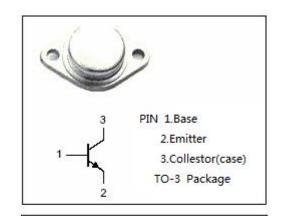
• Designed for medium to high voltage inverters, converters, regulators and switching circuits.

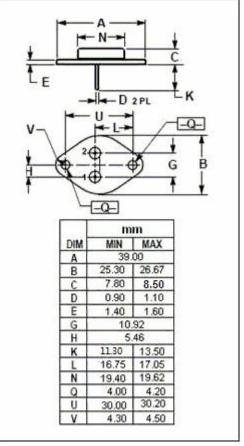
ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

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SYMBOL	PARAMETER	VALUE	UNIT			
V _{CBO}	Collector-Base Voltage	300	V			
V _{CEO}	Collector-Emitter Voltage	300	V			
V _{EBO}	Emitter-Base Voltage	5	V			
Ic	Collector Current-Continuous	5	А			
l _Β	Base Current	10	А			
Pc	Collector Power Dissipation@Tc=25℃	100	W			
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-65~+200	$^{\circ}$ C			

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	0.75	°C/W







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MJ411

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C =100mA ; I _B =0	300		V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1A; I _B = 0.1A		0.8	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 1A; I _B = 0.1A		1.2	V
Iceo	Collector Cutoff Current	V _{CE} = 300V; I _B =0		0.25	mA
І _{ЕВО}	Emitter Cutoff Current	V _{EB} = 5V; I _C =0		5.0	mA
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	30	90	
h _{FE-2}	DC Current Gain	I _C = 2.5A; V _{CE} = 5V	10		



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