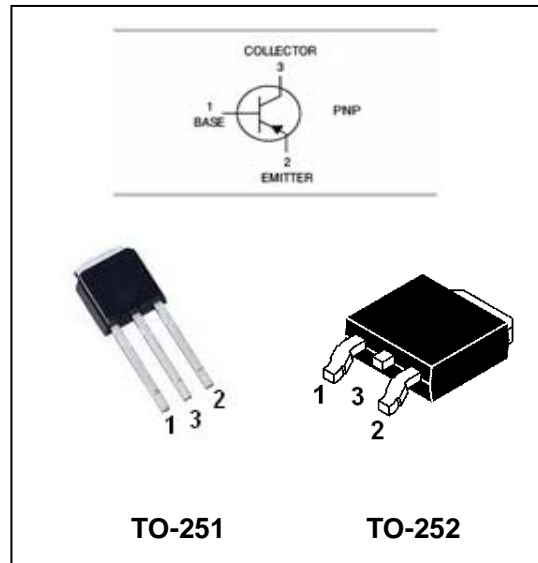


Epitaxial Planar PNP Transistor

MJD127

FEATURES

- High DC Current Gain.
- Built-in a Damper Diode at E-C.
- Lead Formed for Surface Mount Applications.
- Straight Lead.
- Complement to MJD122.



MAXIMUM RATING operating temperature range applies unless otherwise specified

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Volage	-100	V
V_{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current	-8	A
I_{CP}	Collector Power Dissipation	-16	A
I_B	Base Current	-120	mA
P_C	Collector Power Dissipation	1.5	W
T_j, T_{stg}	Junction and Storage temperature range	-65 to +150	°C

Epitaxial Planar PNP Transistor

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ELECTRICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Collector-emitter sustaining voltage	$V_{CEO(SUS)}$	$I_C=-30\text{mA}, I_B=0$	-100			V
Collector cut-off current	I_{CEO}	$V_{CE}=-50\text{V}, I_B=0$			-10	μA
Collector cut-off current	I_{CBO}	$V_{CB}=-100\text{V}, I_E=0$			-10	μA
Emitter cut-off current	I_{EBO}	$V_{EB}=-5\text{V}, I_C=0$			-2	mA
DC current gain	h_{FE}	$V_{CE}=-4\text{V}, I_C=-4\text{A}$ $V_{CE}=-4\text{V}, I_C=-8\text{A}$	1000 100		12K	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=-4\text{A}, I_B=-16\text{mA}$ $I_C=-8\text{A}, I_B=-80\text{mA}$			-2 -4	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C=-8\text{A}, I_B=-80\text{mA}$			-4.5	V
Base-emitter on voltage	$V_{BE(on)}$	$V_{CE}=-4\text{V}, I_C=-4\text{A}$			-2.8	V
Output capacity	C_{ob}	$V_{CB}=-10\text{V}, I_E=0, f=0.1\text{MHz}$			300	pF

TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

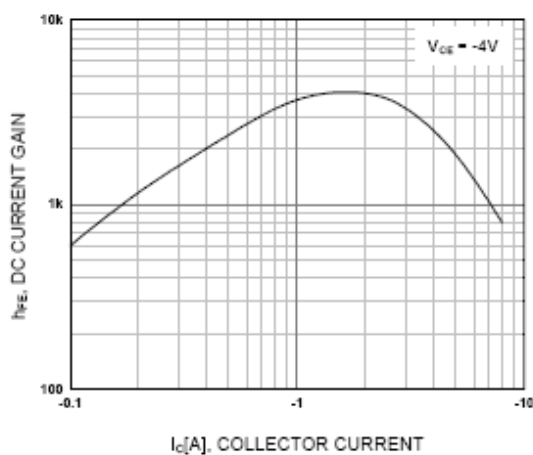


Figure 1. DC current Gain

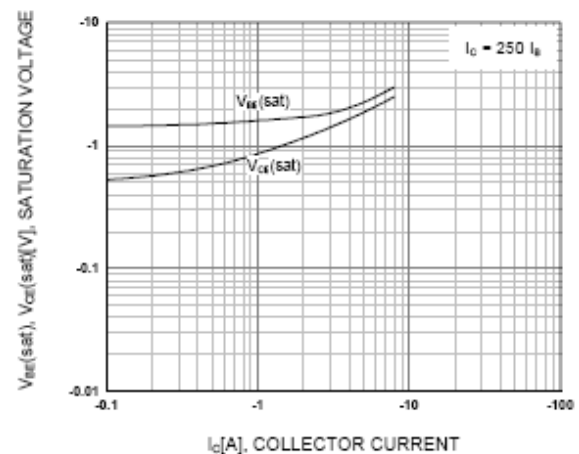


Figure 2. Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

Epitaxial Planar PNP Transistor

MJD127

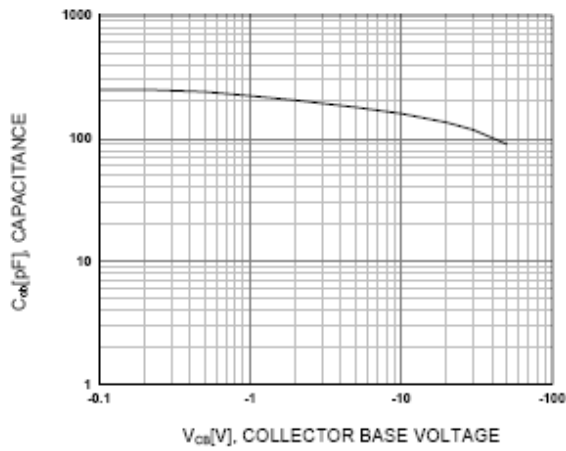


Figure 3. Collector Output Capacitance

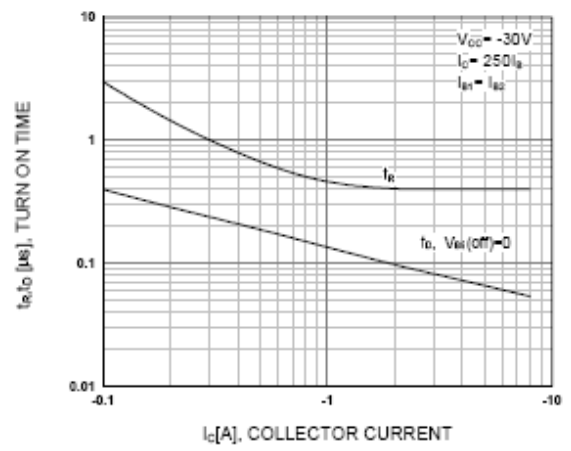


Figure 4. Turn On Time

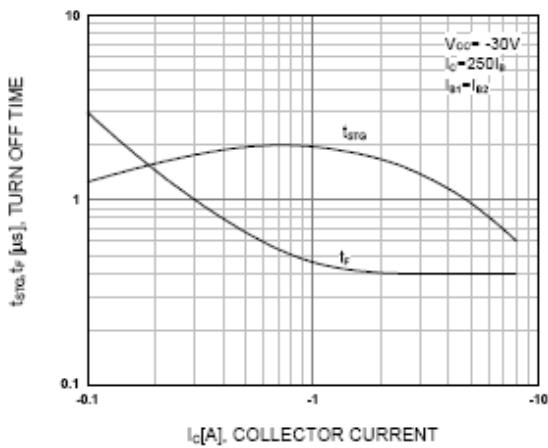


Figure 5. Turn Off Time

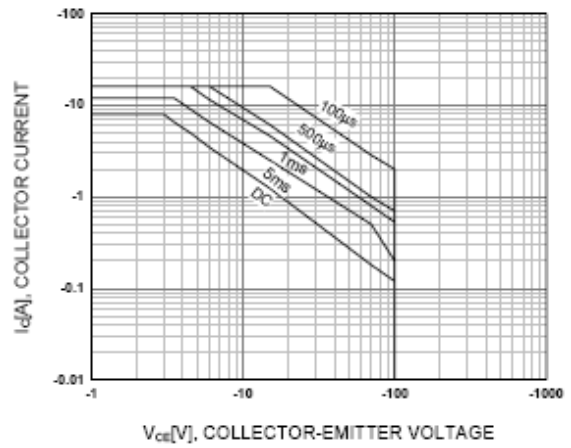


Figure 6. Safe Operating Area

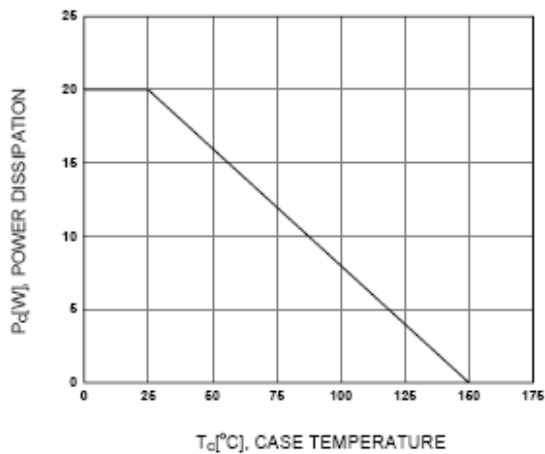


Figure 7. Power Derating

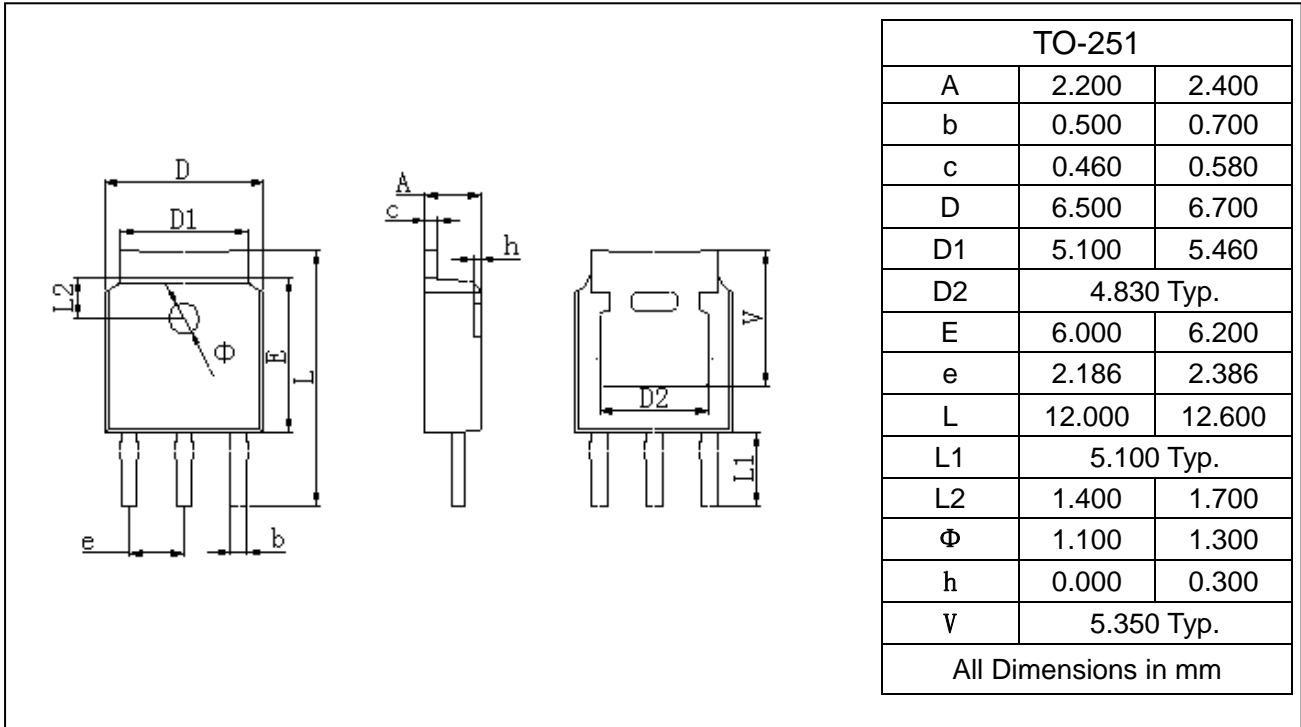
Epitaxial Planar PNP Transistor

MJD127

PACKAGE OUTLINE

Plastic surface mounted package

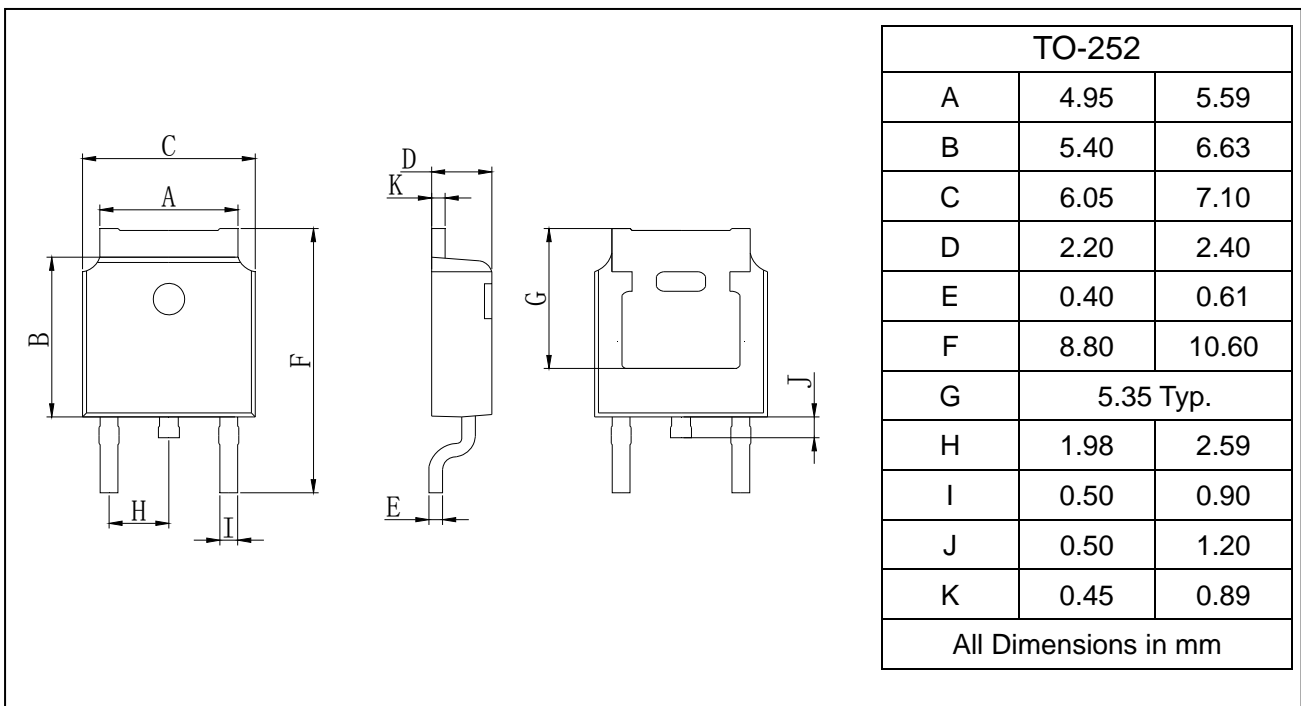
TO-251



PACKAGE OUTLINE

Plastic surface mounted package

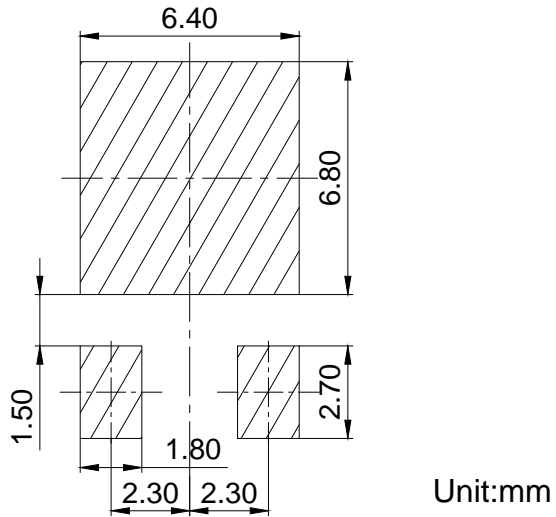
TO-252



Epitaxial Planar PNP Transistor

MJD127

SOLDERING FOOTPRINT



PACKAGE INFORMATION

Device	Package	Shipping
MJD127	TO-251/252	80PCS/Tube
	TO-252	2500PCS/Tape&Reel