

isc Silicon PNP Power Transistors

MJD42C

DESCRIPTION

- DC Current Gain -hFE = 30(Min)@ IC= -0.3A
- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)} = -100V(Min)
- · Complement to Type MJD41C
- DPAK for Surface Mount Applications
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

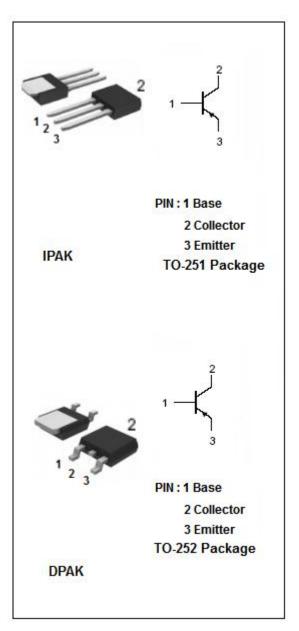
• Designed for use in general purpose amplifer and low speed switching applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V_{CBO}	Collector-Base Voltage	-100	V	
V _{CEO}	Collector-Emitter Voltage	-100	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
lc	Collector Current-Continuous		Α	
Ісм	Collector Current-Peak	-10	А	
Ι _Β	Base Current	-2	Α	
Pc	Collector Power Dissipation T_C =25 $^{\circ}$ C	20	W	
	Collector Power Dissipation T _a =25°C	1.75		
T _j	Junction Temperature	150		
T _{stg}	Storage Temperature Range	-65~150	$^{\circ}$	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case		°C/W
R _{th j-a}	R _{th j-a} Thermal Resistance, Junction to Ambient		°C/W





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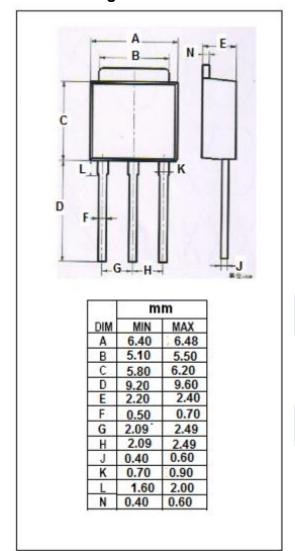
ELECTRICAL CHARACTERISTICS

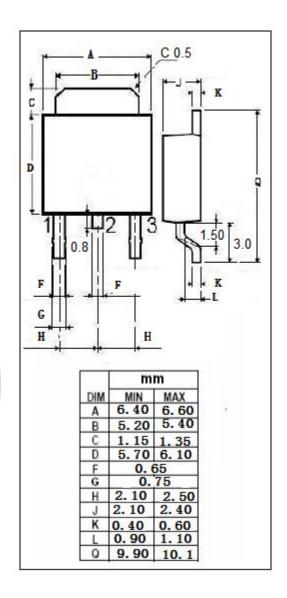
T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -30mA; I _B = 0	-100		٧
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -6A; I _B = -0.6A		-1.5	٧
V _{BE(on)}	Base-Emitter On Voltage	I _C = -6A; V _{CE} = -4V		-2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0		-10	uA
I _{CEO}	Collector Cutoff Current	V _{CE} = -60V; I _B = 0		-50	uA
І _{ЕВО}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0		-0.5	mA
h _{FE-1}	DC Current Gain	I _C = -0.3A; V _{CE} = -4V	30		
h _{FE-2}	DC Current Gain	I _C = -3A ; V _{CE} = -4V	15	75	
f⊤	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -10V	3		MHz



Outline Drawing





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