UNISONIC TECHNOLOGIES CO., LTD

MJE13003D-XS

NPN SILICON TRANSISTOR

NPN SILICON POWER TRANSISTOR

DESCRIPTION

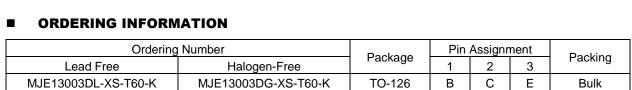
These devices are designed for high-voltage, high-speed power switching inductive circuits where fall time is critical. They are particularly suited for 115V and 220V applications in switch mode.

FEATURES

* 700V blocking capability

APPLICATIONS

- * Switching regulator's, inverters
- * Motor controls
- * Solenoid/relay drivers
- * Deflection circuits



TO-126

В

С

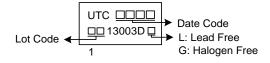
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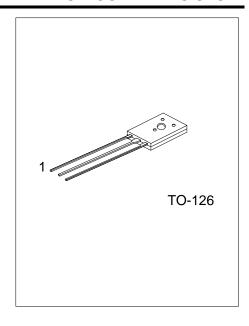
Bulk

MJE13003DG-XS-T60-K

Note: Pin Assignment: B: Base C: Collector E: Emitter MJE13003DG-XS-T60-K (1)Packing Type (1) R: Tape Reel (2)Package Type (2) T60: TO-126 (3)Green Package (3) G: Halogen Free and Lead Free, L: Lead Free

MARKING





www.unisonic.com.tw 1 of 3

■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified.)

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage		V _{CEO(SUS)}	400	V	
Collector-Base Voltage		V _{CBO}	700	V	
Collector-Emitter Voltage (V _{BE} =0)		V _{CES}	700	V	
Emitter Base Voltage		V _{EBO}	9	V	
Collector Current	Continuous	Ic	1.2	А	
	Peak (1)	I _{CM}	2.4		
Power Dissipation	T _A =25°C	ı	1.4	W	
	T _C =25°C	P _D	20	W	
Junction Temperature		T_J	+150	°C	
Storage Temperature		T _{STG}	-55 ~ + 150	°C	

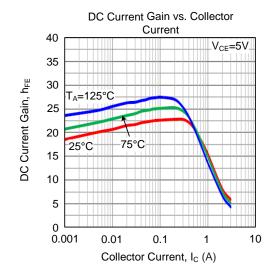
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

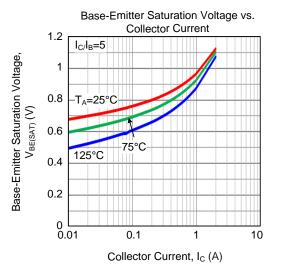
■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise specified.)

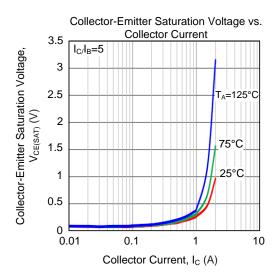
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT				
OFF CHARACTERISTICS (Note)									
V _{CEO(SUS)}	$I_C=10mA$, $I_B=0$	400			V				
I _{CEO}	V _{CEO} =Rated Value,			1	mA				
	V _{BE(OFF)} =1.5 V			5					
I _{EBO}	V _{EB} =9V, I _C =0			1	mA				
h _{FE1}	I _C =0.2A, V _{CE} =5V	15		30	V				
h _{FE2}	I _C =1A, V _{CE} =5V	5		30	V				
V _{CE(SAT)}	I _C =1A, I _B =0.2A			0.6	V				
$V_{BE(SAT)}$	I _C =1A, I _B =0.25A			1.2	V				
Сов	V _{CB} =10V, I _E =0, f=0.1MHz		16		pF				
Resistive Load (Table 1)									
t _D	V _{CC} =125V, I _C =1A, _{B1} =I _{B2} =0.2A, t _P =25μs, Duty Cycle≤1%		0.05	0.1	μs				
t _R			0.5	1	μs				
ts			2	4	μs				
t _F			0.4	0.7	μs				
t _{STG}	I _C =1A, V _{CLAMP} =300V, I _{B1} =0.2A, V _{BE(OFF)} =5V _{DC} , T _C =100°C		1.7	4	μs				
t _C			0.29	0.75	μs				
t _F			0.15		μs				
	VCEO(SUS) ICEO IEBO hFE1 hFE2 VCE(SAT) VBE(SAT) COB tD tR ts tF	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$				

Note: Pulse Test: P_W=300µs, Duty Cycle≤2%.

■ TYPICAL CHARACTERISTICS







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