

MJE18002G

Switch-mode

NPN Bipolar Power Transistor For Switching Power Supply Applications

The MJE18002G have an applications specific state-of-the-art die designed for use in 220 V line operated Switch-mode Power supplies and electronic light ballasts.

Features

- Improved Efficiency Due to Low Base Drive Requirements:
 - ◆ High and Flat DC Current Gain h_{FE}
 - ◆ Fast Switching
 - ◆ No Coil Required in Base Circuit for Turn-Off (No Current Tail)
- Tight Parametric Distributions are Consistent Lot-to-Lot
- Standard TO-220
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Sustaining Voltage	V_{CEO}	450	Vdc
Collector-Emitter Breakdown Voltage	V_{CES}	1000	Vdc
Emitter-Base Voltage	V_{EBO}	9.0	Vdc
Collector Current – Continuous	I_C	2.0	Adc
– Peak (Note 1)	I_{CM}	5.0	
Base Current – Continuous	I_B	1.0	Adc
– Peak (Note 1)	I_{BM}	2.0	
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	50 0.4	W W/ $^\circ\text{C}$
Operating and Storage Temperature	T_J, T_{stg}	-65 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds	T_L	260	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

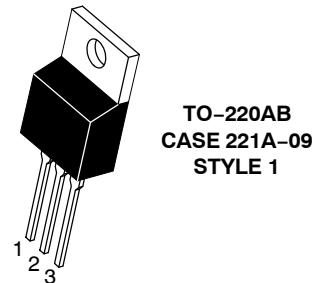
1. Pulse Test: Pulse Width = 5 ms, Duty Cycle $\leq 10\%$.



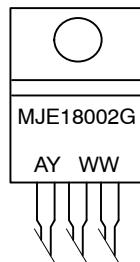
ON Semiconductor®

<http://onsemi.com>

**POWER TRANSISTOR
2.0 AMPERES
100 VOLTS – 50 WATTS**



MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJE18002G	TO-220 (Pb-Free)	50 Units / Rail

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERMM/D.

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ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Sustaining Voltage ($I_C = 100 \text{ mA}$, $L = 25 \text{ mH}$)	$V_{\text{CEO(sus)}}$	450	—	—	Vdc
Collector Cutoff Current ($V_{\text{CE}} = \text{Rated } V_{\text{CEO}}$, $I_B = 0$)	I_{CEO}	—	—	100	μAdc
Collector Cutoff Current ($V_{\text{CE}} = \text{Rated } V_{\text{CES}}$, $V_{\text{EB}} = 0$) ($V_{\text{CE}} = 800 \text{ V}$, $V_{\text{EB}} = 0$)	I_{CES}	— — —	— — —	100 500 100	μAdc
Emitter Cutoff Current ($V_{\text{EB}} = 9.0 \text{ Vdc}$, $I_C = 0$)	I_{EBO}	—	—	100	μAdc

ON CHARACTERISTICS

Base-Emitter Saturation Voltage ($I_C = 0.4 \text{ Adc}$, $I_B = 40 \text{ mAdc}$) ($I_C = 1.0 \text{ Adc}$, $I_B = 0.2 \text{ Adc}$)	$V_{\text{BE(sat)}}$	— —	0.825 0.92	1.1 1.25	Vdc
Collector-Emitter Saturation Voltage ($I_C = 0.4 \text{ Adc}$, $I_B = 40 \text{ mAdc}$) @ $T_C = 125^\circ\text{C}$ ($I_C = 1.0 \text{ Adc}$, $I_B = 0.2 \text{ Adc}$) @ $T_C = 125^\circ\text{C}$	$V_{\text{CE(sat)}}$	— — — —	0.2 0.2 0.25 0.3	0.5 0.5 0.5 0.6	Vdc
DC Current Gain ($I_C = 0.2 \text{ Adc}$, $V_{\text{CE}} = 5.0 \text{ Vdc}$) @ $T_C = 125^\circ\text{C}$ ($I_C = 0.4 \text{ Adc}$, $V_{\text{CE}} = 1.0 \text{ Vdc}$) @ $T_C = 125^\circ\text{C}$ ($I_C = 1.0 \text{ Adc}$, $V_{\text{CE}} = 1.0 \text{ Vdc}$) @ $T_C = 125^\circ\text{C}$ ($I_C = 10 \text{ mAdc}$, $V_{\text{CE}} = 5.0 \text{ Vdc}$)	h_{FE}	14 — 11 11 6.0 5.0 10	— 27 17 20 8.0 8.0 20	34 — — — — — —	—

DYNAMIC CHARACTERISTICS

Current Gain Bandwidth ($I_C = 0.2 \text{ Adc}$, $V_{\text{CE}} = 10 \text{ Vdc}$, $f = 1.0 \text{ MHz}$)	f_T	—	13	—	MHz	
Output Capacitance ($V_{\text{CB}} = 10 \text{ Vdc}$, $I_E = 0$, $f = 1.0 \text{ MHz}$)	C_{ob}	—	35	60	pF	
Input Capacitance ($V_{\text{EB}} = 8.0 \text{ V}$)	C_{ib}	—	400	600	pF	
Dynamic Saturation: determined 1.0 μs and 3.0 μs after rising I_{B1} reach 0.9 final I_{B1} (see Figure 18)	$I_C = 0.4 \text{ A}$ $I_{B1} = 40 \text{ mA}$ $V_{\text{CC}} = 300 \text{ V}$	1.0 μs 3.0 μs	@ $T_C = 125^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	$V_{\text{CE(dsat)}}$ — — — — — —	3.5 8.0 1.5 3.8 8.0 14 2.0 7.0	Vdc — — — — — — —
	$I_C = 1.0 \text{ A}$ $I_{B1} = 0.2 \text{ A}$ $V_{\text{CC}} = 300 \text{ V}$	1.0 μs 3.0 μs	@ $T_C = 125^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$			

2. Proper strike and creepage distance must be provided.

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ELECTRICAL CHARACTERISTICS – continued ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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SWITCHING CHARACTERISTICS: Resistive Load (D.C. $\leq 10\%$, Pulse Width = 20 μs)

Turn-On Time	$I_C = 0.4 \text{ Adc}$ $I_{B1} = 40 \text{ mAadc}$ $I_{B2} = 0.2 \text{ Adc}$ $V_{CC} = 300 \text{ V}$	@ $T_C = 125^\circ\text{C}$	t_{on}	–	200	300	ns
Turn-Off Time			t_{off}	–	1.2	2.5	μs
Turn-On Time	$I_C = 1.0 \text{ Adc}$ $I_{B1} = 0.2 \text{ Adc}$ $I_{B2} = 0.5 \text{ Adc}$ $V_{CC} = 300 \text{ V}$	@ $T_C = 125^\circ\text{C}$	t_{on}	–	85	150	ns
Turn-Off Time			t_{off}	–	1.7	2.5	μs

SWITCHING CHARACTERISTICS: Inductive Load ($V_{clamp} = 300 \text{ V}$, $V_{CC} = 15 \text{ V}$, $L = 200 \mu\text{H}$)

Fall Time	$I_C = 0.4 \text{ Adc}$, $I_{B1} = 40 \text{ mAadc}$, $I_{B2} = 0.2 \text{ Adc}$	@ $T_C = 125^\circ\text{C}$	t_{fi}	–	125	200	ns
Storage Time			t_{si}	–	0.7	1.25	μs
Crossover Time			t_c	–	110	200	ns
Fall Time	$I_C = 1.0 \text{ Adc}$, $I_{B1} = 0.2 \text{ Adc}$, $I_{B2} = 0.5 \text{ Adc}$	@ $T_C = 125^\circ\text{C}$	t_{fi}	–	110	175	ns
Storage Time			t_{si}	–	0.8	2.75	μs
Crossover Time			t_c	–	110	200	ns
Fall Time	$I_C = 0.4 \text{ Adc}$, $I_{B1} = 50 \text{ mAadc}$, $I_{B2} = 50 \text{ mAadc}$	@ $T_C = 125^\circ\text{C}$	t_{fi}	–	140	200	ns
Storage Time			t_{si}	–	185	–	
Crossover Time			t_c	–	2.2	3.0	μs

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TYPICAL STATIC CHARACTERISTICS

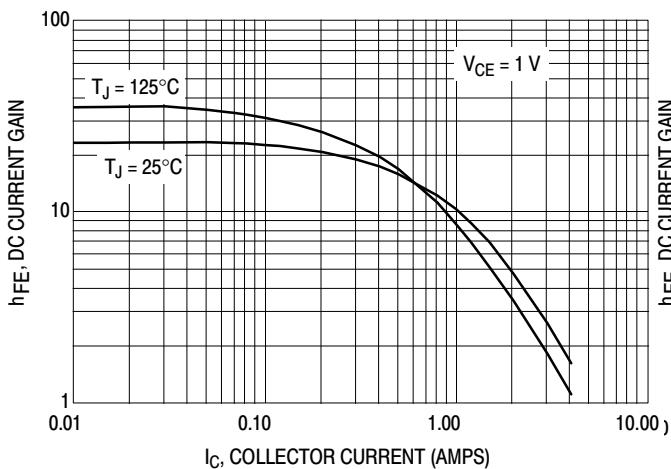


Figure 1. DC Current Gain @ 1 Volt

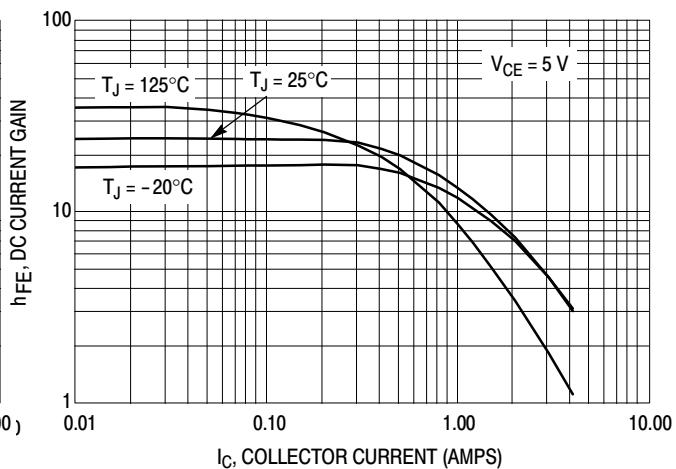


Figure 2. DC Current Gain @ 5 Volts

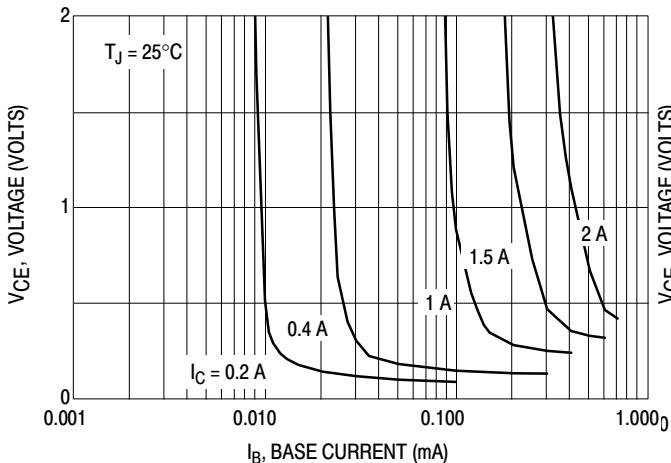


Figure 3. Collector Saturation Region

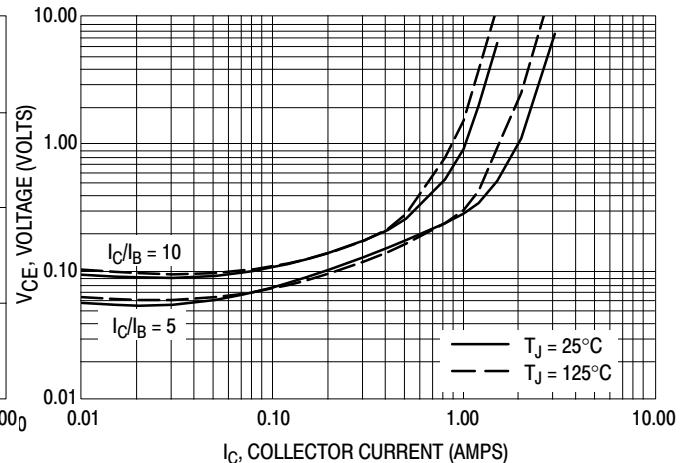


Figure 4. Collector-Emitter Saturation Voltage

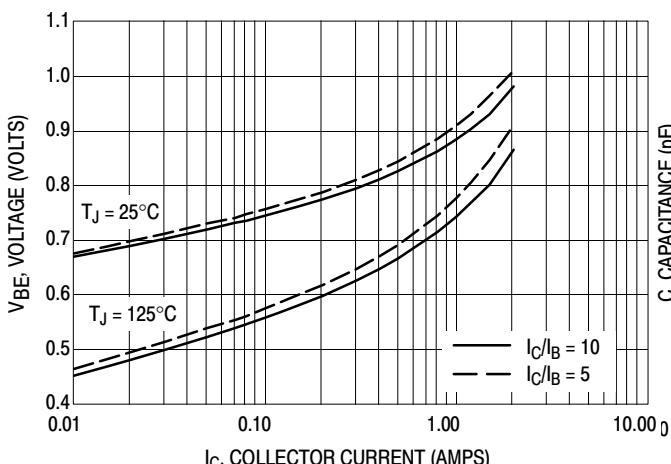


Figure 5. Base-Emitter Saturation Region

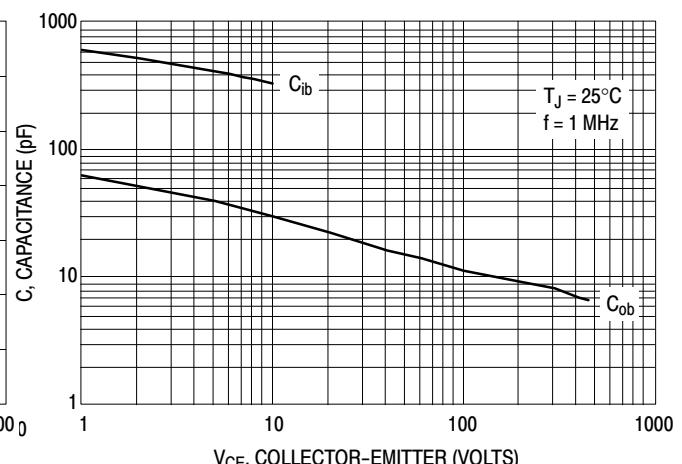


Figure 6. Capacitance

TYPICAL SWITCHING CHARACTERISTICS
($I_{B2} = I_C/2$ for all switching)

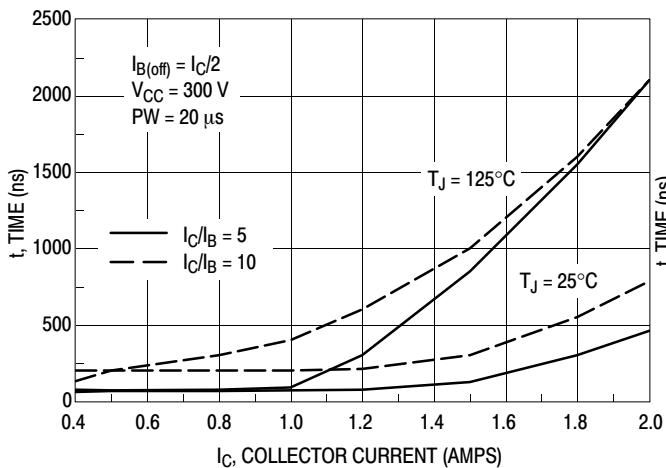


Figure 7. Resistive Switching, t_{on}

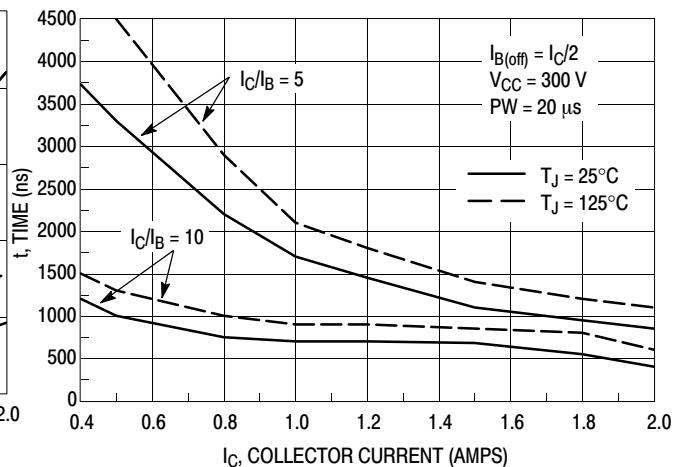


Figure 8. Resistive Switching, t_{off}

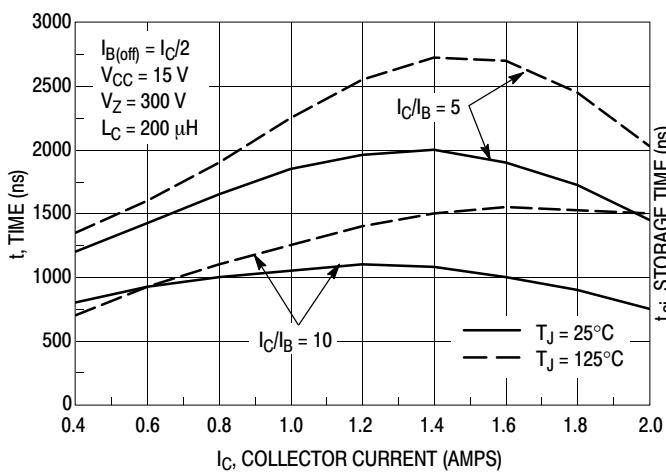


Figure 9. Inductive Storage Time, t_{si}

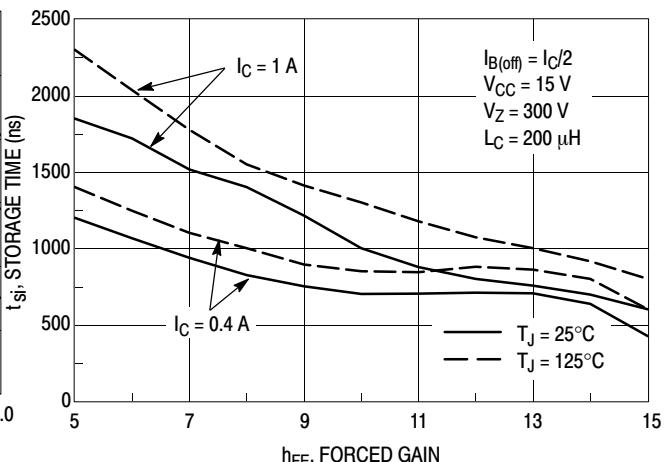


Figure 10. Inductive Storage Time

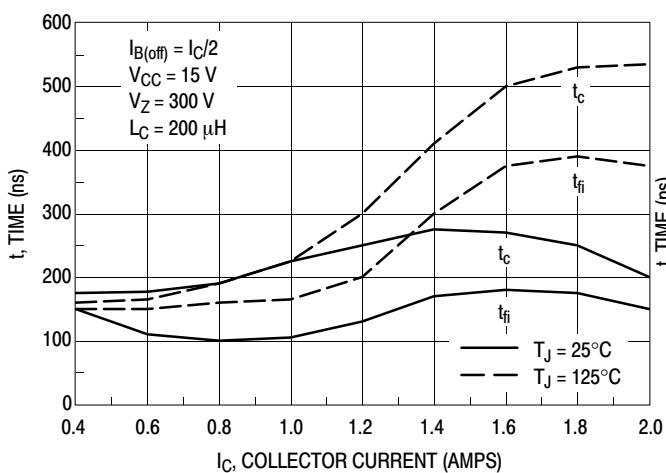


Figure 11. Inductive Switching, t_c and t_{fi} , $I_C/I_B = 5$

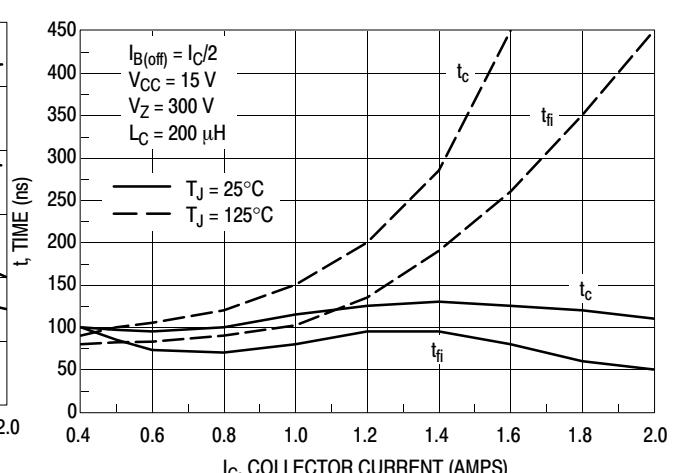


Figure 12. Inductive Switching, t_c and t_{fi} , $I_C/I_B = 10$

TYPICAL SWITCHING CHARACTERISTICS
($I_{B2} = I_C/2$ for all switching)

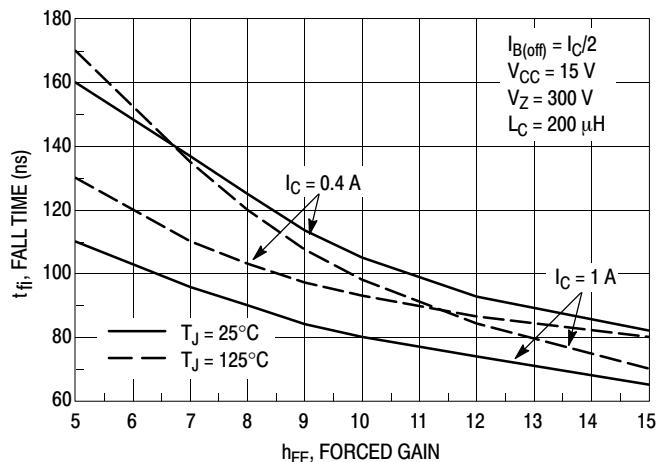


Figure 13. Inductive Fall Time

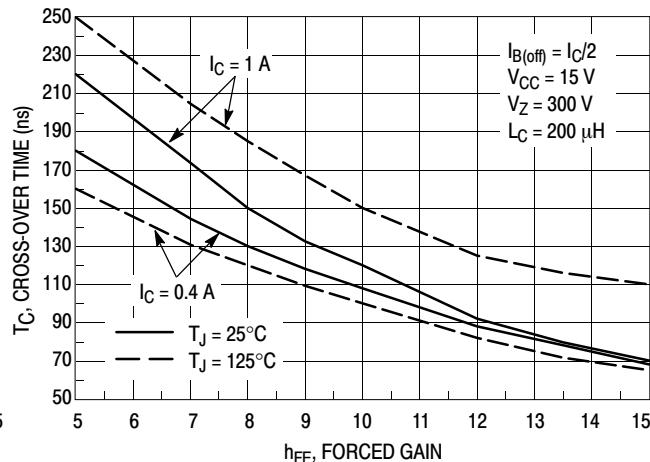


Figure 14. Inductive Crossover Time

GUARANTEED SAFE OPERATING AREA INFORMATION

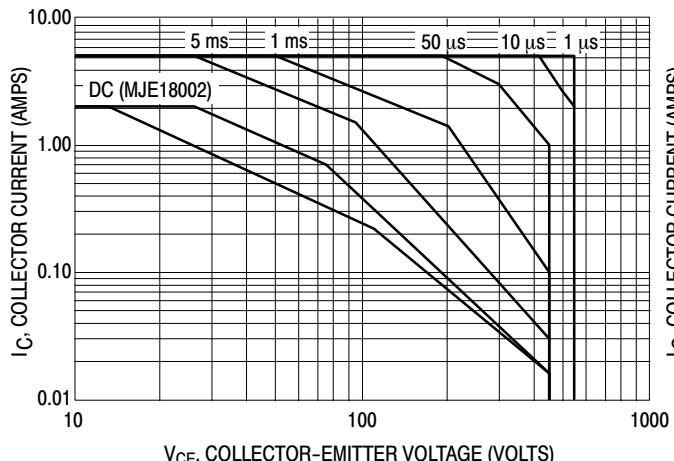


Figure 15. Forward Bias Safe Operating Area

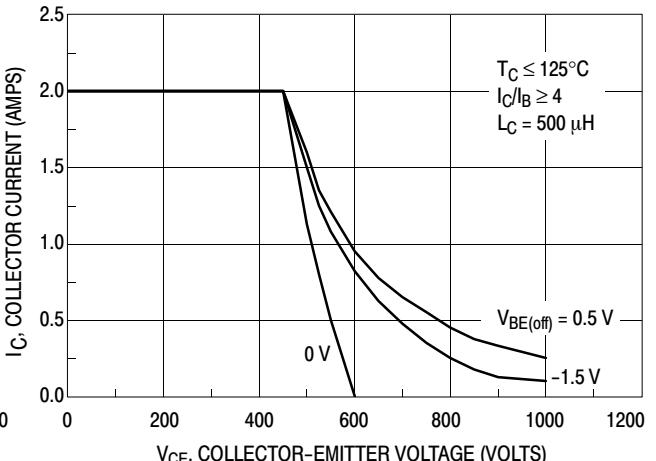


Figure 16. Reverse Bias Switching Safe Operating Area

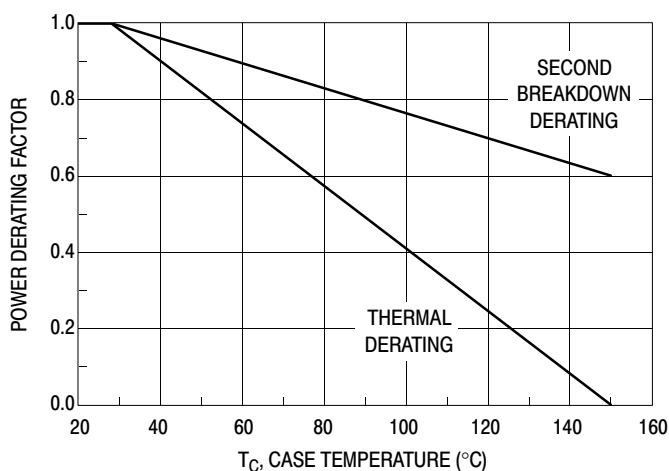


Figure 17. Forward Bias Power Derating

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 15 is based on $T_C = 25^\circ\text{C}$; $T_J(\text{pk})$ is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when $T_C > 25^\circ\text{C}$. Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown on Figure 15 may be found at any case temperature by using the appropriate curve on Figure 17. $T_J(\text{pk})$ may be calculated from the data in Figures 20. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. For inductive loads, high voltage and current must be sustained simultaneously during turn-off with the base to emitter junction reverse biased. The safe level is specified as a reverse biased safe operating area (Figure 16). This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode.

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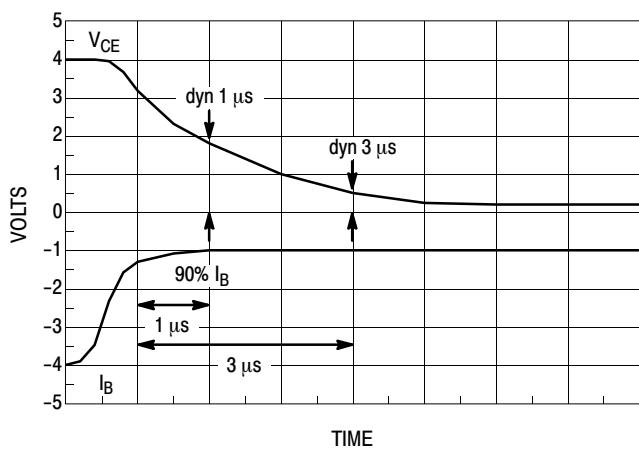


Figure 18. Dynamic Saturation Voltage Measurements

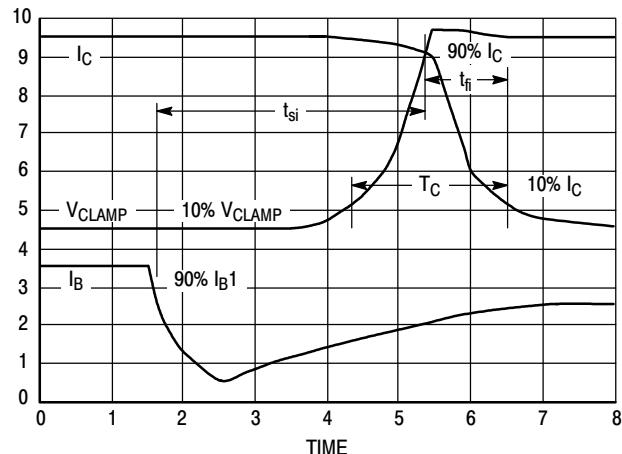


Figure 19. Inductive Switching Measurements

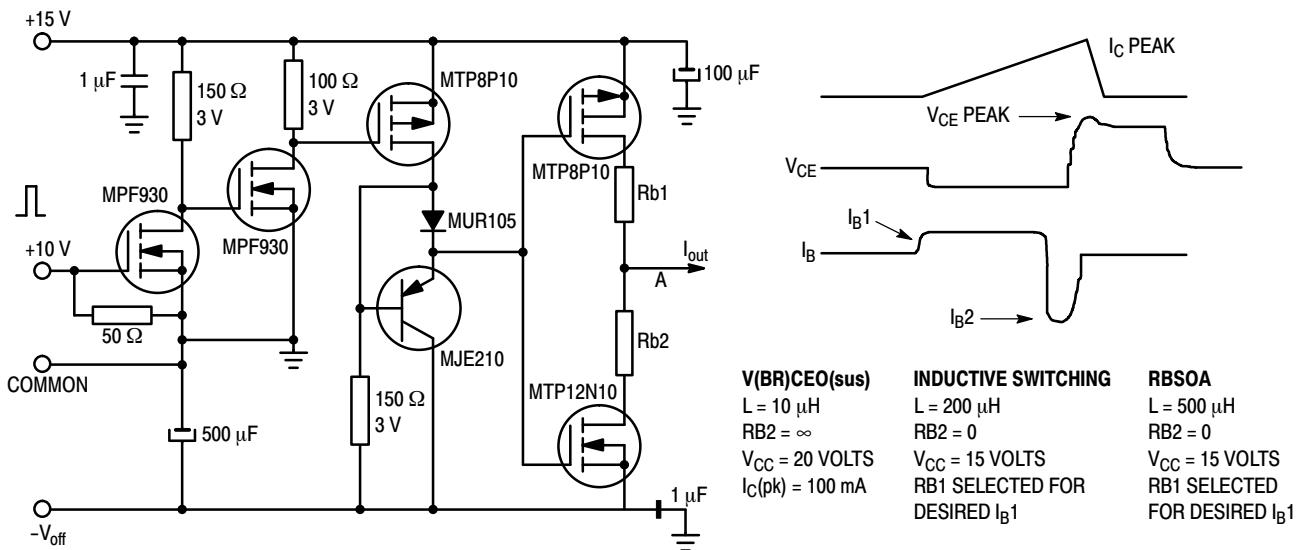


Table 1. Inductive Load Switching Drive Circuit

TYPICAL THERMAL RESPONSE

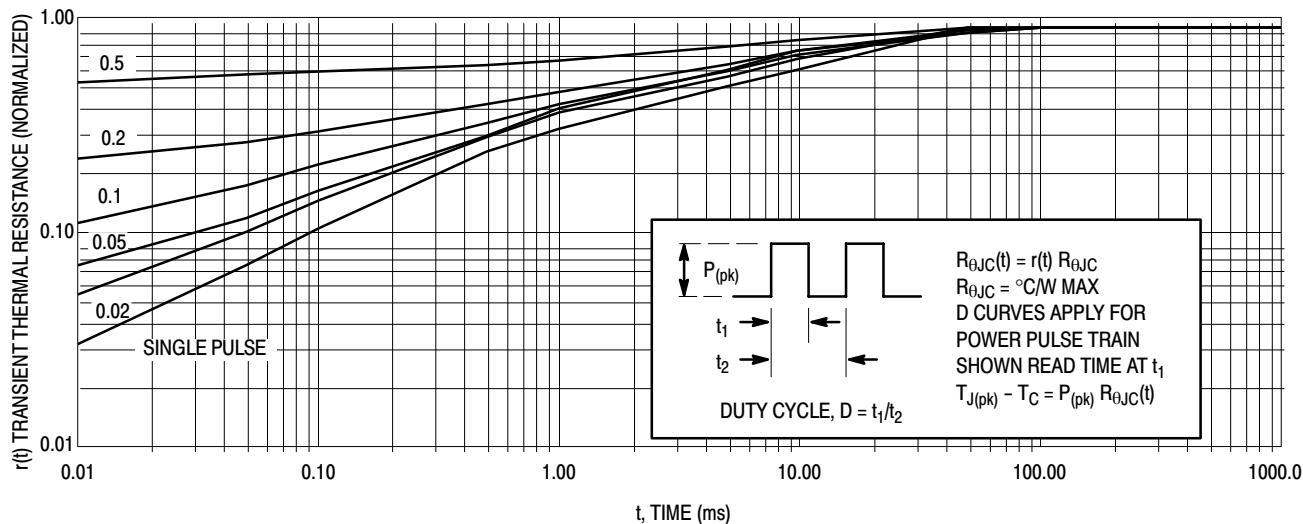


Figure 20. Typical Thermal Response ($Z_{\theta JC}(t)$) for MJE18002

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