

2N 5655 2N 5656 2N 5657 (SILICON) MJE5655 MJE5656 MJE5657

PLASTIC NPN SILICON HIGH-VOLTAGE POWER TRANSISTORS

... designed for use in line-operated equipment such as audio output amplifiers; low-current, high-voltage converters; and AC line relays

- Excellent DC Current Gain — $h_{FE} = 30\text{-}250$ @ $I_C = 100$ mAdc
- Current-Gain — Bandwidth Product —
 $f_T = 10$ MHz (Min) @ $I_C = 50$ mAdc
- Packaged in Thermopad Case for Low Cost
- Choice of Packages — 2N5655, 2N5656, 2N5657 — Case 77
- MJE5655, MJE5656, MJE5657 — Case 199

*MAXIMUM RATINGS

MAXIMUM RATINGS					
Rating	Symbol	2N5655 MJE5655	2N5656 MJE5656	2N5657 MJE5657	Unit
Collector-Emitter Voltage	V_{CE0}	250	300	350	Vdc
Collector-Base Voltage	V_{CB}	275	325	375	Vdc
Emitter-Base Voltage	V_{EB}	$\longleftrightarrow 6.0 \longrightarrow$			Vdc
Collector Current — Continuous Peak	I_C	$\longleftrightarrow 0.5 \longrightarrow$			Adc
		$\longleftrightarrow 1.0 \longrightarrow$			
Base Current	I_B	$\longleftrightarrow 0.25 \longrightarrow$			Adc
		2N5655 Series		MJE5655 Series	
Total Device Dissipation @ $T_C = 25^{\circ}\text{C}$ Derate above 25°C	P_D	20 0.16	30 0.24		Watts W/ $^{\circ}\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +150			$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	2N5655 Series	MJE5655 Series	Unit
Thermal Resistance, Junction to Case	θ_{JC}	6.25	4.167	$^\circ\text{C/W}$

*Indicates JEDEC Registered Data for 2N5655 Series.

FIGURE 1 — POWER DERATING

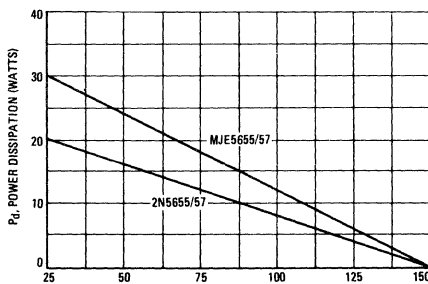
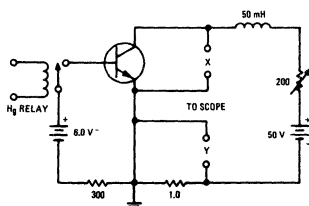


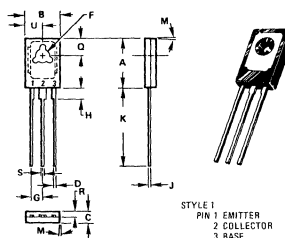
FIGURE 2 — SUSTAINING VOLTAGE TEST CIRCUIT



Safe Area Limits are indicated by Figures 3 and 4. Both limits are applicable and must be observed.

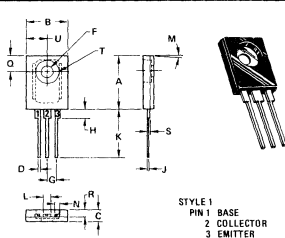
0.5 AMPERE POWER TRANSISTORS NPN SILICON

250-300-350 VOLTS
20 and 30 WATTS



DIM	MIN	MAX	MIN	MAX
A	10.80	11.05	0.425	0.435
B	7.40	7.75	0.295	0.305
C	2.41	2.57	0.095	0.105
D	0.51	0.66	0.020	0.026
E	2.92	3.00	0.115	0.118
F	2.36	2.54	0.093	0.100
G	2.16	2.41	0.085	0.095
H	0.38	0.54	0.015	0.021
J	15.38	16.54	0.605	0.655
K	2.16	2.41	0.085	0.095
L	0.38	0.54	0.015	0.021
M	1.14	1.40	0.045	0.055
N	0.84	0.89	0.035	0.035
U	3.68	3.94	0.145	0.155

CASE 77-03



DIM	MIN	MAX	MIN	MAX
A	16.08	16.33	0.633	0.643
B	12.57	12.83	0.495	0.505
C	3.16	3.45	0.125	0.135
D	0.51	0.78	0.020	0.030
E	3.61	3.86	0.142	0.152
F	2.54	2.80	0.100	0.110
G	2.07	2.32	0.081	0.091
H	0.43	0.69	0.017	0.027
J	14.73	14.89	0.580	0.590
K	2.16	2.41	0.085	0.095
L	0.38	0.54	0.015	0.021
M	1.14	1.40	0.045	0.055
N	0.84	0.89	0.035	0.035
U	6.22	6.48	0.245	0.255

CASE 199-04

2N5655, 2N5656, 2N5657/MJE5655, MJE5656, MJE5657 (continued)

*ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage ($I_C = 100\text{ mAdc}$ (inductive), $L = 50\text{ mH}$)	$V_{CEO(sus)}$	250 300 350	—	Vdc
Collector-Emitter Breakdown Voltage ($I_C = 1.0\text{ mAdc}$, $I_B = 0$)	BV_{CEO}	250 300 350	—	Vdc
Collector Cutoff Current ($V_{CE} = 150\text{ Vdc}$, $I_B = 0$) ($V_{CE} = 200\text{ Vdc}$, $I_B = 0$) ($V_{CE} = 250\text{ Vdc}$, $I_B = 0$)	I_{CEO}	— — —	0.1 0.1 0.1	mAcd
Collector Cutoff Current ($V_{CE} = 250\text{ Vdc}$, $V_{EB(off)} = 1.5\text{ Vdc}$) ($V_{CE} = 300\text{ Vdc}$, $V_{EB(off)} = 1.5\text{ Vdc}$) ($V_{CE} = 350\text{ Vdc}$, $V_{EB(off)} = 1.5\text{ Vdc}$) ($V_{CE} = 150\text{ Vdc}$, $V_{EB(off)} = 1.5\text{ Vdc}$, $T_C = 100^\circ\text{C}$) ($V_{CE} = 200\text{ Vdc}$, $V_{EB(off)} = 1.5\text{ Vdc}$, $T_C = 100^\circ\text{C}$) ($V_{CE} = 250\text{ Vdc}$, $V_{EB(off)} = 1.5\text{ Vdc}$, $T_C = 100^\circ\text{C}$)	I_{CEX}	— — — — — —	0.1 0.1 0.1 1.0 1.0 1.0	mAcd
Collector Cutoff Current ($V_{CB} = 275\text{ Vdc}$, $I_E = 0$) ($V_{CB} = 325\text{ Vdc}$, $I_E = 0$) ($V_{CB} = 375\text{ Vdc}$, $I_E = 0$)	I_{CBO}	— — —	10 10 10	μAcd
Emitter Cutoff Current ($V_{EB} = 6.0\text{ Vdc}$, $I_C = 0$)	I_{EBO}	—	10	μAcd

ON CHARACTERISTICS

DC Current Gain (1) ($I_C = 50\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 100\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 250\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 500\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$)	h_{FE}	25 30 15 5.0	— 250 — —	—
Collector-Emitter Saturation Voltage (1) ($I_C = 100\text{ mAcd}$, $I_B = 10\text{ mAcd}$) ($I_C = 250\text{ mAcd}$, $I_B = 25\text{ mAcd}$) ($I_C = 500\text{ mAcd}$, $I_B = 100\text{ mAcd}$)	$V_{CE(sat)}$	— — —	1.0 2.5 10	Vdc
Base-Emitter Voltage (1) ($I_C = 100\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$)	V_{BE}	—	1.0	Vdc

DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product (2) ($I_C = 50\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$, $f = 10\text{ MHz}$)	f_T	10	—	MHz
Output Capacitance ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f = 100\text{ kHz}$)	C_{ob}	—	25	pF
Small-Signal Current Gain ($I_C = 100\text{ mAcd}$, $V_{CE} = 10\text{ Vdc}$, $f = 1.0\text{ kHz}$)	h_{fe}	20	—	—

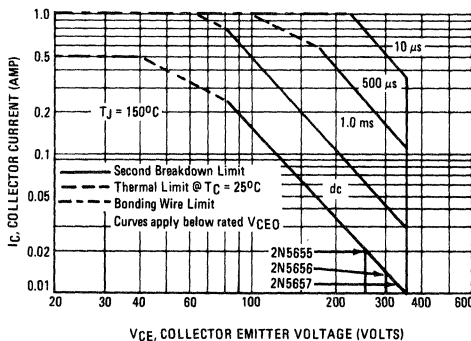
*Indicates JEDEC Registered Data for 2N5655 Series

(1) Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2\text{ }\%$.

(2) f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity

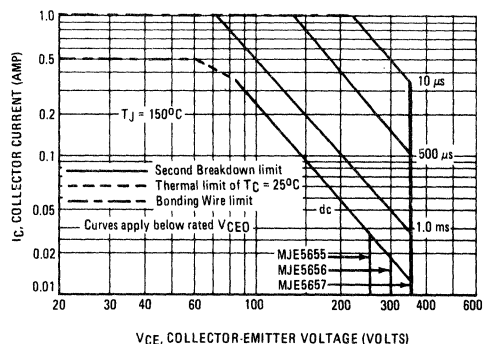
ACTIVE-REGION SAFE OPERATING AREA

FIGURE 3 — 2N5655, 2N5656, 2N5657



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C \cdot V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

FIGURE 4 — MJE5655, MJE5656, MJE5657



The data of Figures 3 and 4 is based on $T_{J(pk)} = 150^\circ\text{C}$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^\circ\text{C}$. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. (See AN-415)

LARGE SIGNAL CHARACTERISTICS

FIGURE 5 – TRANSCONDUCTANCE

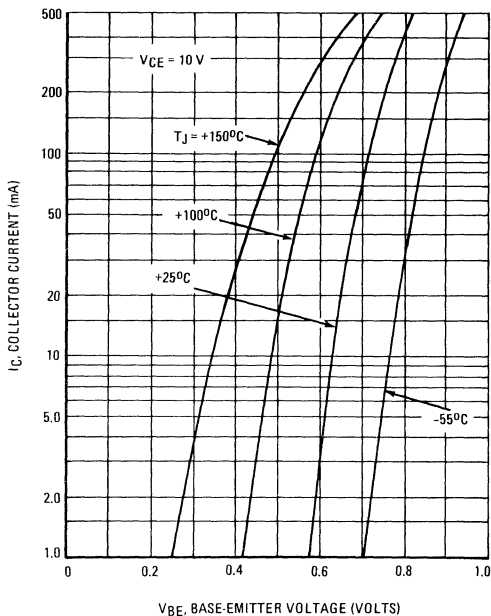
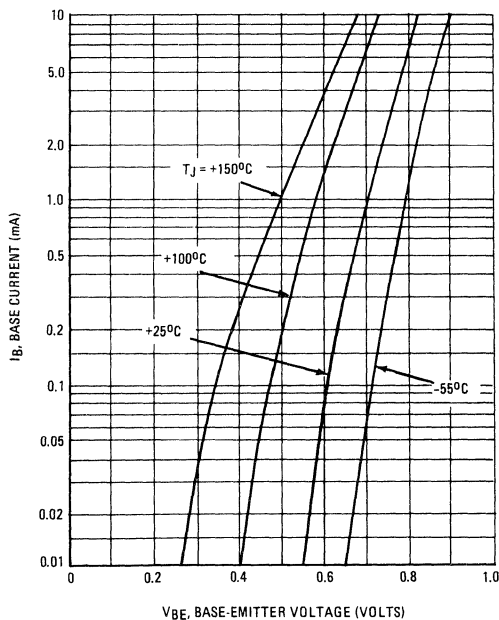


FIGURE 7 – INPUT ADMITTANCE



CUT-OFF CHARACTERISTICS

FIGURE 6 – TRANSCONDUCTANCE

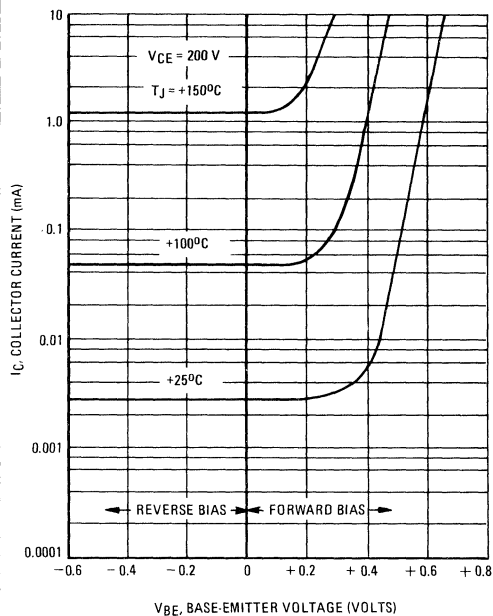


FIGURE 8 – EFFECT OF BASE-EMITTER RESISTANCE

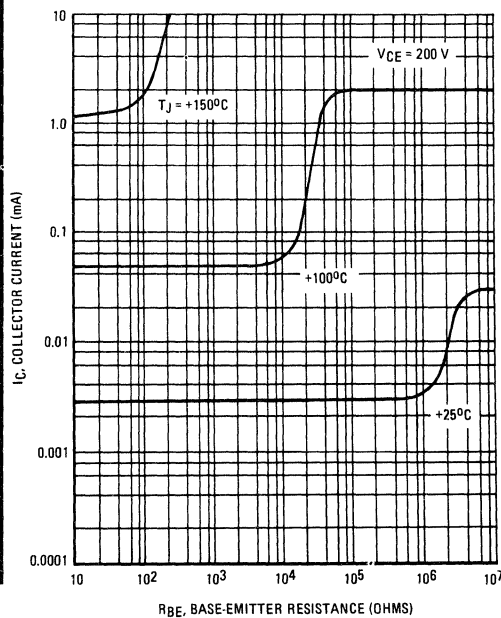


FIGURE 9 – CURRENT GAIN

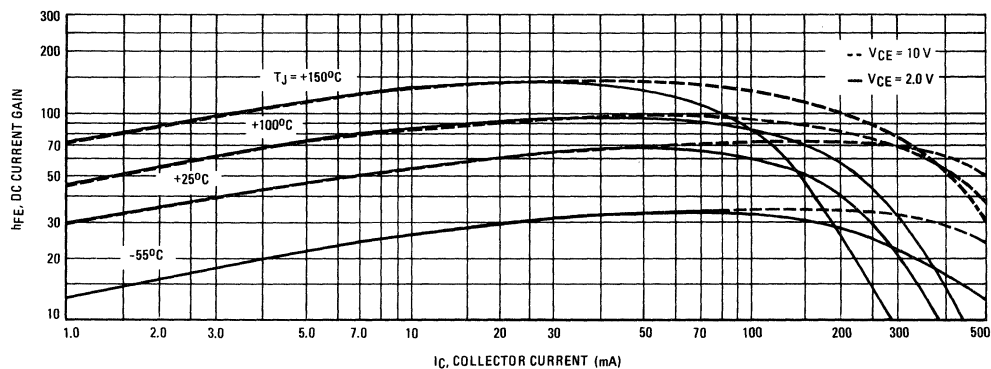


FIGURE 10 – “ON” VOLTAGES

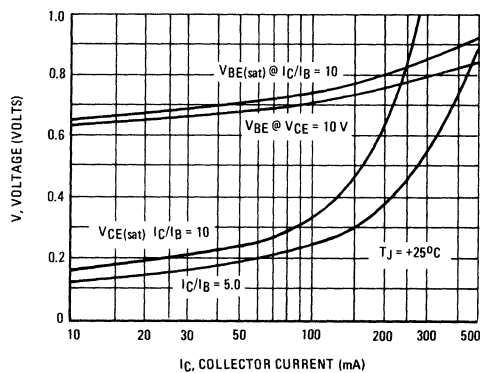


FIGURE 11 – CAPACITANCE

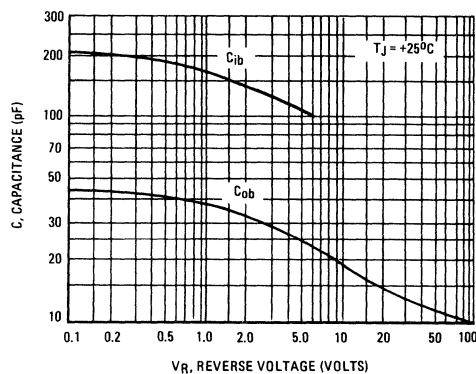


FIGURE 12 – TURN-ON TIME

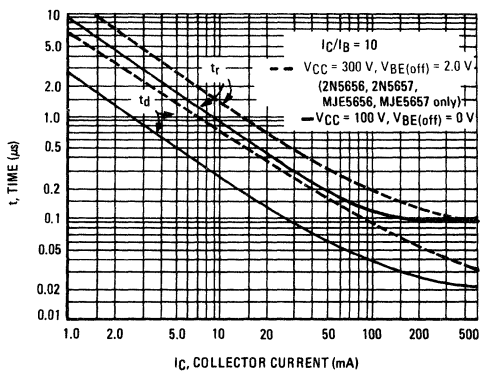


FIGURE 13 – TURN-OFF TIME

