

isc Silicon NPN Power Transistor

MJW16018

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)} = 800V(Min)
- · High Switching Speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

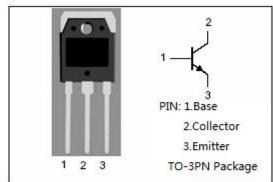
- Designed for high-voltage ,high-speed, power switching in inductive circuits where fall time is critical. They are particularly suited for line operated switch-mode applications.
 Typical applications:
- Switching regulators
- Inverters
- · Solenoid and relay drivers
- Motor controls
- · Deflection circuits

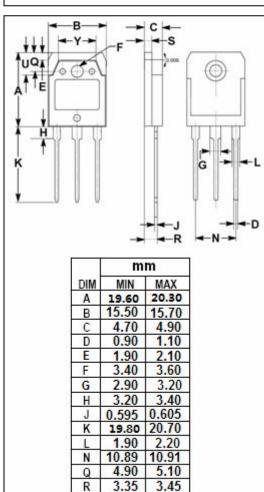
ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector- Base Voltage	1500	V	
V _{CEO(SUS)}	Collector-Emitter Voltage	800	V	
V _{EBO}	Emitter-Base Voltage	6	V	
Ic	Collector Current-Continuous	10	А	
I _{CM}	Collector Current-Peak	15	Α	
I _B	Base Current-Continuous	8	А	
Івм	Base Current-Peak	12	А	
Pc	Collector Power Dissipation@T _C =25℃	125	W	
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature	-55~150	$^{\circ}$	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT	
Rth j-c	Thermal Resistance,Junction to Case	1.0	°C/W	





1.995 2.100 5.90 6.20 9.90 10.10



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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C =50mA ; I _B =0	800			V
V _{CE} (sat)-1	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 2A I _C = 5A; I _B = 2A,T _C =100°C			1.0 1.5	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 10A; I _B = 5A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 2A I _C = 5A; I _B = 2A,T _C =100°C			1.5 1.5	V
I _{CBO}	Collector Cutoff Current	V _{CBO} =1500V;I _E =0 V _{CBO} =1500V;I _E =0;T _C =100°C			0.25 1.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C =0			0.1	mA
h _{FE}	DC Current Gain	Ic= 5A; Vc= 5V	4			
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} =1.0kHz		450		pF

Pulsed Test: Pulse duration = 300 ms, duty cycle ≤ 2%

Switching Times

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t _d	Delay Time	I _C = 5A; I _{B1} =-I _{B2} = 2A; V _{CC} = 250V			0.2	μ S
tr	Rise Time				2.0	μ S
ts	Storage Time				9.0	μS
t _f	Fall Time				0.4	μ \$



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