

INCHANGE SEMICONDUCTOR

isc Silicon NPN Power Transistor

MJW21196

DESCRIPTION

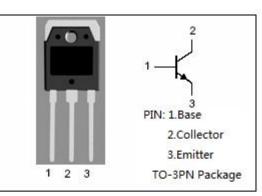
- · Total harmonic distortion characterized
- High DC current gain
- Excellent gain linearity
- High SOA
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

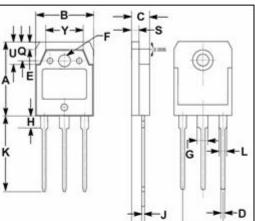
APPLICATIONS

 The MJW21196 is specifically designed for high power audio output disk head positioners and linear applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{сво}	Collector-Base Voltage	400	V
V _{CEO}	Collector-Emitter Voltage	250	V
V _{EBO}	Emitter-Base Voltage	5	V
lc	Collector Current-Continuous	16	А
IB	Base Current-Continuous	5.0	А
Pc	Collector Power Dissipation @ $T_C=25^{\circ}C$	200	W
TJ	Junction Temperature 150		°C
T _{stg}	Storage Temperature Range	-65~150	°C





R

	mm		
DIM	MIN	MAX	
Α	19.60	20.30	
В	15.50	15.70	
С	4.70	4.90	
D	0.90	1.10	
Ε	1.90	2.10	
F	3.40	3.60	
G	2.90	3.20	
Н	3.20	3.40	
J	0.595	0.605	
Κ	19.80	20.70	
L	1.90	2.20	
Ν	10.89	10.91	
Q	4.90	5.10	
R	3.35	3.45	
S	1.995	2.100	
U	5.90	6.20	
Y	9.90	10.10	



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ELECTRICAL CHARACTERISTICS

$T_{\text{C}}\text{=}25\,^{\circ}\!\!\!\!\!\mathrm{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 50mA ; I _B = 0	250			V
V _{CE(sat)} -1	Collector-Emitter Saturation Voltage	I _C = 8.0A; I _B = 0.8A			1.0	V
V _{CE(sat)} -2	Collector-Emitter Saturation Voltage	I _C = 16Α; I _B = 3.2Α			3.0	V
$V_{\text{BE(on)}}$	Base-Emitter On Voltage	Ic=8A ; Vce= 5V			2.0	V
I _{CEO}	Collector Cutoff Current	V _{CE} = 200V ; I _E = 0			100	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			50	μA
h _{FE-1}	DC Current Gain	I _C = 8A ; V _{CE} = 5V	20		80	
h _{FE-2}	DC Current Gain	I _C = 16A ; V _{CE} = 5V	8			
fī	Current-Gain—Bandwidth Product	I _C =1A ; V _{CE} = 10V	4			MHz

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