1 Features and Benefits

- Programmable parameters in application:
  - Wide magnetic Latch range: ±0.4mT to ±80mT
  - Wide magnetic Switch range: ±1.5mT to ±66mT
  - Programmable Hysteresis: 1mT to 36mT
  - Programmable Active Pole: North or South
  - Programmable Output Behaviour: Direct or Inverted
  - Built-in Negative TC coefficient: 0 to -2000 ppm/°C
  - Increased Traceability: 32 bits ID on chip
- Wide operating voltage range: from 2.7V to 24V
- Reverse Supply Voltage Protection
- Output Current Limit with Auto-Shutoff
- Under-Voltage Lockout Protection
- Thermal Protection
- Lateral Sensitivity and dual die option

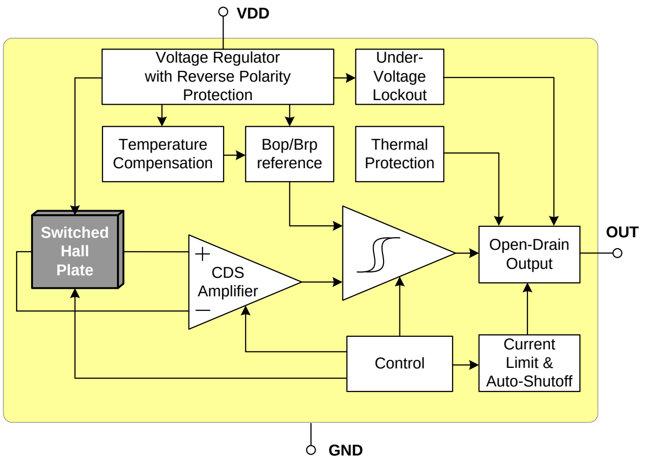
2 Application Examples

- Automotive, Consumer and Industrial
- Solid-state switch
- 3-phase BLDC motor commutation
- Wiper motor
- Window lifter
- Sunroof/Tailgate opener
- Seat motor adjuster
- Electrical power steering
- Brake Light switch

3 Ordering Information

Product Code	Temperature Code	Package Code	Comment
MLX92232LSE-AAA-000-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch/Latch, TC=0 ppm/°C
MLX92232LUA-AAA-000-BU	L (-40°C to 150°C)	UA (TO92-3L)	3-wire Switch/Latch, TC=0 ppm/°C
MLX92232LVA-AAA-000-BU	L (-40°C to 150°C)	VA (SIP 4L)	Dual Die 3-wire Switch/Latch, TC=0 ppm/°C
MLX92232LSE-AAA-001-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch/Latch, TC=-400 ppm/°C
MLX92232LUA-AAA-001-BU	L (-40°C to 150°C)	UA (TO92-3L)	3-wire Switch/Latch, TC=-400 ppm/°C
MLX92232LSE-AAA-002-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch/Latch, TC=-1100 ppm/°C
MLX92232LUA-AAA-002-BU	L (-40°C to 150°C)	UA (TO92-3L)	3-wire Switch/Latch, TC=-1100 ppm/°C
MLX92232LSE-AAA-003-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch/Latch, TC=-2000 ppm/°C
MLX92232LUA-AAA-003-BU	L (-40°C to 150°C)	UA (TO92-3L)	3-wire Switch/Latch, TC=-2000 ppm/°C
MLX92232LSE-AAA-200-RE	L (-40°C to 150°C)	SE (TSOT-3L)	3-wire Switch/Latch IMC version, TC=0 ppm/°C
MLX92232LUA-AAA-200-BU	L (-40°C to 150°C)	UA (TO92-3L)	3-wire Switch/Latch IMC version, TC=0 ppm/°C

4 Functional Diagram



5 General Description

The Melexis MLX92232 is the second generation programmable Hall-effect sensor designed in mixed signal CMOS technology. The device integrates a voltage regulator, Hall sensor with advanced offset cancellation system and an open-drain output driver, all in a single package.

With the built-in reverse voltage protection, a serial resistor or diode on the supply line is not required so that even remote sensors can be specified for low voltage operation down to 2.7V while being reverse voltage tolerant. In the event of a drop below the minimum supply voltage during operation, the under-voltage lock-out protection will automatically freeze the device, preventing the electrical perturbation to affect the magnetic measurement circuitry.

The open drain output is fully protected against short-circuit with a built-in current limit. An additional automatic output shut-off is activated in case of a prolonged short-circuit condition. A self-check is then periodically performed to switch back to normal operation if the short-circuit condition is released.

The on-chip thermal protection also switches off the output if the junction temperature increases above an abnormally high threshold. It will automatically recover once the temperature decreases below a safe value.

Furthermore the MLX92232 features a full set of programmable parameters that can be adjusted in the application in order to achieve the highest possible system accuracy by compensating the mechanical tolerances.

An Integrated Magnetic Concentrator option (IMC) has been added to sense the lateral field component. This is adding more flexibility in the module design. A dual die option is also available for applications that need a secondary output; these can be programmed independently from each other.

Table of Contents

6 Glossary of Terms

Tesla	Units for the magnetic flux density, 1 mT = 10 Gauss
TC	Temperature Coefficient in ppm/°C
IMC	Integrated Magnetic Concentrator
POR	Power on Reset

7 Absolute Maximum Ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

Parameter	Symbol	Value	Units
Supply Voltage [1, 2]	V_{DD}	+27	V
Supply Voltage (Load Dump) [1, 4]	V_{DD}	+32	V
Supply Current [1, 2, 3]	I_{DD}	+20	mA
Supply Current [1, 3, 4]	I_{DD}	+50	mA
Reverse Supply Voltage [1, 2]	V_{DDREV}	-24	V
Reverse Supply Voltage [1, 4]	V_{DDREV}	-30	V
Reverse Supply Current [1, 2, 5]	I_{DDREV}	-20	mA
Reverse Supply Current [1, 4, 5]	I_{DDREV}	-50	mA
Output Voltage [1, 2]	V_{OUT}	+27	V
Output Current [1, 2, 5]	I_{OUT}	+20	mA
Output Current [1, 4, 6]	I_{OUT}	+75	mA
Reverse Output Voltage [1]	V_{OUTREV}	-0.5	V
Reverse Output Current [1, 2]	I_{OUTREV}	-100	mA
Maximum Junction Temperature [7]	T_J	+165	°C
Storage Temperature Range	Ts	-55 to +165	°C
ESD Sensitivity – HBM [8]	-	4000	V
ESD Sensitivity – CDM [9]	-	1000	V
Magnetic Flux Density	B	Unlimited	mT

Table 1: Absolute maximum ratings

1 The maximum junction temperature should not be exceeded

2 For maximum 1 hour

3 Including current through protection device

4 For maximum 500ms

5 Through protection device

6 For V_{OUT}≤27V

7 For 1000 hours

8 Human Model according AEC-Q100-002 standard

9 Charged Device Model according AEC-Q100-011 standard

8 General Electrical Specifications

DC Operating Parameters V_{DD} = 2.7V to 24V, T_A = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ[1]	Max	Units
Supply Voltage	V_{DD}	Operating	2.7	-	24	V
Supply Current [2]	I_{DD}		1.5	3.0	4.5	mA
Supply Current [3]	I_{DD}		3.0	6.0	9.0	mA
Reverse supply current	I_{DDREV}	V_{DD} = -16V	-1	-	-	mA
Output Saturation Voltage	V_{DSON}	V_{DD} = 3.5 to 24V, I_{OUT} = 20mA	-	0.3	0.5	V
Output Leakage	I_{OFF}	V_{OUT} = 12V, V_{DD} = 12V	-	-	10	µA
Output Rise Time [4, 8] (R_{PU} dependent)	t_R	R_{PU} = 1kΩ, V_{DD} = 12V, V_{PU} = 5V C_{LOAD} = 50pF to GND	0.1	0.3	1	µs
Output Fall Time [4, 8] (On-chip controlled)	t_F	R_{PU} = 1kΩ, V_{DD} = 12V, V_{PU} = 5V C_{LOAD} = 50pF to GND	0.1	0.3	1	µs
Power-On Time [5,6,9]	t_{ON}	V_{DD} = 5V, dV_{DD}/dt > 2V/us	-	40	70	µs
Power-On Output State	-	$t < t_{ON}$		High (V_{PU})		-
Output Current Limit	I_{CL}	V_{DD}=3.5 to 24V, V_{OUT} = 12V	25	40	70	mA
Output ON Time under Current Limit conditions[10]	t_{CLON}	V_{PU} = 12V, R_{PU} = 100Ω	150	240		µs
Output OFF Time under Current Limit conditions[10]	t_{CLOFF}	V_{PU} = 12V, R_{PU} = 100Ω	-	3.5	-	ms
Chopping Frequency	f_{CHOP}		-	340	-	kHz
Refresh Period	t_{PER}		-	6	-	µs
Output Jitter (p-p) [4]	t_{JITTER}	Over 1000 successive switching events @10kHz triangle wave magnetic field, B > ±(B_{OPMAX} +20mT)	-	±3.2	-	µs
Maximum Switching Frequency [4, 7]	f_{SW}	B > ±3(B_{OPMAX} +1mT), triangle wave magnetic field	30	65	-	kHz
Under-voltage Lockout Threshold	V_{UVL}		-	-	2.7	V
Under-voltage Lockout Reaction time [4]	t_{UVL}		-	1	-	µs
Thermal Protection Threshold	T_{PROT}	Junction temperature	-	190[11]	-	°C
Thermal Protection Release	T_{REL}	Junction temperature	-	180[11]	-	°C
SE Package Thermal Resistance	R_{THJA}	Single layer PCB, JEDEC standard test boards		300		°C/W
UA package Thermal Resistance	R_{THJA}	Single layer PCB, JEDEC standard test boards		200		°C/W
VA package Thermal Resistance	R_{THJA}	Single layer PCB, JEDEC standard test boards		105		°C/W

Table 2: General Electrical parameters

1 *Typical values are defined at T_A = +25°C and V_{DD} = 12V*
2 *Valid for 92232LSE-AAA-xxx and 92232LUA-AAA-xxx versions*
3 *Valid for 92232LVA-AAA-xxx version*
4 *Guaranteed by design and verified by characterization, not production tested*
5 *The Power-On Time represents the time from reaching V_{DD} = 2.7V to the first refresh of the output*
6 *Power-On Slew Rate is not critical for the proper device start-up.*
7 *Maximum switching frequency corresponds to the maximum frequency of the applied magnetic field which is detected without loss of pulses*
8 *R_{PU} and V_{PU} are respectively the external pull-up resistor and pull-up power supply*
9 *Activated output with 1 mT overdrive*
10 *If the Output is in Current Limitation longer than t_{CLON} the Output is switched off in high-impedance state. The Output returns back in active state at next reaching of B_{OP} or after t_{CLOFF} time interval*
11 *T_{PROT} and T_{REL} are the corresponding junction temperature values*

9 Magnetic Specifications

DC Operating Parameters V_{DD} = 2.7V to 24V, T_A = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ[1]	Max	Units
Latch Threshold Programming Range[2, 3]	B_{LTH}	V_{DD}=12V, T_A=25°C	±0.4		±80	mT
Switch Operating Point Programming Range[3, 4]	B_{OP}	V_{DD}=12V, T_A=25°C	±1.5		±66	mT
Proportional Hysteresis Ratio Programming[5, 6]	HYS_{RATIO}	V_{DD}=12V, T_A=25°C	0.1		0.55	-
Fixed Hysteresis Value 0[7, 8]	B_{FHYS0}		-	0	-	mT
Fixed Hysteresis Value 1[7, 8]	B_{FHYS1}		-	1	-	mT
Fixed Hysteresis Value 2[7, 8]	B_{FHYS2}		-	1.2	-	mT
Fixed Hysteresis Value 3[7, 8]	B_{FHYS3}		-	1.4	-	mT
Fixed Hysteresis Value 4[7, 8]	B_{FHYS4}		-	1.8	-	mT
Fixed Hysteresis Value 5[7, 8]	B_{FHYS5}		-	2.2	-	mT
Latch Sensor Magnetic Offset[9]	B_{OFFSET}	T_A=25°C	-0.5		0.5	mT
		T_A=-40°C to 150°C	-0.9		0.9	mT
Temperature Coefficient[10]	TC	Flat		0		ppm/°C
		SmCo		-400		
		NdFeB		-1100		
		Hard Ferrite		-2000		
Factory Programmed B_{OP}, Switch[11]	B_{OP}	V_{DD}=12V, T_A=25°C, target 28mT	26	28	30	mT
Factory Programmed B_{RP}, Switch[11]	B_{RP}	V_{DD}=12V, T_A=25°C, target 28mT, HYS_{RATIO}=0.25	19	21	23	mT
Factory Programmed B_{OP}, Latch[12]	B_{OP}	V_{DD}=12V, T_A=25°C, target 12mT	10	12	14	mT
Factory Programmed B_{RP}, Latch[12]	B_{RP}	V_{DD}=12V, T_A=25°C, target -12mT	-14	-12	-10	mT

Table 3: Magnetic Specifications

The hysteresis is programmable for each B_{OP} with a fixed value or proportional (ratiometric) value to B_{OP}:

1) Ratio metric hysteresis example: B_{OP} = 10mT → 4.5mT ≤ B_{RP} ≤ 9mT
2) Fixed hysteresis example: B_{OP} = 10mT → 7.8mT ≤ B_{RP} ≤ 9mT

---

1 The typical values are defined at T_A = 25°C and V_{DD} = 12V.

2 For Latch sensor B_{LTH}=(B_{OP}-B_{RP})/2. The Latch programming step is typically between 0.7% and 1.5% of the programmed B_{LTH} value for |B_{LTH}|≥1.2mT and 0.018mT for |B_{LTH}|≤ 1.2mT.

3 Guaranteed by design and verified by characterization. The programming ranges for BLTH and BOP include some margin for process deviations.

4 For Switch sensor the B_{OP} programming step is typically between 0.7% and 1.5% of the programmed B_{OP} value for |B_{OP}|≥ 4.8mT and 0.072mT for |B_{OP}|≤ 4.8mT.

5 For Switch sensor with proportional hysteresis HYS_{RATIO}=B_{HYS}/B_{OP}. The HYS_{RATIO} programming step is 0.05.

6 The given min/max limits are typical values.

7 For Switch sensor with fixed hysteresis value

8 Guaranteed by design and verified by characterization.

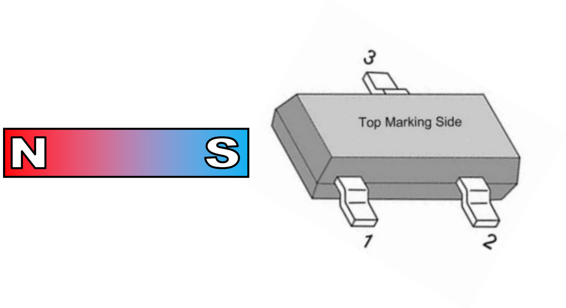
9 For Latch sensor offset is defined as B_{OFFSET} = (B_{OP}+B_{RP})/2.

10 The temperature Coefficient is calculated using following formula:

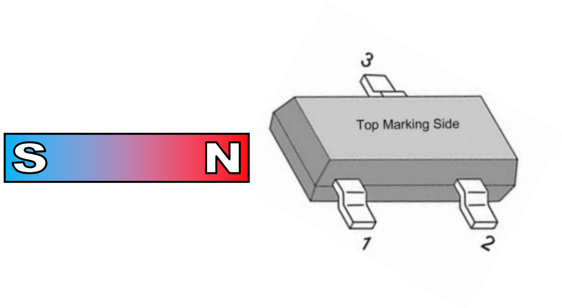
$$TC = \frac{(B_{OPT2} - B_{RPT2}) - (B_{OPT1} - B_{RPT1})}{(B_{OPT1} - B_{RPT1}) * (T_2 - T_1)} * 10^6, \text{ppm/}^{\circ}\text{C}; T_1 = -40^{\circ}\text{C}; T_2 = 150^{\circ}\text{C}$$

11 Valid for 92232LSE-AAA-0xx, 92232LUA-AAA-0xx, 92232LVA-AAA-0xx versions

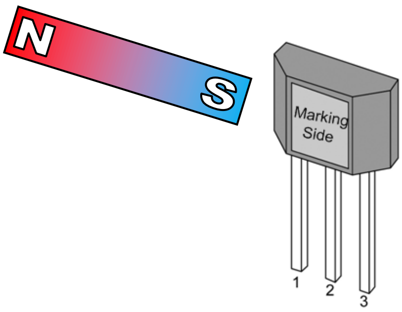
12 Valid for 92232LUA-AAA-2xx



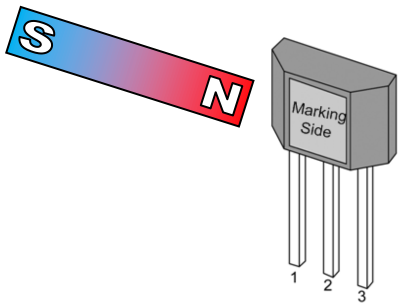
X-axis Sensitive
South Active Pole



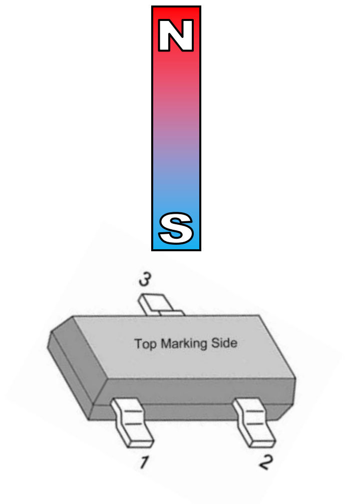
X-axis Sensitive
North Active Pole



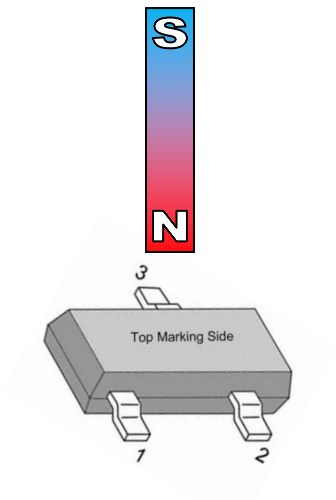
X-axis Sensitive
South Active Pole



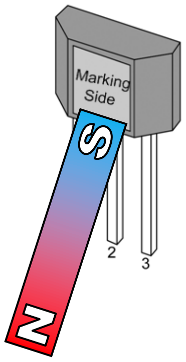
X-axis Sensitive
North Active Pole



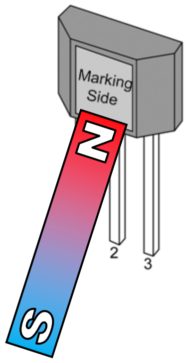
Z-axis Sensitive
South Active Pole



Z-axis Sensitive
North Active Pole



Z-axis Sensitive
South Active Pole



Z-axis Sensitive
North Active Pole

10 Magnetic Behaviour

10.1 Latch Sensor

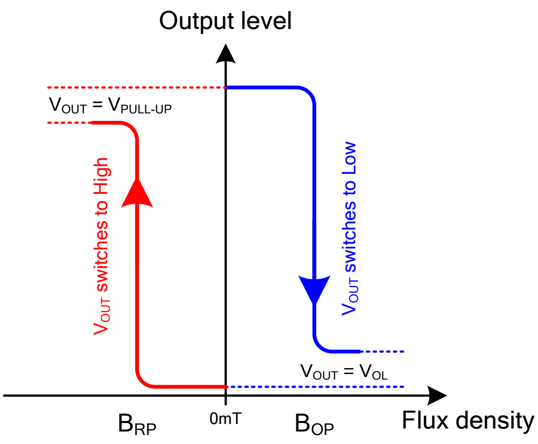


Fig.1 – South Active Pole

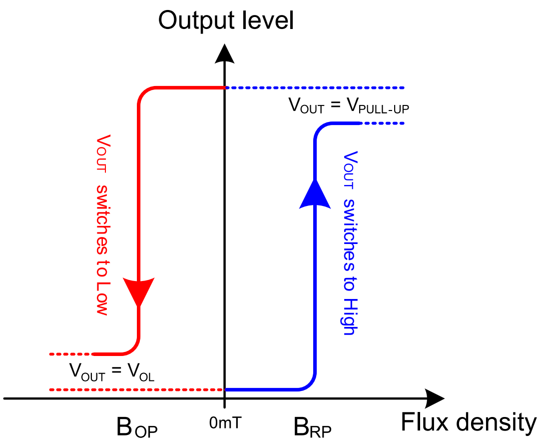


Fig.2 – North Active Pole

10.2 Switch Sensor

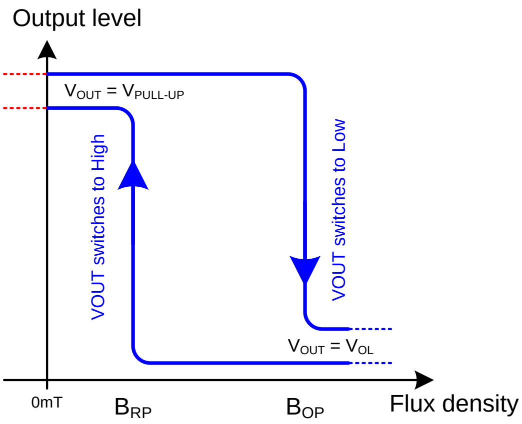


Fig.3 – Direct South Active Pole

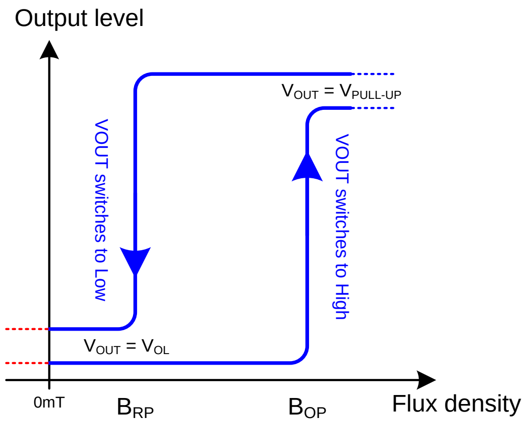


Fig.4 – Inverted South Active Pole

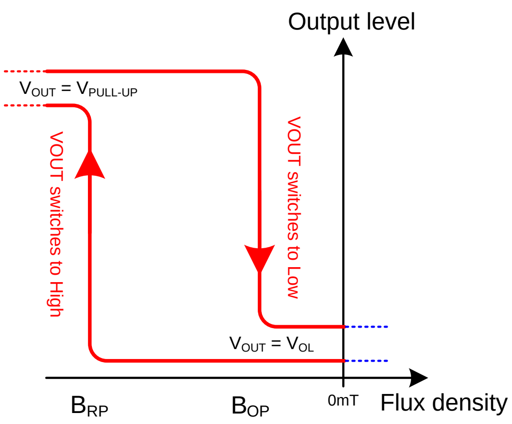


Fig.5 – Direct North Active Pole

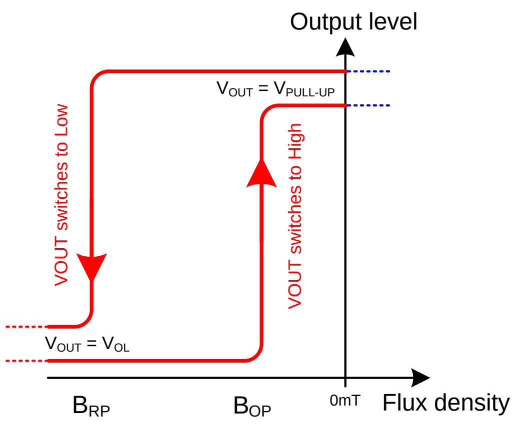
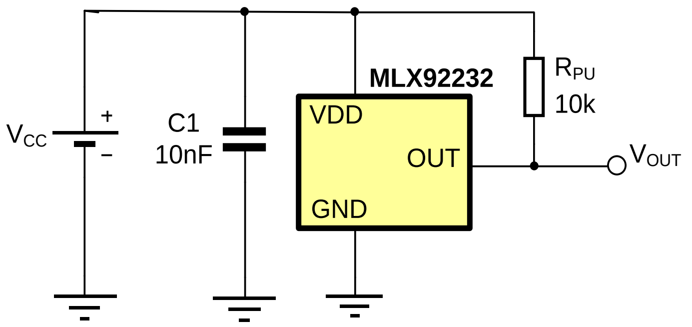


Fig.6 – Inverted North Active Pole

11 Application Information

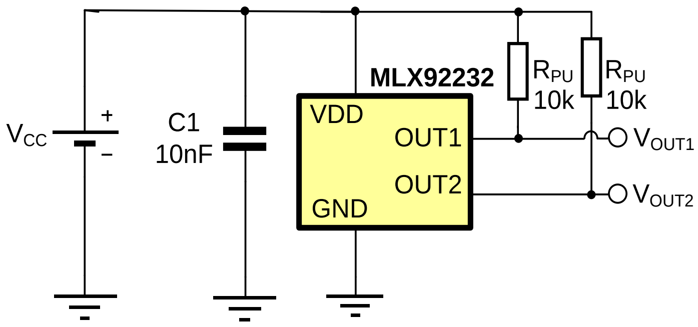
11.1 Typical Three-Wire Application Circuit

11.1.1 92232LSE-AAA-xxx, 92232LUA-AAA-xxx



Notes:
1. For proper operation, a 10nF to 100nF bypass capacitor should be placed as close as possible to the V_{DD} and ground pin.
2. The pull-up resistor R_{PU} value should be chosen in to limit the current through the output pin below the maximum allowed continuous current for the device.
3. A capacitor connected to the output is not needed, because the output slope is generated internally.

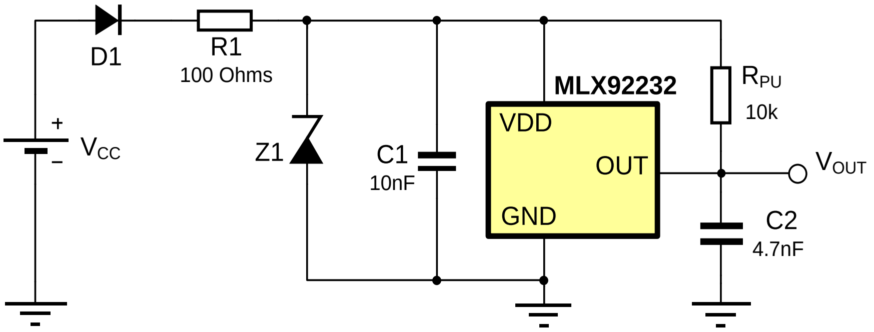
11.1.2 92232LVA-AAA-xxx



Notes:
1. For proper operation, a 10nF to 100nF bypass capacitor should be placed as close as possible to the V_{DD} and ground pin.
2. The pull-up resistors R_{PU} values should be chosen in to limit the current through the output pin below the maximum allowed continuous current for the device.
3. A capacitors connected to the outputs are not needed, because the output slope is generated internally.

11.2 Automotive and Harsh, Noisy Environments Three-Wire Circuit

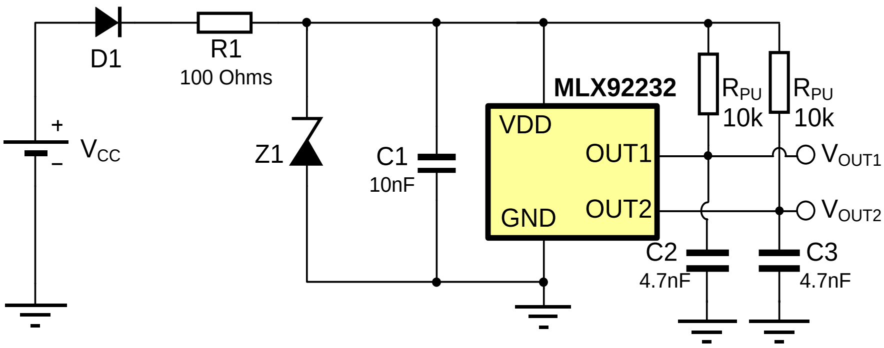
11.2.1 92232LSE-AAA-xxx, 92232LUA-AAA-xxx



Notes:
1. For proper operation, a 10nF to 100nF bypass capacitor should be placed as close as possible to the V_{DD} and ground pin.
2. The device could tolerate negative voltage down to -24V, so if negative transients over supply line V_{PEAK}< -30V are expected, usage of the diode D1 is recommended. Otherwise only R1 is sufficient.
When selecting the resistor R1, three points are important:
- the resistor has to limit I_{DD}/I_{DDREV} to 50mA maximum
- the resistor has to withstand the power dissipated in both over voltage conditions (V_{R1}^2/R1)
- the resulting device supply voltage V_{DD} has to be higher than V_{DD} min ($V_{DD} = V_{CC} – R1.I_{DD}$)

3. The device could tolerate positive supply voltage up to +27V (until the maximum power dissipation is not exceeded), so if positive transients over supply line with V_{PEAK}> 32V are expected, usage a zener diode Z1 is recommended. The R1-Z1 network should be sized to limit the voltage over the device below the maximum allowed.

11.2.2 92232LVA-AAA-xxx



Notes:
1. For proper operation, a 10nF to 100nF bypass capacitor should be placed as close as possible to the V_{DD} and ground pin.
2. The device could tolerate negative voltage down to -24V, so if negative transients over supply line V_{PEAK}< -30V are expected, usage of the diode D1 is recommended. Otherwise only R1 is sufficient.
When selecting the resistor R1, three points are important:
- the resistor has to limit I_{DD}/I_{DDREV} to 50mA maximum
- the resistor has to withstand the power dissipated in both over voltage conditions (V_{R1}^2/R1)
- the resulting device supply voltage V_{DD} has to be higher than V_{DD} min ($V_{DD} = V_{CC} – R1.I_{DD}$)

3. The device could tolerate positive supply voltage up to +27V (until the maximum power dissipation is not exceeded), so if positive transients over supply line with V_{PEAK}> 32V are expected, usage a zener diode Z1 is recommended. The R1-Z1 network should be sized to limit the voltage over the device below the maximum allowed.

12 Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD's (Surface Mount Devices)

- IPC/JEDEC J-STD-020
  Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices
  (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113
  Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing
  (reflow profiles according to table 2)

Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20
  Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15
  Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD's (Through Hole Devices)

- EN60749-15
  Resistance to soldering temperature for through-hole mounted devices

Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EIA/JEDEC JESD22-B102 and EN60749-21
  Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

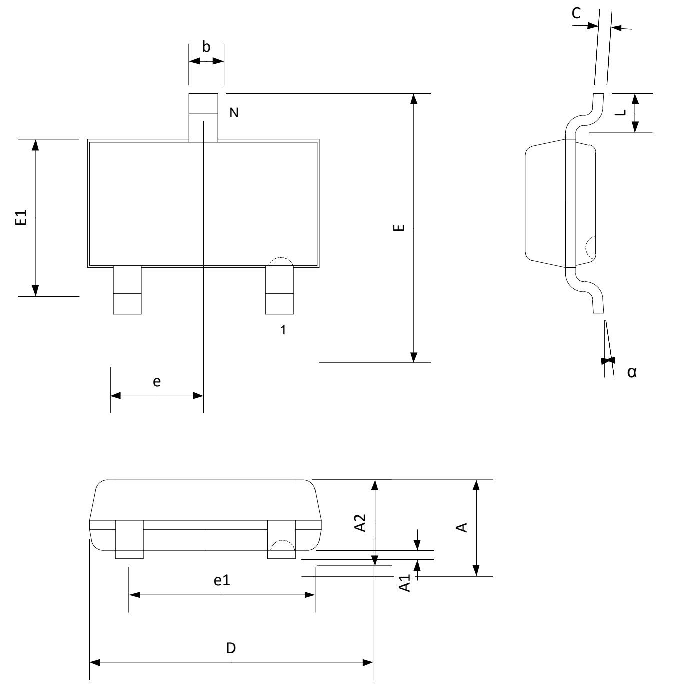
Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: http://www.melexis.com/quality.aspx

13 ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

14 Package Information

14.1 SE (TSOT-3L) Package Information



Notes:

1. All dimensions are in millimeters
2. Outermost plastic extreme width does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.15mm per side.
3. Outermost plastic extreme length does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.25mm per side.
4. The lead width dimension does not include dambar protrusion. Allowable dambar protrusion shall be 0.07mm total in excess of the lead width dimension at maximum material condition.
5. Dimension is the length of terminal for soldering to a substrate.
6. Formed lead shall be planar with respect to one another with 0.076mm at seating plane.

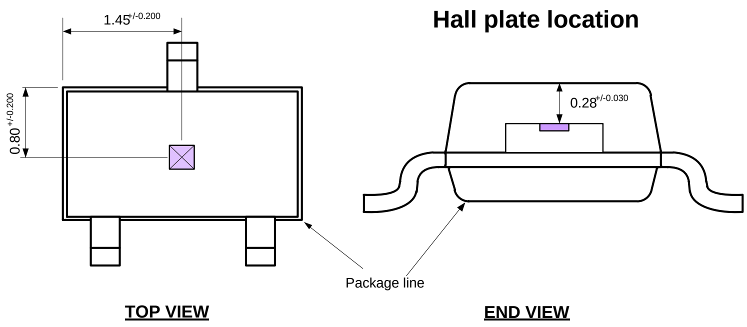
Marking:

Top mark: 31ww ==> ww; assembly week

IMC version: 33ww ==> ww; assembly week

Bottom mark: YLLL ==> Y; last digit of year LLL= last 3 digits of lotnr

Hall plate location



Notes:

1. All dimensions are in millimeters
2. XY Hall plate position tolerances do not include the mold flashes and protrusions described in the package drawing

This table in mm

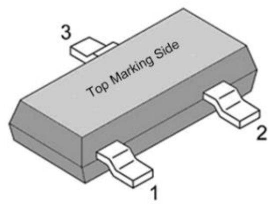
N		A	A1	A2	D	E	E1	L	b	c	e	e1	α
3	min	–	0.025	0.85	2.80	2.60	1.50	0.30	0.30	0.10	0.95 BSC	1.90 BSC	0°
	max	1.00	0.10	0.90	3.00	3.00	1.70	0.50	0.45	0.20			8°

Notes:

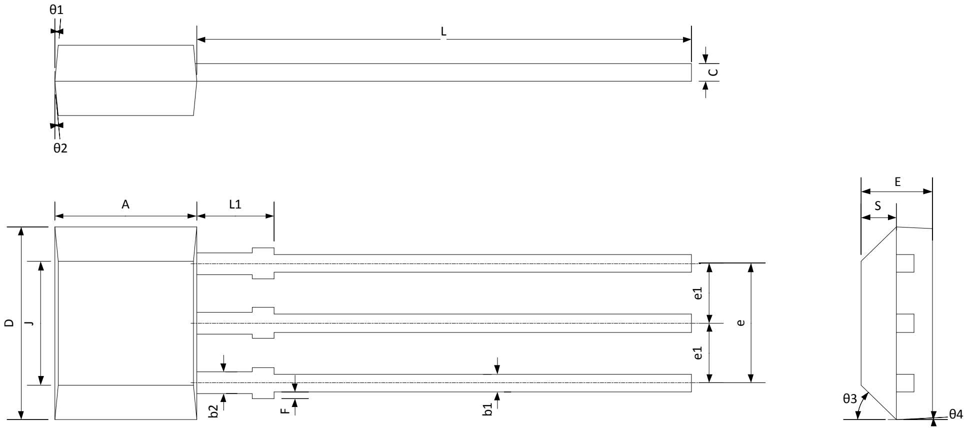
1. Dimension “D” and “E1” do not include mold flash or protrusions. Mold flash or protrusion shall not exceed 0.15mm on “D” and 0.25mm on “E” per side.
2. Dimension “b” does not include dambar protrusion.

SE Pin №	Name	Type	Function
1	VDD	Supply	Supply Voltage pin
2	OUT	Output	Open Drain output
3	GND	Ground	Ground pin

Table 4: SE Package pinout



14.2 UA (TO92-3L) Package Information



Notes:

1. All dimensions are in millimeters
2. Package dimension exclusive molding flash.
3. The end flash shall not exceed 0.127 mm on the top side.

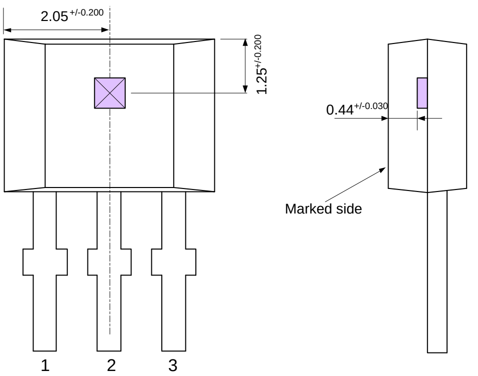
Marking:

1st Line : 31WW → WW - calendar week

2nd Line : YLLL: → Y - last digit of year

LLL- Lot nr (3 digits)

Hall plate location



Notes:

1. All dimensions are in millimeters

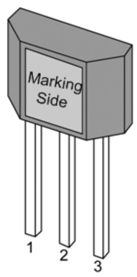
This table in mm

	A	D	E	F	J	L	L1	S	b1	b2	c	e	e1
min	2.80	3.90	1.40	0.00	2.51	14.0	0.90	0.63	0.35	0.43	0.35	2.51	1.24
max	3.20	4.30	1.60	0.20	2.72	15.0	1.10	0.84	0.44	0.52	0.44	2.57	1.30

	θ1	θ2	θ3	θ4
min / max	7° REF	7° REF	45° REF	7° REF

Notes:

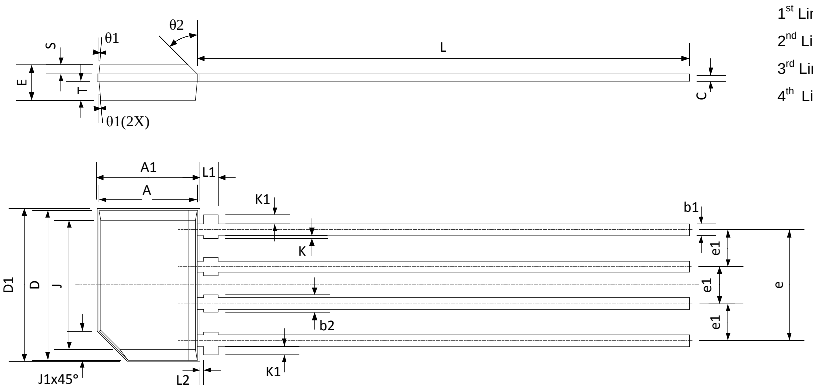
1. Mold flashes and protrusion are not included.
2. Gate burrs shall not exceed 0.127um on the top side.



UA Pin №	Name	Type	Function
1	VDD	Supply	Supply Voltage pin
2	GND	Ground	Ground pin
3	OUT	Output	Open Drain output

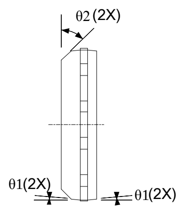
Table 5: UA Package pinout

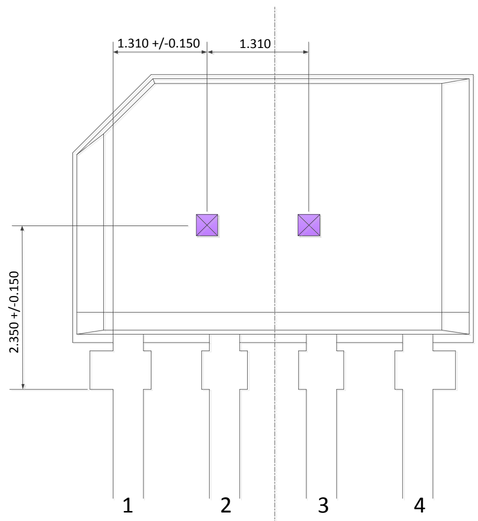
14.3 VA (SIP 4L) Package Information



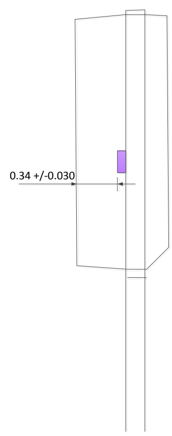
Notes:
1. All dimensions are in millimeters
2. Package dimension exclusive molding flash.

Marking:
1st Line : 30_30 → Mark as is
2nd Line : LLLLLLL: → 1st die lot number
3rd Line : TTTTTTT: → 2nd die lot number
4th Line : YYWW: → YY= year ;
WW= calendar week number





HALL PLATES LOCATION



Notes:
1. All dimensions are in millimeters

This table in mm

Type		A	A1	D	D1	E	J	J1	K	K1	L	L1	S	T
VA	min	3.30	3.63	5.08	5.33	1.10	4.10	1.00 REF	0.00	0.25	17.5	–	0.24	0.61
	max	3.46	3.79	5.24	5.43	1.20	4.50		0.15	0.35	18.5	1.00	0.29	0.66

		b1	b2	c	e	e1	θ1	θ2
VA	min	0.35	0.40	0.18	3.76	1.22	7° REF	45° REF
	max	0.48	0.60	0.34	3.86	1.32		

Notes:
1. Dimension “A” and “D” do not include mold flash protrusions & gate burrs
2. Dimension “A1” does not include gate burrs, but includes mold flash and interlead flash.
3. Dimension “D1” includes mold flash at both ends.
4. Gate burrs shall not exceed 0.15mm measured from end of mold flash (flange).

VA Pin №	Name	Type	Function
1	OUT1	Output 1st die	Open Drain output
2	VDD	Supply	Supply Voltage pin
3	GND	Ground	Ground pin
4	OUT2	Output 2nd die	Open Drain output

Table 6: VA (SIP 4L) single in line package pinout

15 Contact

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For additional information, please contact our Direct Sales team and get help for your specific needs:

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