MMBV609LT1

Preferred Device

Silicon Tuning Diode

This device is designed for FM tuning, general frequency control and tuning, or any top-of-the-line application requiring back-to-back diode configuration for minimum signal distortion and detuning. This device is supplied in the SOT-23 plastic package for high volume, pick and place assembly requirements.

Features

- High Figure of Merit Q = 450 (Typ) @ $V_R = 3.0$ Vdc, f = 50 MHz
- Guaranteed Capacitance Range
- Dual Diodes Save Space and Reduce Cost
- Surface Mount Package
- Available in 8 mm Tape and Reel
- Monolithic Chip Provides Improved Matching
- Hyper Abrupt Junction Process Provides High Tuning Ratio
- Pb–Free Package is Available

MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	20	Vdc
Forward Current	١ _F	100	mAdc
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P _D	225 1.8	mW mW/°C
Junction Temperature	TJ	+125	°C
Storage Temperature Range	T _{stg}	-55 to +125	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



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DUAL VOLTAGE VARIABLE CAPACITANCE DIODE





MARKING DIAGRAM



5L = Specific Device Code

M = Date Code*

= Pb–Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBV609LT1	SOT-23	3,000 / Tape & Reel
MMBV609LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel

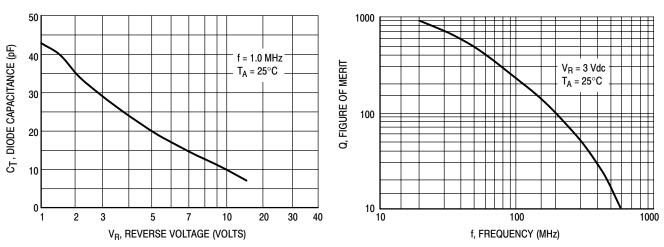
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MMBV609LT1

ELECTRICAL CHARACTERISTICS (EACH DIODE) ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage $(I_R = 10 \ \mu Adc)$	V _{(BR)R}	20	-	-	Vdc
Reverse Voltage Leakage Current (V _R = 15 Vdc)	۱ _R	-	-	10	nAdc
Diode Capacitance (V _R = 3.0 Vdc, f = 1.0 MHz)	CT	26	-	32	pF
Capacitance Ratio C3/C8 (f = 1.0 MHz)	C _R	1.8	-	2.4	-
Figure of Merit ($V_R = 3.0 \text{ Vdc}, f = 50 \text{ MHz}$)	Q	250	450	-	-



TYPICAL CHARACTERISTICS

Figure 1. Diode Capacitance

Figure 2. Figure of Merit

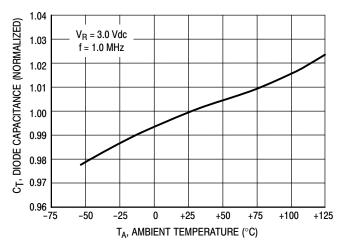
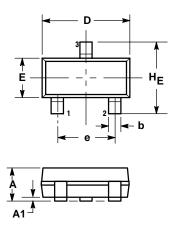
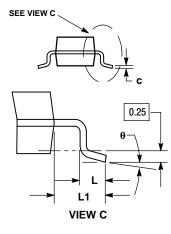


Figure 3. Diode Capacitance

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AN**





NOTES

- DIMENSIONING AND TOLERANCING PER ANSI 1. Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 2. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF 3.
- BASE MATERIAL. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08. 4.

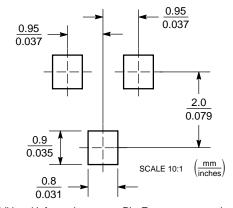
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2 10	2 40	2 64	0.083	0.094	0 104

STYLE 9:

PIN 1. ANODE 2. ANODE

3 CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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