

MN1020019 / 0219 / 0419 / 0819

Type		MN1020019 / 0219 / 0419 / 0819	
ROM (×8-Bit / ×16-Bit)		External / 16 K / 32 K / 64 K (External Memory Expandable)	
RAM (×8-Bit / ×16-Bit)		3 K / 1 K / 2 K / 3 K (External Memory Expandable)	
Minimum Instruction Execution Time		MN1020019 / 0219 / 0419 / 0819 : 100 ns (at 4.5 V to 5.5 V, 20 MHz) MN1020019 / 0219 / 0419 : 200 ns (at 2.7 V to 3.3 V, 10 MHz)	
Interrupts		<ul style="list-style-type: none"> • RESET • Watchdog • Timer Counter 0 to 3 • External 0 to 3 • NMI • Serial ch 0, 1 Transmission • Serial ch 0, 1 Reception • A/D Conversion finish 	
Timer Counter		<p>Timer Counter 0,1 : 8-Bit × 1 (Timer Output, Event Count)</p> <p>Clock Source . . . 1/(1 to 256) of System Clock, External Clock</p> <p>Interrupt Source . . . Underflow of Timer Counter 0, 1</p> <p>Timer Counter 2 : 8-Bit × 1 (Timer Output, Event Count, UART Baud Rate Generator, Synchronous Serial Clock Generator, DRAM Refresh Timing Generator)</p> <p>Clock Source . . . 1/(1 to 256) of System Clock, External Clock</p> <p>Interrupt Source . . . Underflow of Timer Counter 2</p> <p>Timer Counter 3 : 8-Bit × 1 (Timer Output, Event Count, UART Baud Rate Generator, Synchronous Serial Clock Generator)</p> <p>Clock Source . . . 1/(1 to 256) of System Clock, External Clock</p> <p>Interrupt Source . . . Underflow of Timer Counter 3</p> <p style="text-align: center;">Connectable Timer Counter 0, 1</p>	
Serial Interface		<p>Serial 0 : 7,8-Bit × 1 (Common use with UART, Transfer direction of MSB/LSB selectable)</p> <p>Clock Source . . . 1/8 of Timer Counter 2, 1/8 of Timer Counter 3, External Clock</p> <p>Serial 1 : 7,8-Bit × 1 (Common use with UART, Transfer direction of MSB/LSB selectable)</p> <p>Clock Source . . . 1/8 of Timer Counter 2, 1/8 of Timer Counter 3, External Clock</p> <p>UART × 2 (Common use with Serial 0, 1)</p>	
I/O Pins	I/O	51	<ul style="list-style-type: none"> • Common use 51 (35 by-bit, 16 by-byte) (MN1020219 / 0419 / 0819), 21 (All individual bit control) (MN1020019)
	Input	1	<ul style="list-style-type: none"> • Common use 1
A/D Inputs		8-Bit × 4ch (with S/H)	
Notes		DRAM Refresh Controller	
Package		QFH064-P-1414B	

Electrical Characteristics

A/D, D/A Characteristics

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Relative Error		VDD = 5 V, VSS = 0 V			±3	LSB
A/D Conversion Time		fosc = 20 MHz	4	8		μs
Analog Input Voltage	VIA		VSS		VDD	V

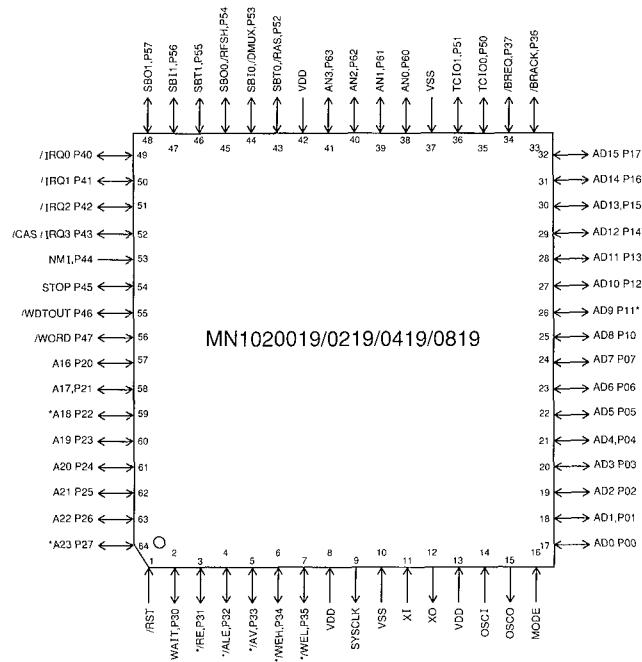
(Ta = 25 °C, VDD = 5.0 V, VSS = 0 V)

Support Tool

In-Circuit Emulator

PX-ICE102L00 + PX-PRB1020019

Pin Assignment



QFH064-P-1414B

* Use of these ports are disabled for MN1020019