

□ MN103SE3 Series

Type	MN103SE3K	MN103SFE3K
Internal ROM type	Mask ROM	FLASH
ROM (byte)	256K	
RAM (byte)	8K	
Package (Lead-free)	QFP100-P-1818B	
Minimum Instruction Execution Time	16.7 ns (4.5 V to 5.5 V)	

■ Interrupts

9 external interrupts
 47 internal interrupts: Watchdog timer. Timer. Serial I/F. PWM. A/D conversion finish. System error

■ Timer Counter

8-bit timer × 12
 Timer 0 to 7, 14 to 17 Interval timer. Event count. Cascading connectable
 16-bit timer × 6
 Timer 8 to 10Interval timer. Event count. PWM output. Double buffer
 Timer 11Interval timer. Event count. PWM output (6 pins simultaneous output are available). Double buffer
 Timer 12, 13Interval timer. Double buffer. Start synchronized with 3-phase PWM are available
 Watchdog timer × 1

■ Serial interface

UART (full duplex) /Synchronous interfaces selective × 3
 Serial 0, 17-bit, 8-bit transmission
 Serial 21-bit to 8-bit transmission (synchronous). 2 and 3 channel type selectable (synchronous)

■ I/O Pins

I/O 81 : Common use × 81

■ A/D converter

10-bit × 3 unit. 20 channels
 Minimum conversion time: 1.0 μs
 Simultaneous conversion of 3 series are available
 Conversion start synchronized with 3-phase PWM or timer 12, 13 are available

■ Motor Control PWM

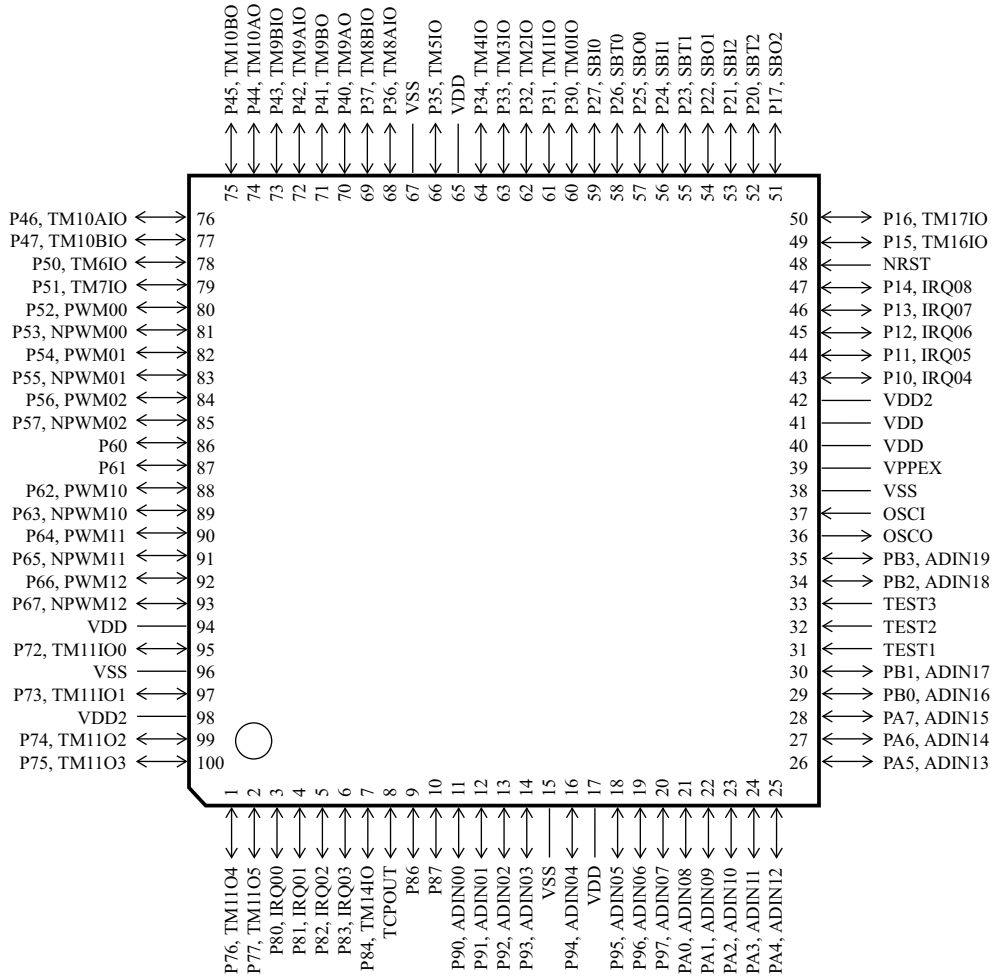
16-bit 3-phase PWM × 2
 Triangular waveform or jigsaw waveform. Dead time setup. Double buffer. Output polar switching is available. PWM output pin protect function

■ Electrical Characteristics (A/D converter characteristics)

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Non-linear error		10-bit			±3	LSB
A/D conversion time	TAD		1.0			μs
Analog input voltage	VIA		VSS		VDD	V

Ta = 25 °C. VDD = 5.0 V. VSS = 0 V

■ Pin Assignment
QFP100-P-1818B



Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).
Consult our sales staff in advance for information on the following applications:
 - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
 - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
 - Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.