❑ MN158418

Type			**MN158418**
ROM (x8-bit)			4K
RAM (x4-bit)			256
Number of Instructions			94
Minimum Instruction Execution Time			**2.0μs (at 4.5 to 5.5V, 4MHz)**
Interrupts			• RESET • External • Timer • Serial
Timer Counter			**Timer Counter : 8-bit x 1** Clock Source .....1/2, 1/8, 1/32, 1/128 of System Clock Interrupt Source .....Overflow of Timer Counter
Serial Interface			**Serial : 8-bit x 1** (Synchronous Type) Clock Source .....System Clock, $\overline{SBT}$ Pin Input
I/O Pins	**High Voltage I/O**	**12**	• Pch Open-drain (Breakdown Voltage -30V) : 12 • Specified pull-down Resistor available : 12 (Mask Option)
	Input	**7**	• Common use : 4 • Specified pull-up Resistor available : 2 • Specified pull-down Resistor available : 2 • Specified pull-up Resistor available : 3 (Software Programmable)
	High Voltage Output	**29**	• Pch Open-drain available (Breakdown Voltage -30V) : 29 • Specified pull-down Resistor available : 29 (MaskOption)
	Output	**8**	
FLP			41
Notes			Crystal / CR Oscillation Selectable
Package			SDIP064-P-0750

Electrical Characteristics

Supply Current

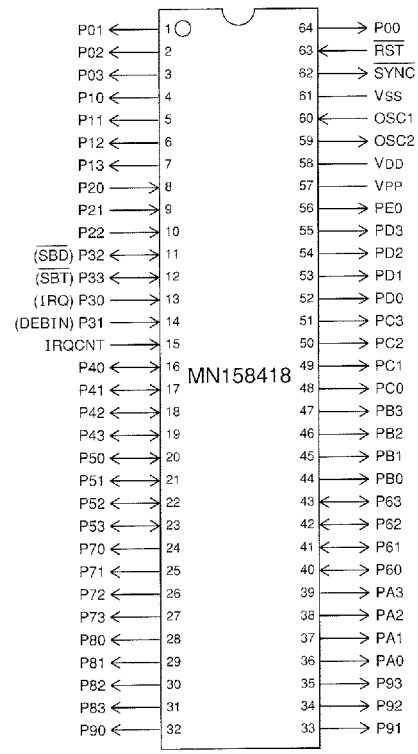
Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc=4.0MHz		2.5	6.0	mA
Supply Current at STOP	IDD3	VPP=0V			20	μA
Supply Current at HALT	IDD2	fosc=4.0MHz, VPP=0V		0.6	1.5	mA

(Ta= −10 to +70°C, VDD=5.0V, VSS=0V)

Support Tool

In-Circuit Emulator	PX-ICE1500 + PX-PRB158418
Piggyback	Use **EP158418** as piggy in SDIP064-P-0750 package.

Pin Assignment



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