□ MN1876476

| Туре | MN1876476 | | | | |
|----------------------------|---|--|--|--|--|
| ROM (×8-Bit) | 64 K | | | | |
| RAM (×8-Bit) | ×8-Bit) 928 | | | | |
| Minimum Instruction Execut | ion Time 0.5 μs at 2/3 frequency dividing (at 4.5 V to 5.5 V, 12 MHz) | | | | |
| Interrupts | • RESET • External 0 • External 1 • External 2 • External 3 • Timer 0 • Timer 1 • Timer 2 • I²C • Serial • Remote Control • Line 21 • MOSD • COSD | | | | |
| Timer Counter | Timer Counter 0 : 8-Bit × 1 Clock Source 1/1, 1/4, 1/16, 1/64 of System Clock Interrupt Source . Overflow of Timer Counter 0 | | | | |
| | Timer Counter 1 : 8-Bit × 1 Clock Source 1/2, 1/16, 1/64, 1/256, 1/512 of System Clock Interrupt Source Overflow of Timer Counter 1 | | | | |
| | Time Base Counter Clock Source 1/4096 of System Clock Interrupt Source 1/1, 1/2, 1/4, 1/8 of Timer Counter 2 Watchdog Counter for Clock (Clock function) AC Counter | | | | |
| | Watchdog Counter for Clock (Clock function) AC Counter | | | | |
| Serial Interface | Serial 0 : 8-Bit × 1 (Transmission/Reception of variable bit length, Fransfer direction of MSB/LSB selectable, Clock Polarity selectable, Start Condition function) Clock Source System Clock I ² C × 1 (Two bus line system) | | | | |
| I/O Pins I/O | 36 • Common use 28 | | | | |
| Input High Voltage 0 | utput 7 • Nch Open-Drain (Breakdown Voltage 12 V) 7 | | | | |
| A/D Inputs | 5/7-Bit × 10ch (without S/H) | | | | |
| PWM | 14-Bit \times 1ch (Repetition Cycle 16 μ s, at 12 MHz), 8-Bit \times 8ch (Repetition Cycle 32 μ s, at 12 MHz), 7-Bit \times 1ch (Repetition Cycle 16 μ s, at 12 MHz) | | | | |
| Special Ports | Hsync Detection, Remote Control Reception | | | | |
| CRTC | Double OSD built-in (Menu OSD 12 × 18512 letters, Caption OSD 12 × 26176 letters) | | | | |
| Notes | Remote Control Data Detection Circuit built-in | | | | |
| Package | SDIP064-P-0750 | | | | |
| Electrical Characteristics | A/D Converter Characteristics | | | | |
| | C Compa | | | | |

| Parameter | Symbol | Condition | min | Limit Ng | max | Unit |
|----------------------|--------|---------------|------|-------------|-----|------|
| A/D Conversion Time | TAD | fosc = 12 MHz | 9/12 | | | μs |
| Analog Input Voltage | VAD | | VSS | | VDD | V |

(Ta = -20 °C to +70 °C, VDD = 5.0 V, VSS = 0 V)

Support Tool

| In-Circuit Emulator | PX-ICE1870 / 80 + PX-PRB1876476 | | |
|---------------------|------------------------------------|------------------------------------|--|
| EPROM built-in Type | Туре | MN18P76476 | |
| | ROM (× 8-Bit) | 64 K | |
| | RAM (× 8-Bit) | 928 | |
| | Minimum Instruction Execution Time | 0 5 μs (at 4 5 V to 5 5 V, 12 MHz) | |
| | Package | SDIP064-P-0750 | |

Pin Assignment



※ P04 • IRQ0 pin

| TYPE A | Stand-By function is availabe | Input pin |
|--------|------------------------------------|-----------|
| TYPE B | Stand-By function is not available | I/O pin |

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