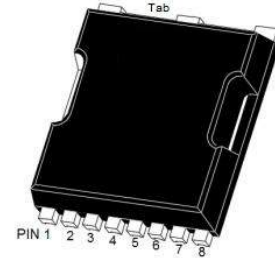


■ PRODUCT CHARACTERISTICS

VDSS	200V
$R_{DS(on)Typ}(V_{GS}@=10V)$	6mΩ
ID	114A

■ FEATURES

Surface-mounted package
Advanced trench cell design
Super trench

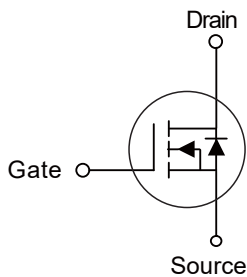


TOLL-8L

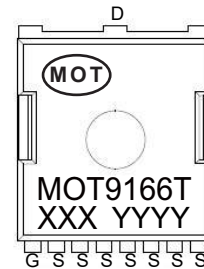
■ APPLICATIONS

High power system inverter
Light electric vehicles
BMS
Drones

■ SYMBOL



Pin configuration (Top view)



XXX = Lot Number
YYYY = Year Week

Marking

Order information

Device	Package	Shipping
MOT9166T/TR	TOLL-8L	4000/Tape&Reel

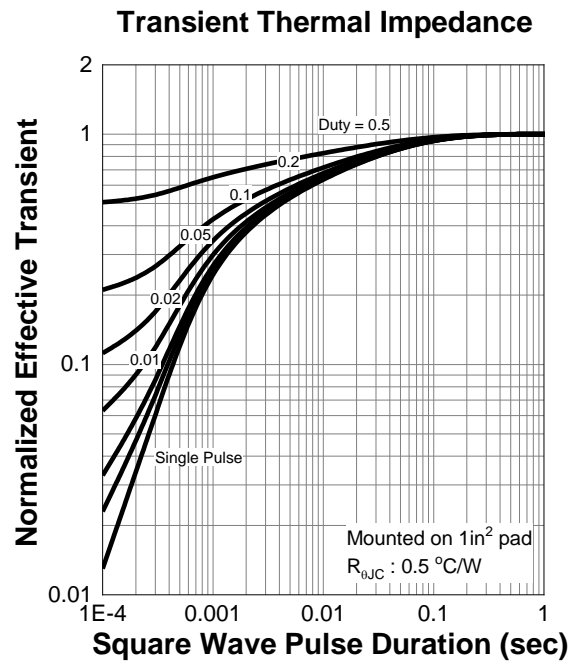
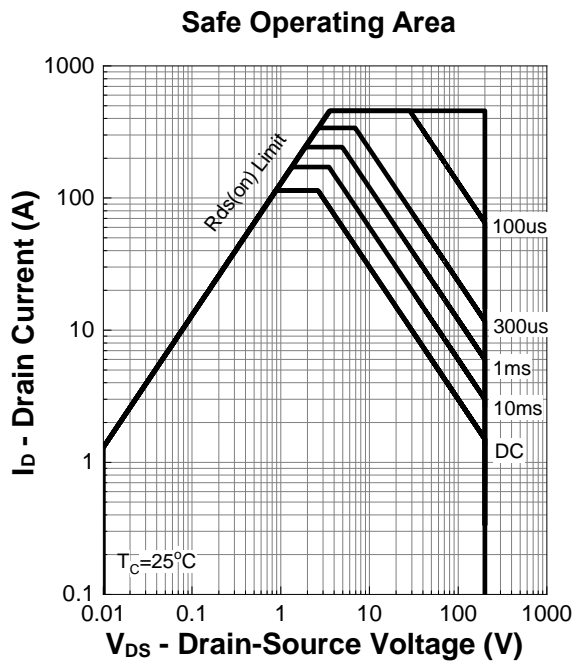
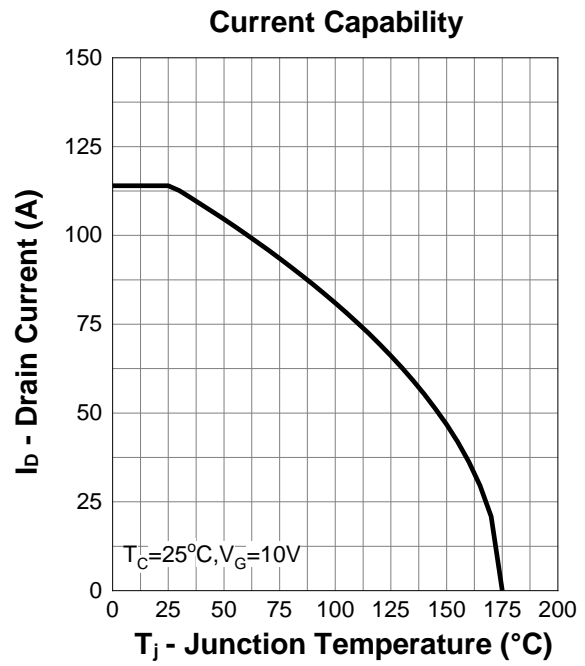
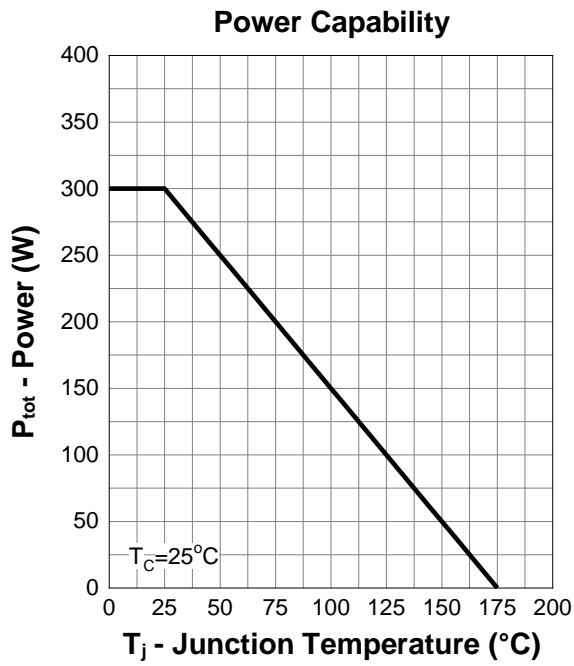
■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Max	Unit
Drain-Source Voltage	V_{DS}	$T_C = 25^\circ\text{C}$	200	-	V
Gate-Source Voltage	V_{GS}	$T_C = 25^\circ\text{C}$	-	± 20	V
Drain Current (DC)	I_D	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	114	A
		$T_C = 100^\circ\text{C}, V_{GS} = 10\text{ V}$	-	81	A
Drain Current (Pulsed)	I_{DM}	$T_C = 25^\circ\text{C}, V_{GS} = 10\text{ V}$	-	456	A
Drain power dissipation	P_{tot}	$T_C = 25^\circ\text{C}$	-	300	W
Storage Temperature	T_{stg}		-55	175	$^\circ\text{C}$
Junction Temperature	T_J		-	175	$^\circ\text{C}$
Continuous-Source Current	I_S	$T_C = 25^\circ\text{C}$	-	114	A
Single Pulsed Avalanche Energy	E_{AS}	$V_{DD}=50\text{V}, L=1.0\text{mH}$	-	1800	mJ
Thermal Resistance- Junction to Ambie	$R_{\theta JA}$		-	40	$^\circ\text{C}/\text{W}$
Thermal Resistance- Junction to Case	$R_{\theta JC}$		-	0.5	

■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise specified)

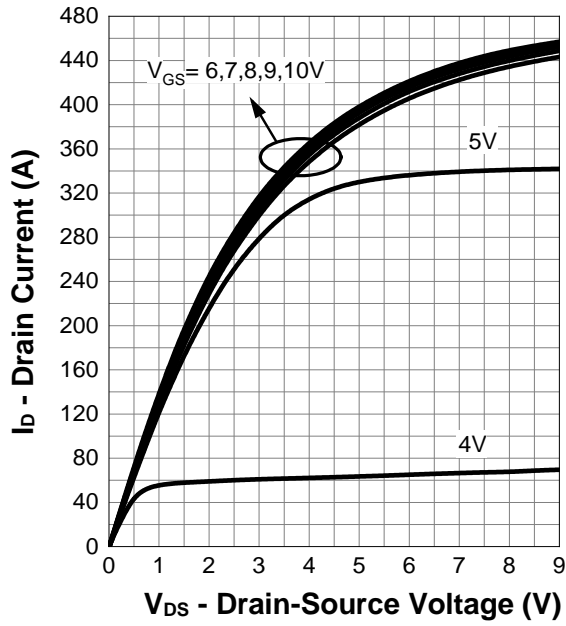
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0 V, I _{DS} = 250 μA	200	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _{DS} = 250 μA	2	-	4	V
Drain Leakage Current	I _{DSS}	V _{DS} = 160 V, V _{GS} = 0 V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = 0 V, V _{GS} = ± 20 V	-	-	±100	nA
On-State Resistance	R _{DS(ON)}	V _{GS} = 10 V, I _{DS} = 50 A	-	6.0	7.1	mΩ
		V _{GS} = 6 V, I _{DS} = 30 A	-	6.4	7.8	
Diode Characteristics						
Diode Forward Voltage	V _{SD}	I _{SD} = 50 A, V _{GS} = 0 V	-	-	1.3	V
Reverse Recovery Time	t _{rr}	I _{DS} = 50 A, V _{GS} = 0 V dI _{SD} /dt = 100 A/μs	-	148	-	nS
Reverse Recovery Charge	Q _{rr}		-	805	-	nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 100V Frequency = 1 MHz	-	11636	-	pF
Output Capacitance	C _{oss}		-	570	-	
Reverse Transfer Capacitance	C _{rss}		-	60	-	
Turn-on Delay Time	t _{d(on)}	V _{DS} = 100V, V _{GEN} = 10 V, R _G = 3.9 Ω, R _L = 2 Ω, I _{DS} = 50 A	-	25	-	nS
Turn-on Rise Time	t _r		-	70	-	
Turn-off Delay Time	t _{d(off)}		-	135	-	
Turn-off Fall Time	t _f		-	84	-	
Gate Charge Characteristics						
Total Gate Charge	Q _g	V _{DS} = 100V, V _{GS} = 10 V, I _{DS} = 50 A	-	186	-	nC
Gate-Source Charge	Q _{gs}		-	58	-	
Gate-Drain Charge	Q _{gd}		-	31	-	

■ TYPICAL CHARACTERISTICS

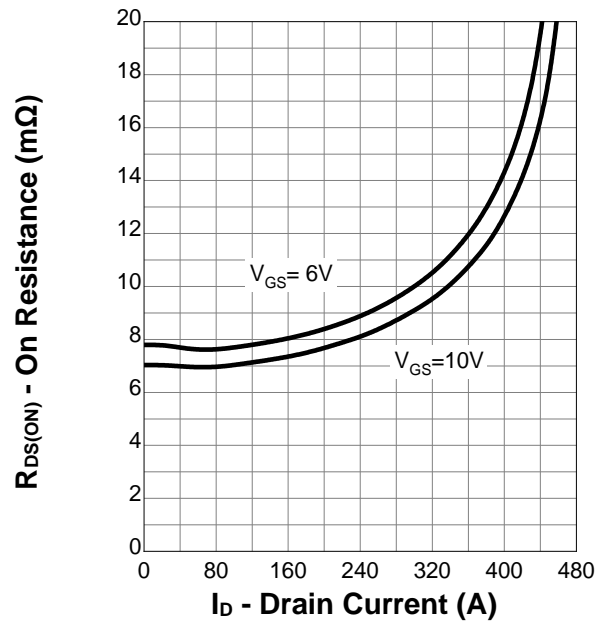


■ TYPICAL CHARACTERISTICS(Cont.)

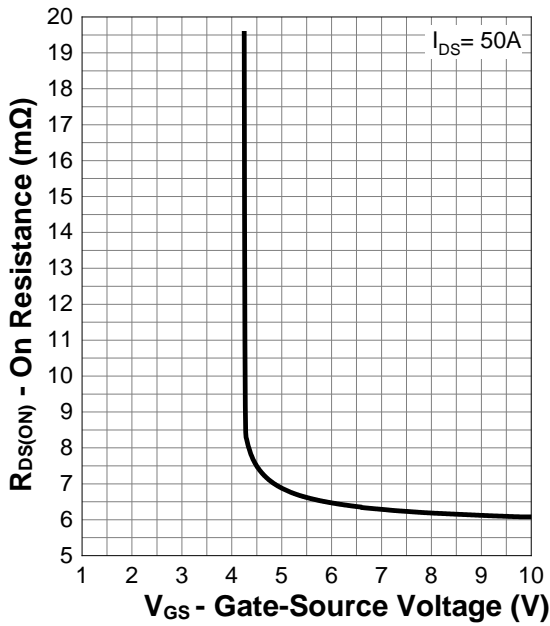
Output Characteristics



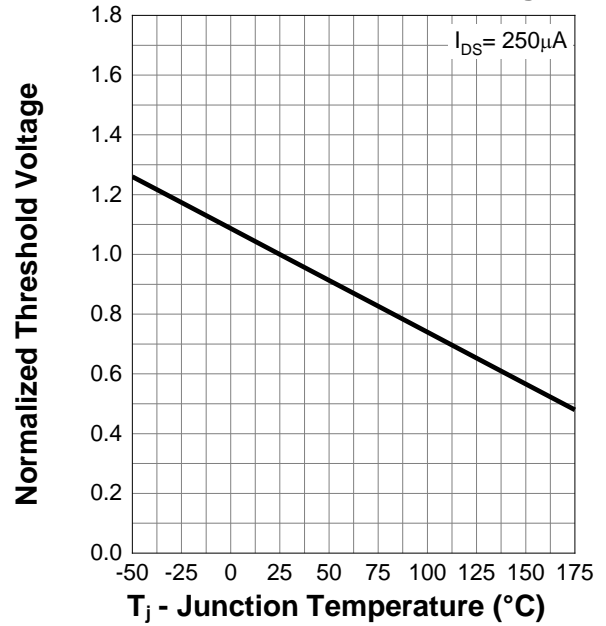
On Resistance



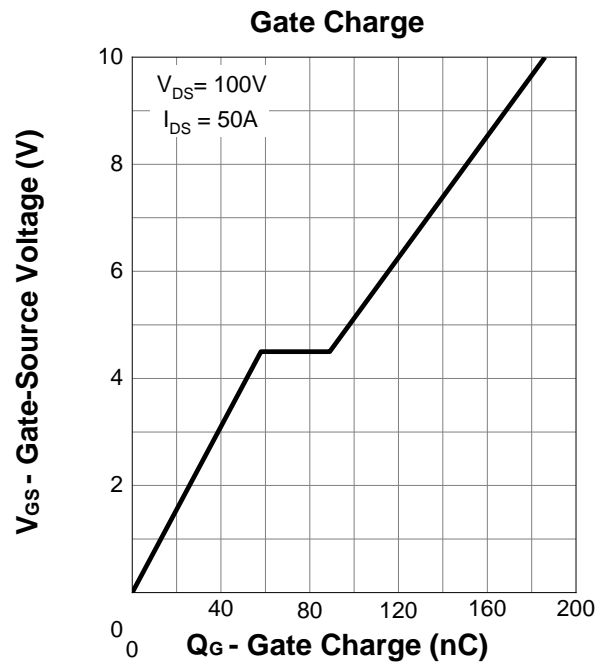
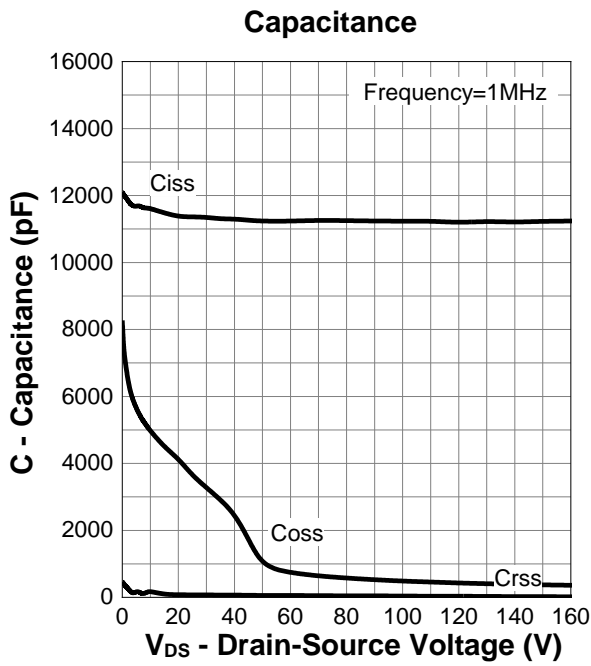
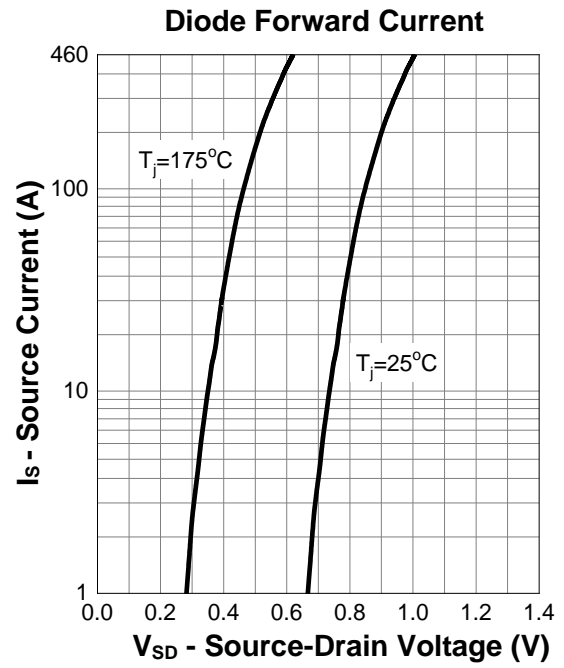
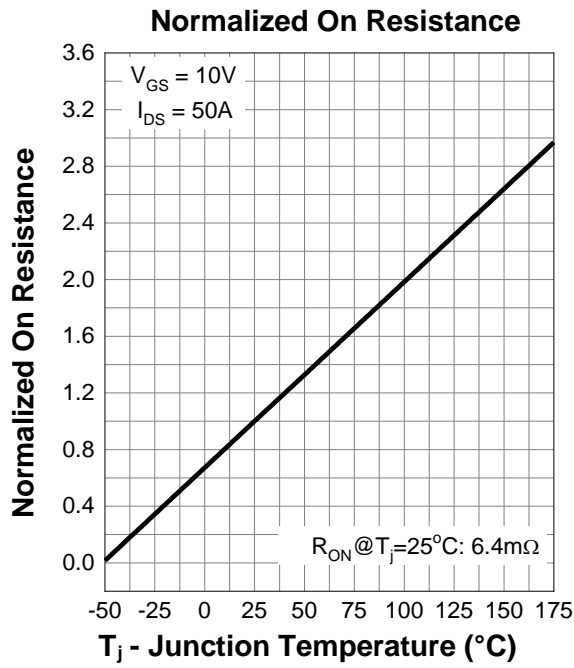
Transfer Characteristics



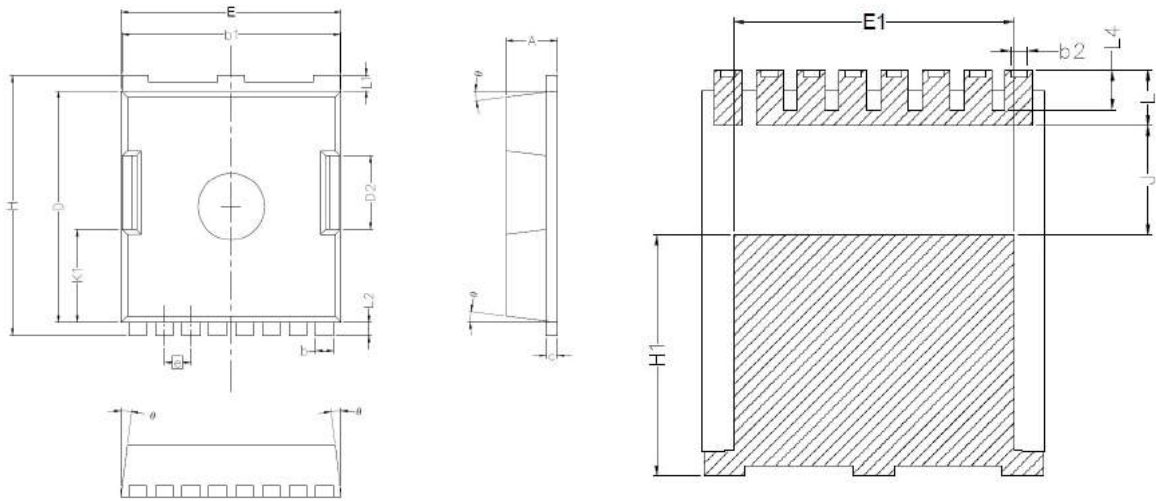
Normalized Threshold Voltage



■ TYPICAL CHARACTERISTICS(Cont.)



■ TOLL-8L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°

- The information contained hSurface-mounted package Advnced terch cell design Super trencherein is subject to change without notice.
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