Low Voltage 1:18 Clock Distribution Chip

The MPC942 is a 1:18 low voltage clock distribution chip with 2.5V or 3.3V LVCMOS output capabilities. The device is offered in two versions; the MPC942C has an LVCMOS input clock while the MPC942P has a LVPECL input clock. The 18 outputs are 2.5V or 3.3V LVCMOS compatible and feature the drive strength to drive 50Ω series or parallel terminated transmission lines. With output–to–output skews of 200ps, the MPC942 is ideal as a clock distribution chip for the most demanding of synchronous systems. The 2.5V outputs also make the device ideal for supplying clocks for a high performance Pentium II[™] microprocessor based design.

- LVCMOS/LVTTL Clock Input
- 2.5V LVCMOS Outputs for Pentium II Microprocessor Support
- 150ps Maximum Targeted Output-to-Output Skew
- Maximum Output Frequency of 250MHz @ 3.3 $\ensuremath{\mathsf{V_{CC}}}$
- 32–Lead TQFP Packaging
- Single 3.3V or 2.5V Supply

With a low output impedance ($\approx 12\Omega$), in both the HIGH and LOW logic states, the output buffers of the MPC942 are ideal for driving series terminated transmission lines. With an output impedance of 12Ω the MPC942 can drive two series terminated transmission lines from each output. This capability gives the MPC942 an effective fanout of 1:36. The MPC942 provides enough copies of low skew clocks for most high performance synchronous systems.

The LVCMOS/LVTTL input of the MPC942C provides a more standard LVCMOS interface. The OE pins will place the outputs into a high impedance state. The OE pin has an internal pullup resistor.

The MPC942 is a single supply device. The V_{CC} power pins require either 2.5V or 3.3V. The 32–lead TQFP package was chosen to optimize performance, board space and cost of the device. The 32–lead TQFP has a 7x7mm body size with a conservative 0.8mm pin spacing.

MPC942C



FA SUFFIX 32–LEAD TQFP PACKAGE CASE 873A–02

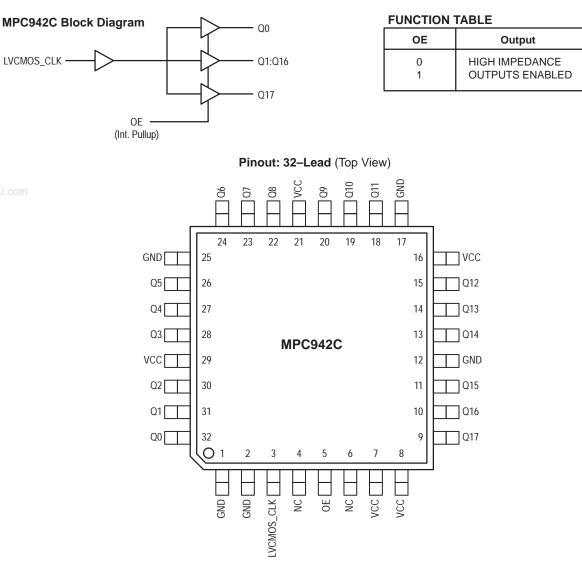
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Freescale Semiconductor, Inc.

MPC942C

LOGIC DIAGRAM



ABSOLUTE MAXIMUM RATING

| Symbol | Parameter | Min | Max | Unit |
|-------------------|---------------------------|------|-----------------------|------|
| V _{CC} | Supply Voltage | -0.3 | 3.6 | V |
| VI | Input Voltage | -0.3 | V _{CC} + 0.3 | V |
| I _{IN} | Input Current | | ±20 | mA |
| T _{Stor} | Storage Temperature Range | -40 | 125 | °C |

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MOTOROLA

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MPC942C

DC CHARACTERISTICS (T_A = 0° to 70°C, V_{CCI} = 2.5V \pm 5%, V_{CCO} = 2.5V \pm 5%)

| | | | | , | | |
|------------------|----------------------------------|-----|-----|------|------|--------------------------|
| Symbol | Characteristic | Min | Тур | Max | Unit | Condition |
| VIH | Input HIGH Voltage | 2.0 | | VCCI | V | |
| VIL | Input LOW Voltage | | | 0.8 | V | |
| V _{OH} | Output HIGH Voltage | 2.0 | | | V | I _{OH} = -16 mA |
| VOL | Output LOW Voltage | | | 0.5 | V | I _{OL} = 16 mA |
| I _{IN} | Input Current | | | ±200 | μA | |
| C _{IN} | Input Capacitance | | 4.0 | | pF | |
| C _{PD} | Power Dissipation Capacitance | | 14 | | pF | Per Output |
| ZOUT | Output Impedance | | 12 | | Ω | |
| et lcc om | Maximum Quiescent Supply Current | | 0.5 | | mA | |

AC CHARACTERISTICS (T_A = 0° to 70°C, V_{CCI} = 2.5V ±5%, V_{CCO} = 2.5V ±5%)

| Symbol | Characteristic | Min | Тур | Max | Unit | Condition |
|---------------------------------|-----------------------|-----|-----|-----|------|------------|
| F _{max} | Maximum Frequency | | | 200 | MHz | |
| ^t PLH | Propagation Delay | 1.5 | | 2.8 | ns | |
| ^t sk(o) | Output-to-Output Skew | | | 200 | ps | |
| ^t sk(pr) | Part-to-Part Skew | | | 1.3 | ns | Notes 1, 2 |
| ^t sk(pr) | Part-to-Part Skew | | | 600 | ps | Notes 1, 3 |
| dt | Duty Cycle | 45 | | 55 | % | |
| t _r , t _f | Output Rise/Fall Time | 0.2 | | 1.0 | ns | |

DC CHARACTERISTICS (T_A = 0° to 70°C, V_{CCI} = 3.3V \pm 5%, V_{CCO} = 3.3V \pm 5%)

| Symbol | Characteristic | Min | Тур | Max | Unit | Condition |
|-----------------|----------------------------------|-----|-----|------|------|--------------------------|
| VIH | Input HIGH Voltage | 2.4 | | VCCI | V | |
| VIL | Input LOW Voltage | | | 0.8 | V | |
| VOH | Output HIGH Voltage | 2.4 | | | V | I _{OH} = -20 mA |
| VOL | Output LOW Voltage | | | 0.5 | V | I _{OL} = 20 mA |
| IIN | Input Current | | | ±200 | μΑ | |
| C _{IN} | Input Capacitance | | 4.0 | | pF | |
| C _{PD} | Power Dissipation Capacitance | | 14 | | pF | Per Output |
| ZOUT | Output Impedance | | 12 | | Ω | |
| ICC | Maximum Quiescent Supply Current | | 0.5 | | mA | |

AC CHARACTERISTICS (T_A = 0° to 70°C, V_{CCI} = 3.3V \pm 5%, V_{CCO} = 3.3V \pm 5%)

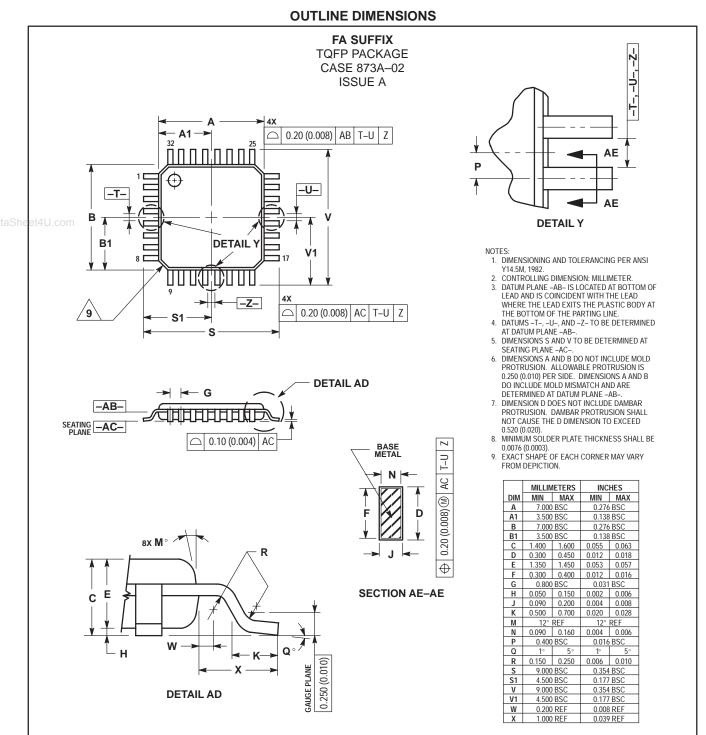
| Symbol | Characteristic | Min | Тур | Max | Unit | Condition |
|---------------------------------|-----------------------|-----|-----|-----|------|------------|
| F _{max} | Maximum Frequency | | | 250 | MHz | |
| ^t PLH | Propagation Delay | 1.3 | | 2.3 | ns | Note 1 |
| ^t sk(o) | Output-to-Output Skew | | | 200 | ps | |
| ^t sk(pr) | Part-to-Part Skew | | | 1.0 | ns | Notes 1, 2 |
| ^t sk(pr) | Part-to-Part Skew | | | 500 | ps | Notes 1, 3 |
| dt | Duty Cycle | 45 | | 55 | % | |
| t _r , t _f | Output Rise/Fall Time | 0.2 | | 1.0 | ns | |

1. Tested using standard input levels, production tested @ 133 MHz.

2. Across temperature and voltage ranges, includes output skew.

3. For a specific temperature and voltage, includes output skew.

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