

MPF4117,A MPF4118,A MPF4119,A

CASE 29-02, STYLE 5
TO-92 (TO-226AA)

JFET
DC AMPLIFIER TRANSISTOR
N-CHANNEL — DEPLETION

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	- 40	Vdc
Drain-Gate Voltage	V_{DG}	- 40	Vdc
Gate Current	I_G	50	mAdc
Total Device Dissipation (at $T_A = 25^\circ\text{C}$ Derate above 25°C)	P_D	300 2.0	mW mW/°C
Storage Channel Temperature Range	T_{stg}	- 65 to + 125	°C

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Gate-Source Breakdown Voltage ($V_{DS} = 0, I_G = -1.0 \mu\text{Adc}$)	$V_{(BR)GSS}$	- 40	—	Vdc	
Gate Reverse Current ($V_{GS} = 20 \text{ Vdc}, V_{DS} = 0$)	I_{GSS}	—	- 10	pAdc	
		MPF4117, 4118, 4119 MPF4117A, 4118A, 4119A	—		- 1.0
($V_{GS} = 20 \text{ Vdc}, V_{DS} = 0, T_A = 125^\circ\text{C}$)	I_{GSS}	—	- 25	nAdc	
		MPF4117, 4118, 4119 MPF4117A, 4118A, 4119A	—		- 2.5
Gate Source Cutoff Voltage ($V_{DS} = 10 \text{ Vdc}, I_D = 1.0 \text{ nAdc}$)	$V_{GS(off)}$	- 0.6 - 1.0 - 2.0	- 1.8 - 3.0 - 6.0	Vdc	
	MPF4117,A MPF4118,A MPF4119,A				
ON CHARACTERISTICS					
Zero-Gate-Voltage Drain Current(1) ($V_{DS} = 10 \text{ Vdc}, V_{GS} = 0$)	I_{DSS}	0.03 0.08 0.20	0.09 0.24 0.60	mAdc	
	MPF4117,A MPF4118,A MPF4119,A				
SMALL-SIGNAL CHARACTERISTICS					
Input Capacitance ($V_{DS} = 10 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz}$)	C_{iss}	—	3.0	pF	
Reverse Transfer Capacitance ($V_{DS} = 10 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz}$)	C_{rss}	—	1.5	pF	
Common-Source Forward Transconductance ($V_{DS} = 10 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ kHz}$)	g_{fs}	70	210	μmhos	
		MPF4117,A MPF4118,A MPF4119,A	80 100		250 330
Common-Source Output Conductance ($V_{DS} = 10 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ kHz}$)	g_{os}	—	3.0	μmhos	
		MPF4117,A MPF4118,A MPF4119,A	— — —		5.0 10

(1) I_{DSS} is measured during a 2.0 ms interval 100 ms after power is applied.

MPF4117,A, MPF4118,A, MPF4119,A

FIGURE 1 — TRANSFER CHARACTERISTICS

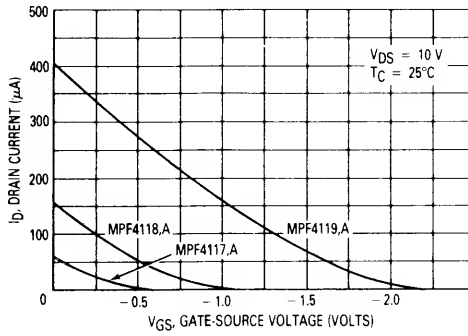


FIGURE 2 — TRANSCONDUCTANCE CHARACTERISTICS

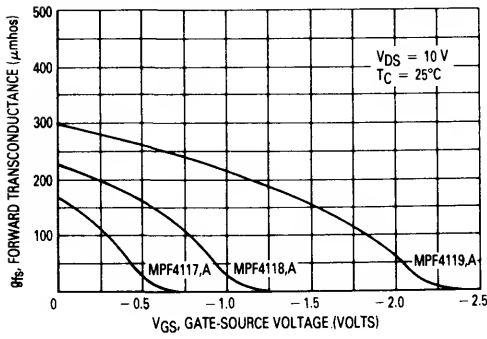


FIGURE 3 — CAPACITANCE versus DRAIN-SOURCE VOLTAGE

