

OKI Semiconductor

PEDR26T51203L-02-04

Issue Date: Mar. 26, 2004

MR26T51203L

Preliminary

32M-Word × 16-Bit or 64M-Word × 8-Bit P2ROM

FEATURES

- 33,554,432-word × 16-bit/67,108,864-word × 8-bit electrically switchable configuration
- 2.7 V to 3.6 V power supply
- Access time 120 ns MAX
- Operating current 40 mA MAX (5MHz)
- Standby current 20 μA MAX
- Input/Output TTL compatible
- Three-state output

PACKAGES

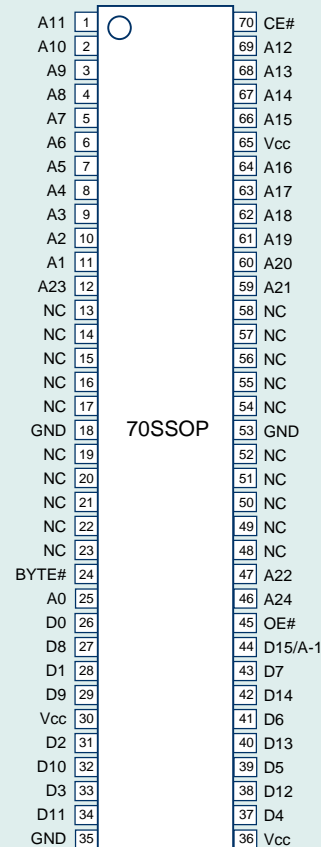
- MR26T51203L-xxxMB
70-pin plastic SSOP (P-SSOP70-P-500-0.80-K-MC)

P2ROM ADVANCED TECHNOLOGY

P2ROM stands for Production Programmed ROM. This exclusive Oki technology utilizes factory test equipment for programming the customers code into the P2ROM prior to final production testing. Advancements in this technology allows production costs to be equivalent to MASKROM and has many advantages and added benefits over the other non-volatile technologies, which include the following;

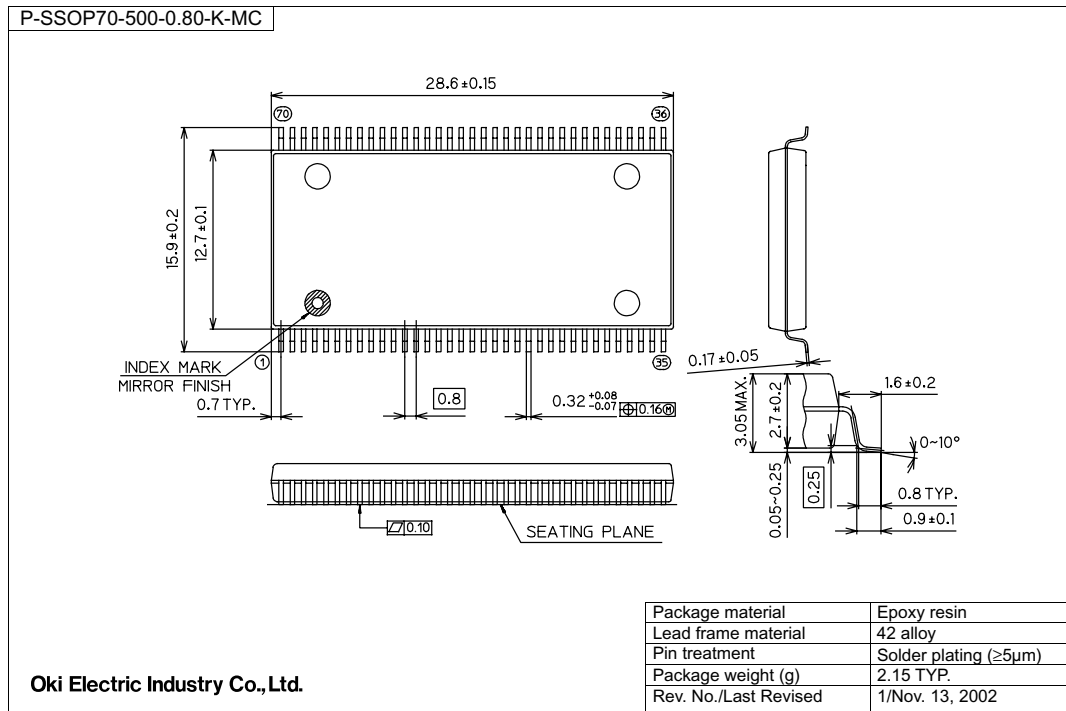
- **Short lead time**, since the P2ROM is programmed at the final stage of the production process, a large P2ROM inventory "bank system" of un-programmed packaged products are maintained to provide an aggressive lead-time and minimize liability as a custom product.
- **No mask charge**, since P2ROMs do not utilize a custom mask for storing customer code, no mask charges apply.
- **No additional programming charge**, unlike Flash and OTP that require additional programming and handling costs, the P2ROM already has the code loaded at the factory with minimal effect on the production throughput. The cost is included in the unit price.
- **Custom Marking is** available at no additional charge.

PIN CONFIGURATION (TOP VIEW)



PACKAGE DIMENSIONS

(Unit: mm)



Notes for Mounting the Surface Mount Type Package

The surface mount type packages are very susceptible to heat in reflow mounting and humidity absorbed in storage.

Therefore, before you perform reflow mounting, contact Oki's responsible sales person for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

REVISION HISTORY

Document No.	Date	Page		Description
		Previous Edition	Current Edition	
PEDR26T51203L-02-01	Mar. 2003	–	–	
PEDR26T51203L-02-02	Apr. 2003	1	1	Change operating current to 40mA from 70mA, standby current to 20μA from 10μA.
PEDR26T51203L-02-03	Jun. 17, 2003	1	1, 3	Add MR26T51203L-xxxMB.
PEDR26T51203L-02-04	Mar. 26, 2004	1, 2	1	Delete MR26T51203L-xxxTM.

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1. The information contained herein can change without notice owing to product and/or technical improvements. Before using the product, please make sure that the information being referred to is up-to-date.
2. The outline of action and examples for application circuits described herein have been chosen as an explanation for the standard action and performance of the product. When planning to use the product, please ensure that the external conditions are reflected in the actual circuit, assembly, and program designs.
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