# The RF Line Microwave Pulse Power Transistors

Designed for Class B and C common base amplifier applications in short pulse TACAN, IFF, and DME transmitters.

- Guaranteed Performance @ 1090 MHz, 50 Vdc Output Power = 90 Watts Peak Minimum Gain = 8.4 dB
- 100% Tested for Load Mismatch at All Phase Angles with 10:1 VSWR
- Industry Standard Package
- Nitride Passivated
- Gold Metallized for Long Life and Resistance to Metal Migration
- Internal Input Matching for Broadband Operation
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

#### MAXIMUM RATINGS

| Rating  | Symbol           | Value       | Unit          |
|---|------------------|-------------|---------------|
| Collector-Base Voltage  | VCBO             | 70          | Vdc           |
| Emitter–Base Voltage  | VEBO             | 4.0         | Vdc           |
| Collector–Current — Peak (1)  | IC               | 6.0         | Adc           |
| Total Device Dissipation @ T <sub>C</sub> = 25°C (1) (2)<br>Derate above 25°C | PD               | 290<br>1.66 | Watts<br>W/°C |
| Storage Temperature Range   | T <sub>stg</sub> | -65 to +150 | °C            |

## THERMAL CHARACTERISTICS

| Characteristic                           | Symbol           | Мах | Unit |
|--|------------------|-----|------|
| Thermal Resistance, Junction to Case (3) | R <sub>θJC</sub> | 0.6 | °C/W |

#### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

| Characteristic   | Symbol                | Min | Тур | Max | Unit |
|--|-----------------------|-----|-----|-----|------|
| OFF CHARACTERISTICS  | •                     |     |     | •   |      |
| Collector–Emitter Breakdown Voltage $(I_C = 25 \text{ mAdc}, V_{BE} = 0)$    | V <sub>(BR)</sub> CES | 70  | —   | _   | Vdc  |
| Collector–Base Breakdown Voltage $(I_C = 25 \text{ mAdc}, I_E = 0)$          | V(BR)CBO              | 70  | —   | _   | Vdc  |
| Emitter–Base Breakdown Voltage $(I_E = 5.0 \text{ mAdc}, I_C = 0)$           | V(BR)EBO              | 4.0 | —   | _   | Vdc  |
| Collector Cutoff Current<br>( $V_{CB} = 50 \text{ Vdc}, I_E = 0$ )           | ICBO                  | _   | —   | 5.0 | mAdc |
| ON CHARACTERISTICS   |                       |     |     |     |      |
| DC Current Gain (4)<br>(I <sub>C</sub> = 2.5 Adc, V <sub>CE</sub> = 5.0 Vdc) | hFE                   | 10  | 30  | _   | —    |

NOTES:

1. Pulse Width = 10  $\mu$ s, Duty Cycle = 1%.

2. These devices are designed for RF operation. The total device dissipation rating applies only when the device is operated as RF amplifiers.

3. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.

4. 80  $\mu s$  Pulse on Tektronix 576 or equivalent.

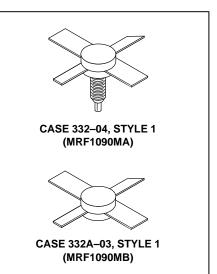
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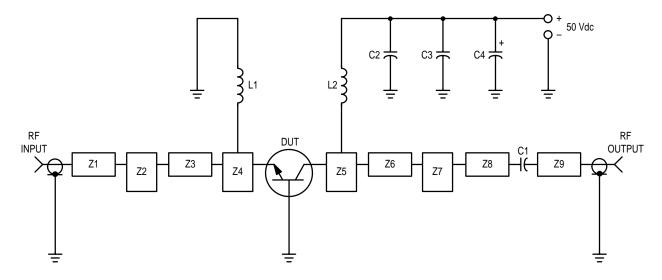


90 W PEAK, 960–1215 MHz MICROWAVE POWER TRANSISTORS NPN SILICON



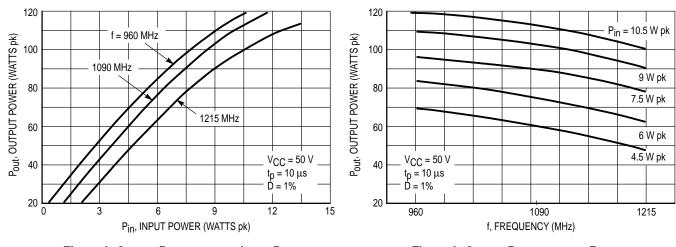
## **ELECTRICAL CHARACTERISTICS** — continued ( $T_C = 25^{\circ}C$ unless otherwise noted)

| Characteristic   | Symbol          | Min                            | Тур  | Max | eei4Unit <sup>om</sup> |
|--|-----------------|--------------------------------|------|-----|------------------------|
| DYNAMIC CHARACTERISTICS  |                 |                                |      |     |                        |
| Output Capacitance<br>( $V_{CB} = 50 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$ )                                      | C <sub>ob</sub> | —                              | 12   | 16  | pF                     |
| FUNCTIONAL TESTS (Pulse Width = 10 μs, Duty Cycle = 1.0%)  |                 |                                |      |     |                        |
| Common–Base Amplifier Power Gain<br>(V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 90 W pk, f = 1090 MHz)               | G <sub>PB</sub> | 8.4                            | 10.8 | -   | dB                     |
| Collector Efficiency<br>(V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 90 W pk, f = 1090 MHz)                           | η               | 35                             | 40   | _   | %                      |
| Load Mismatch<br>(V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 90 W pk, f = 1090 MHz,<br>VSWR = 10:1 All Phase Angles) | Ψ               | No Degradation in Power Output |      |     |                        |



C1, C2 — 220 pF Chip Capacitor, 100–mil ATC C3 — 0.1  $\mu$ F C4 — 47  $\mu$ F/75 V L1, L2 — 3 Turns #18 AWG, 1/8" ID Z1–Z9 — Distributed Microstrip Elements, See Photomaster Board Material — 0.031" Thick Glass Teflon,  $\epsilon_r = 2.5$ 





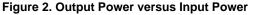


Figure 3. Output Power versus Frequency

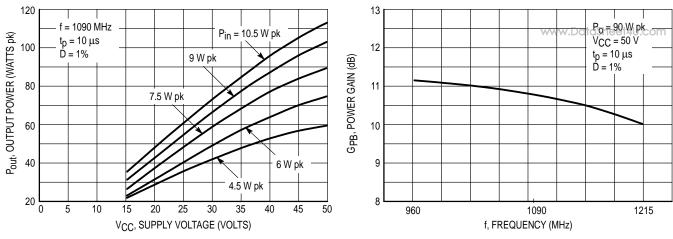
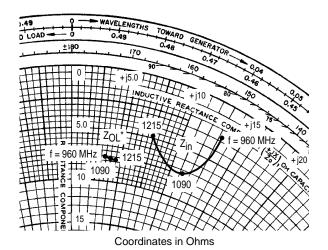


Figure 4. Output Power versus Supply Voltage

Figure 5. Power Gain versus Frequency



| P <sub>out</sub> = 90 W pk | V <sub>CC</sub> = 50 V |
|----------------------------|------------------------|
| t <sub>p</sub> = 10 μs     | D = 1%                 |

| f    | Z <sub>in</sub> | Z <sub>OL</sub> * |
|------|-----------------|-------------------|
| MHz  | Ohms            | Ohms              |
| 960  | 2.8 + j13.2     | 7.6 + j3.5        |
| 1090 | 7.4 + j11.4     | 7.6 + j4.0        |
| 1215 | 4.7 + j7.5      | 7.7 + j4.5        |

Z<sub>OL</sub>\* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage, and frequency.

#### Figure 6. Series Equivalent Input/Output Impedance

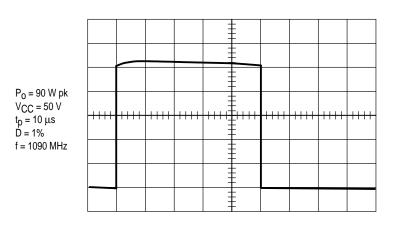
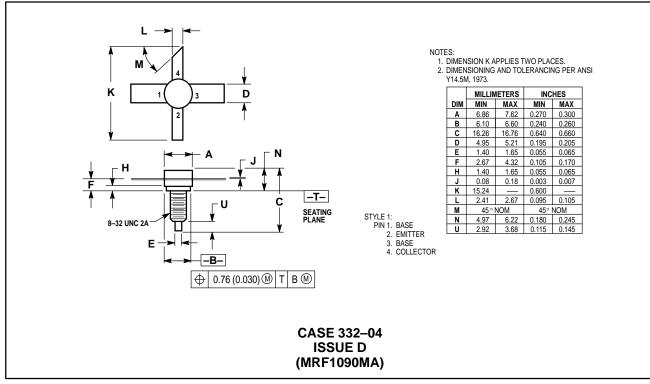
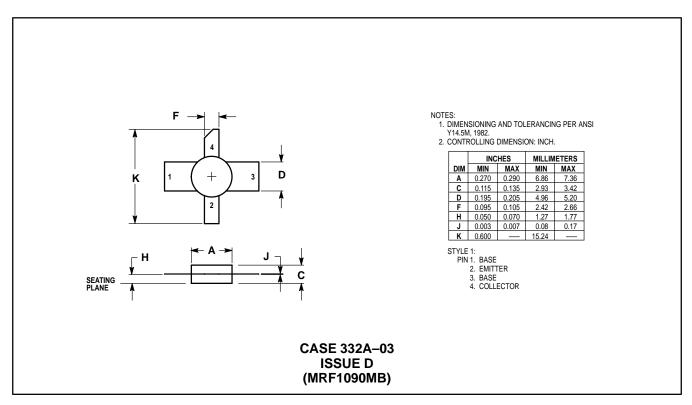


Figure 7. Typical Pulse Performance

## PACKAGE DIMENSIONS





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TOUCHTONE 602–244–6609
ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park,
US & Canada ONLY 1–800–774–1848
51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4-32-1,

Nishi-Gotanda, Shinagawa-ku, Tokyo 141, Japan. 81-3-5487-8488

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