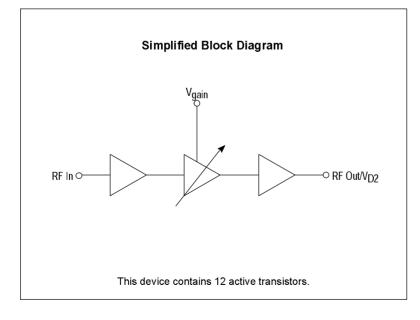


1.8 – 1.9 GHz GaAs Low Noise Amplifier with Gain Control

Designed primarily for use in 1.8 to 1.9 GHz wireless Personal Communication Systems (PCS) such as DCS1800, PCS1900, PHS, and DECT. The MRFIC1830 is a two–stage low noise amplifier with an integrated step attenuator and is packaged in a low–cost Micro–8 package. The attenuator is controlled by a CMOS compatible $V_{\mbox{gain}}$ pin. The LNA can be turned off during transmit mode to save current by using the CMOS compatible Receive Enable pin. The amplifier can be matched to optimize gain or noise figure with simple off–chip input matching.

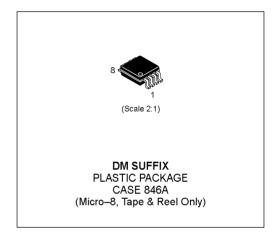
- Usable Frequency Range = 1800 to 2000 MHz
- 19 dB Typ Gain at 1.8 GHz and 17.5 dB at 1.9 GHz
- Gain Attenuation = 19.5 dB (Typ)
- 2.1 dB Typ Noise Figure for DCS and 2.3 dB for PCS
- Simple Off-chip Matching for Maximum Gain/Noise Figure Flexibility
- High Reverse Isolation = 38 dB (Typ)
- Low Power Consumption = 30 mW (Typ)
- Single Bias Supply = 2.7 to 4.5 V
- Low Standby Current = 20 μA (Typ)
- Low Cost Surface Mount Plastic Package
- Device Marking = M1830

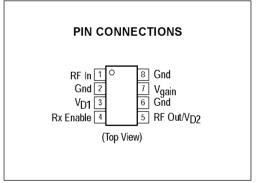


MRFIC1830

DCS/PCS GaAs LOW NOISE AMPLIFIER WITH GAIN CONTROL

SEMICONDUCTOR TECHNICAL DATA





ORDERING INFORMATION

31(321(11) 31(11) 111(31)					
Device	Operating Temp Range	Package			
MRFIC1830DMR2	T _A = -30 to 70°C	Micro–8 Tape & Reel*			

*2,500 Units per 12 mm, 13 inch reel.

MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted)

Rating	Symbol	Limit	Unit
Supply Voltage	V _{D1} , V _{D2}	5.5	Vdc
RF Input Power	P _{RF}	3	dBm
Gain Control Voltage	V_{gain}	5.5	Vdc
Enable Voltage	Rx Enable	5.5	Vdc
Storage Temperature Range	T _{stg}	– 65 to 150	°C
Operating Ambient Temperature	TA	– 20 to 70	°C

NOTES: 1. Meets Human Body Model (HBM) ≤250 V and Machine Model ≤50 V. 2. ESD data available upon request.

RECOMMENDED OPERATING CONDITIONS

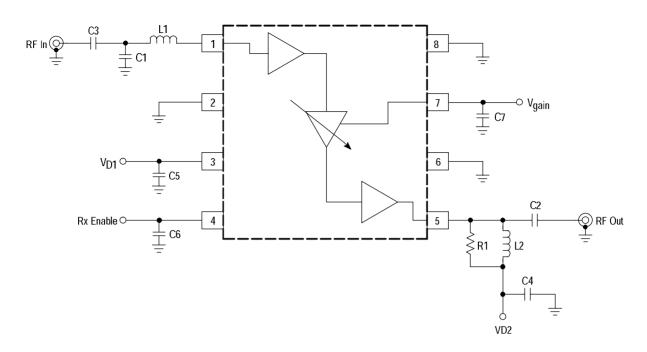
Parameter	Symbol	Min	Тур	Max	Unit
RF Frequency (DCS)	fRF				GHz
DCS		1.8	_	1.88	
PCS		1.93	-	1.99	
Supply Voltage	V _{D1} , V _{D2}	2.7	=	4.5	Vdc
V Gain, High Gain	V _{gain}	_	3.0	-	Vdc
V Gain, Low Gain	V _{gain}	_	0	-	Vdc
Rx Enable Voltage, On	Rx Enable	2.7	-	V _{D1} , V _{D2}	Vdc
Rx Enable Voltage, Off	Rx Enable	0	-	0.2	Vdc

 $\begin{tabular}{ll} \textbf{ELECTRICAL CHARACTERISTICS} & (V_{D1,\ D2} = 2.8\ V,\ T_{A} = 25^{\circ}\text{C},\ RF = 1840\ MHz (1960\ MHz\ for\ PCS),\ RF\ In = -30\ dBm, \\ Rx\ Enable = 2.8\ V,\ V_{gain} = 2.8\ V,\ unless otherwise noted.\ Tested in circuit shown in Figure 1.) \\ \end{tabular}$

Characteristic	Symbol	Min	Тур	Max	Unit
RF Gain	_				dB
DCS		17	19	21	
PCS		-	17.5	-	
RF Gain (V _{gain} = 0 V) (DCS and PCS)	_	_	-0.5	2.5	dB
SSB Noise Figure (DCS) [Note]	_				dB
DCS		-	2.1	3.0	
PCS		_	2.3	_	
SSB Noise Figure (Vgain = 0 V) (DCS and PCS) [Note]	_	_	9.5	20	dB
RF Input 3rd Order Intercept Point [Note]	_				dBm
DCS and PCS		-12	-9.0	_	
V _{gain} = 0 V (DCS and PCS)		-7.0	-1.0	_	
Input 1.0 dB Gain Compression [Note]	_				dBm
DCS and PCS		-21.5	-20	_	
V _{gain} =0 V (DCS and PCS)		-16	-13	_	
Reverse Isolation (S ₁₂)	_				dB
V _{gain} = 3.0 V		-	38	_	
V _{gain} = 0 V		-	47	_	
Input Return Loss	_				dB
V _{gain} = 3.0 V		-	15	_	
V _{gain} = 0 V		-	12	_	
Output Return Loss	_				dB
V _{gain} = 3.0 V		-	12	_	
V _{gain} = 0 V		-	12	_	
Supply Current Rx Mode	-	_	9.0	12	mA
Supply Current Standby Mode (Rx Enable = 0 V)		-	20	200	μΑ

NOTE: Guaranteed by design.

Figure 1. 1.9 GHz Test Circuit



 $\begin{array}{lll} \textbf{DCS (1.8 - 1.88 GHz)} & \textbf{PCS (1.93 - 1.99 GHz)} \\ \textbf{C1} = \textbf{Not Used} & \textbf{C1} = \textbf{1.0 pF} \\ \textbf{C2} = \textbf{1.5 pF} & \textbf{C2} = \textbf{1.3 pF} \\ \textbf{C3, C4, C5, C6, C7} = \textbf{1.0 kpF} & \textbf{C3, C4, C5, C6, C7} = \textbf{1.0 kpF} \\ \textbf{L1} = \textbf{5.6 nH} & \textbf{L1} = \textbf{5.6 nH} \\ \textbf{L2} = \textbf{2.2 nH} & \textbf{L2} = \textbf{2.2 nH} \\ \textbf{R1} = \textbf{390 } \Omega & \textbf{R1} = \textbf{390 } \Omega \end{array}$

Figure 2. Reverse Isolation versus Frequency

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TA = 70°C

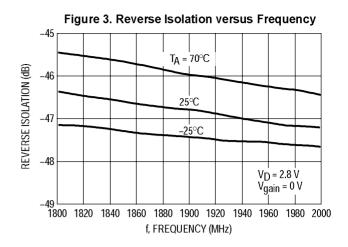
25°C

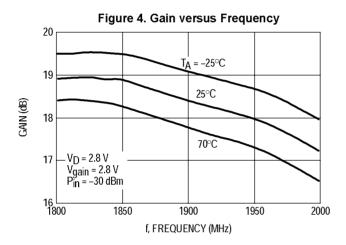
VD = 2.8 V

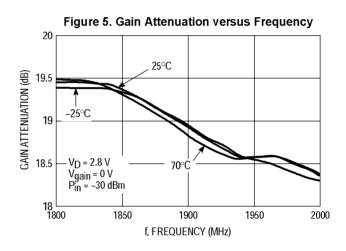
Vgain = 2.8 V

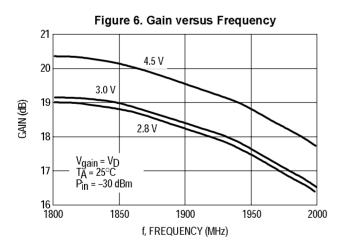
Vgain = 2.8 V

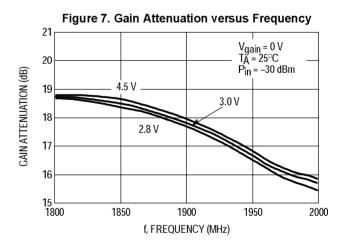
FREQUENCY (MHz)

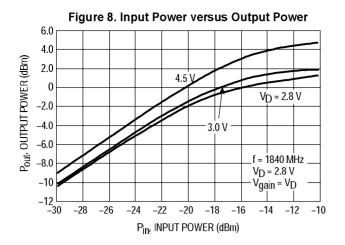


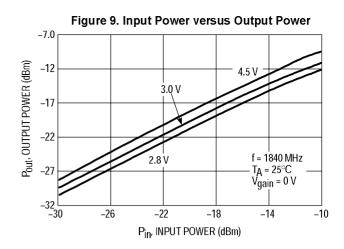


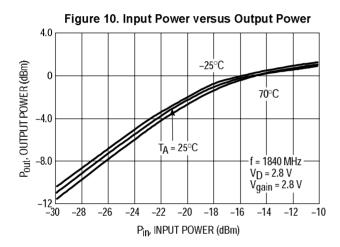


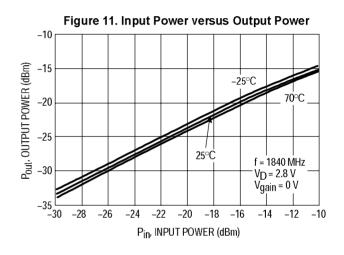


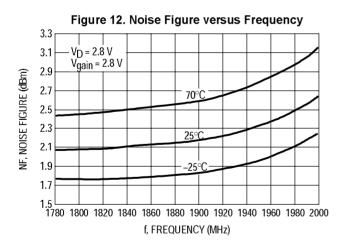


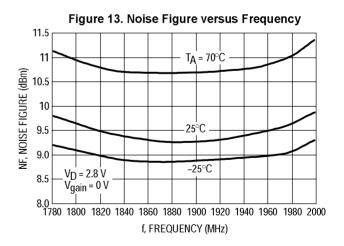




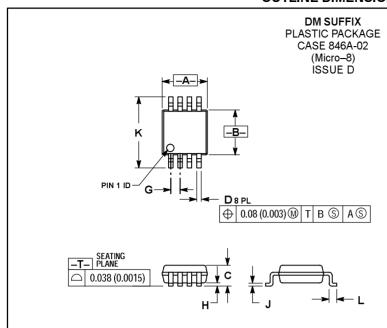








OUTLINE DIMENSIONS



- DIMENSIONING AND TOLERANCING PER ANSI Y145M 1982
- Y14.5M), 1982.
 CONTROLLING DIMENSION: MILLIMETER.
 DIMENSION A DOES NOT INCLUDE MOLD FLASH,
 PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH,
 PROTRUSIONS OR GATE BURRS SHALL NOT
 EXCEED 0.15 (0.006) PER SIDE.

 4. DIMENSION B DOES NOT INCLUDE INTERLEAD
- FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.

	MILLIN	IETERS	INCHES			
DIM	MIN	MAX	MIN	MAX		
Α	2.90	3.10	0.114	0.122		
В	2.90	3.10	0.114	0.122		
С	_	1.10		0.043		
D	0.25	0.40	0.010	0.016		
G	0.65	0.65 BSC		BSC 0.026 BSC		BSC
Н	0.05	0.15	0.002	0.006		
J	0.13	0.23	0.005	0.009		
K	4.75	5.05	0.187	0.199		
	0.40	0.70	0.016	0.028		

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