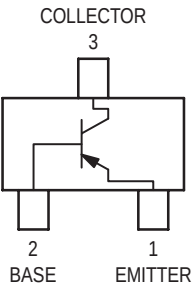


PNP General Purpose Amplifier
Transistor Surface Mount



MSB710-RT1

Motorola Preferred Device



CASE 318D-04, STYLE 1
SC-59

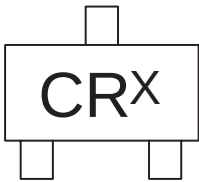
MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector–Base Voltage	V _{(BR)CBO}	–60	Vdc
Collector–Emitter Voltage	V _{(BR)CEO}	–50	Vdc
Emitter–Base Voltage	V _{(BR)EBO}	–7.0	Vdc
Collector Current — Continuous	I _C	–500	mAdc
Collector Current — Peak	I _{C(P)}	–1.0	Adc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P _D	200	mW
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	–55 ~ +150	°C

DEVICE MARKING



The “X” represents a smaller alpha digit Date Code. The Date Code indicates the actual month in which the part was manufactured.

Preferred devices are Motorola recommended choices for future use and best overall value.

replaces MSB710–QT1/D

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage ($I_C = -10\text{ mAdc}$, $I_E = 0$)	$V_{(BR)CEO}$	-50	—	Vdc
Collector–Base Breakdown Voltage ($I_C = -10\text{ }\mu\text{Adc}$, $I_E = 0$)	$V_{(BR)CBO}$	-60	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = -10\text{ }\mu\text{Adc}$, $I_C = 0$)	$V_{(BR)EBO}$	-7.0	—	Vdc
Collector–Base Cutoff Current ($V_{CB} = -20\text{ Vdc}$, $I_E = 0$)	I_{CBO}	—	-0.1	μAdc
DC Current Gain ⁽¹⁾ ($V_{CE} = -10\text{ Vdc}$, $I_C = -150\text{ mAdc}$) ($V_{CE} = -10\text{ Vdc}$, $I_C = 500\text{ mAdc}$)	h_{FE1} h_{FE2}	120 40	240 —	—
Collector–Emitter Saturation Voltage ($I_C = -300\text{ mAdc}$, $I_B = -30\text{ mAdc}$)	$V_{CE(sat)}$	—	-0.6	Vdc
Collector–Base Saturation Voltage ($I_C = -300\text{ mAdc}$, $I_B = -30\text{ mAdc}$)	$V_{BE(sat)}$	—	-1.5	Vdc
Output Capacitance ($V_{CB} = -10\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_{ob}	—	15	pF

1. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, D.C. $\leq 2\%$.