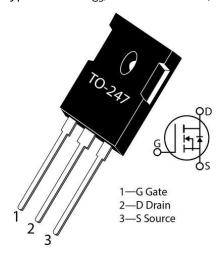
1700V, 35 mΩ N-Channel mSiC[™] MOSFET

MSC035SMA170B



Product Overview

1700V, 35 m Ω typical at 20 V_{GS}, 41 m Ω typical at 18 V_{GS}, Silicon Carbide (SiC) N-Channel MOSFET, TO-247.



Features

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T_{I(max)} = 175 °C
- · Fast and reliable body diode
- Superior avalanche ruggedness
- · RoHS compliant

Benefits

- High efficiency to enable lighter and more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

Applications

- Photovoltaic (PV) inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- · Induction heating and welding
- Hybrid Electric Vehicle (HEV) powertrain and Electric Vehicle (EV) charger
- Power supply and distribution

1. Device Specifications

This section shows the specifications of this device.

1.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings of this device.

Table 1-1. Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain source voltage	1700	V
I_D	Continuous drain current at T _C = 25 °C	71	Α
	Continuous drain current at T _C = 100 °C	50	
I _{DM}	Pulsed drain current ¹	200	
V_{GS}	Gate-source voltage	23 to -10	V
	Transient gate-source voltage	25 to -12	
P _D	Total power dissipation at T _C = 25 °C	427	W
	Linear derating factor	2.85	W/°C

Note:

1. Repetitive rating: pulse width and case temperature are limited by the maximum junction temperature.

The following table shows the thermal and mechanical characteristics of this device.

Table 1-2. Thermal and Mechanical Characteristics

Symbol	Characteristic/Test Conditions	Min.	Тур.	Max.	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance	_	0.27	0.35	°C/W
T _J	Operating junction temperature	-55	_	175	°C
T _{STG}	Storage temperature	-55	_	150	
T_L	Lead temperature for 10 seconds	_	_	300	°C
_	Mounting torque, 6-32 or M3 screw	_	_	10	lbf-in
		_	_	1.1	N-m
Wt	Package weight	_	0.22	_	OZ
		_	6.2	_	g

ESD practices should comply with JESD-625.

1.2 Electrical Performance

The following table shows the static characteristics of this device. $T_J = 25$ °C unless otherwise specified.

Table 1-3. Static Characteristics

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$V_{GS} = 0V, I_D = 100 \mu A$	1700	_	_	٧
R _{DS(on)}	Drain-source on resistance ¹	V _{GS} = 20V, I _D = 30A	_	35	45	mΩ
		V _{GS} = 18V, I _D = 30A	_	41	_	
V _{GS(th)}	Gate-source threshold voltage	$V_{GS} = V_{DS}$, $I_D = 2.5 \text{ mA}$	1.9	3.2	_	٧
I _{DSS}	Zero gate voltage drain current	V _{DS} = 1700V, V _{GS} = 0V	_	0.3	35	μΑ
		V_{DS} = 1700V, V_{GS} = 0V, T_J = 175 °C	_	3.5	_	
I _{GSS}	Gate-source leakage current	V _{GS} = 20V/–10V	_	_	±100	nA



Note:

1. Pulse test: pulse width < 380 μ s, duty cycle < 2%.

The following table shows the dynamic characteristics of this device. T_J = 25 °C unless otherwise specified. The dynamic characteristics are characterized, not 100% tested, at the recommended operating V_{GS} = 20V/–5V.

Table 1-4. Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance	$V_{GS} = 0V$	_	3300	_	pF
C _{rss}	Reverse transfer capacitance	V _{DD} = 1000V	_	13	_	
C _{oss}	Output capacitance	$V_{AC} = 25 \text{ mV}$ f = 200 KHz	_	155	_	
Q _G	Total gate charge	V _{GS} = -5V/20V	_	178	_	nC
Q_{GS}	Gate-source charge	V _{DD} = 850V	_	49	_	
Q_{GD}	Gate-drain charge	I _D = 30A	_	27	_	
t _{d(on)}	Turn-on delay time	V _{DD} = 1360V	_	65	_	ns
t _r	Voltage rise time	$V_{GS} = -5V/20V$	_	28	_	
t _{d(off)}	Turn-off delay time	$I_D = 50A$	_	28	_	
t _f	Voltage fall time	$R_{G(ext)} = 4\Omega$	_	15	_	
E _{ON}	Turn-on switching energy	Freewheeling diode = MSC035SMA170B (V _{GS} = -5V);	_	5645	_	μJ
E _{OFF}	Turn-off switching energy	reference Figure 1-18	_	176	_	
ESR	Gate equivalent series resistance	f = 1 MHz, 25 mV, drain short	_	0.85	_	Ω
SCWT	Short circuit withstand time	V _{DS} = 1200V, V _{GS} = 20V	_	3.1	_	μs
E _{AS}	Avalanche energy, single pulse	I _D = 30A	_	4000	_	mJ

The following table shows the body diode characteristics of this device. T_J = 25 °C unless otherwise specified.

Table 1-5. Body Diode Characteristics

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
V_{SD}	Diode forward voltage	$I_{SD} = 30A, V_{GS} = 0V$	_	3.7	_	V
		$I_{SD} = 30A, V_{GS} = -5V$	_	3.9	5	
t _{rr}	Reverse recovery time	I_{SD} = 50A, V_{GS} = -5V, Drive R_G = 4 Ω , V_{DD} =	_	72	_	ns
Q _{rr}	Reverse recovery charge	1360V, dI/dt = –15000 A/μs	_	1234	_	nC
I _{RRM}	Reverse recovery current		_	34	_	Α



1.3 Typical Performance Curves

Data for performance curves are characterized, not 100% tested.

Figure 1-1. Drain Current vs. V_{DS} at T_{J}

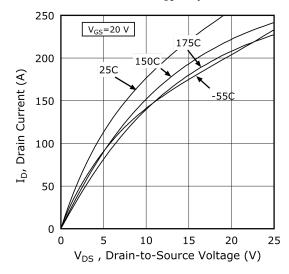


Figure 1-2. Drain Current vs. V_{DS} at V_{GS}

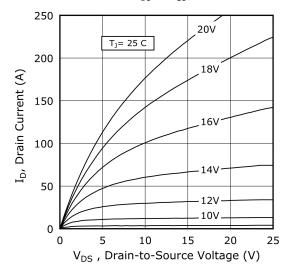


Figure 1-3. Drain Current vs. V_{DS} at V_{GS}

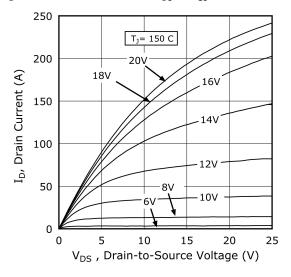


Figure 1-4. Drain Current vs. V_{DS} at V_{GS}

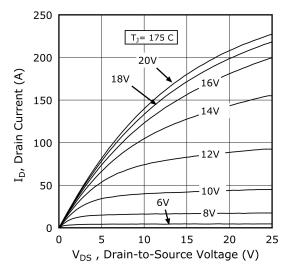




Figure 1-5. R_{DS(on)} vs. Junction Temperature

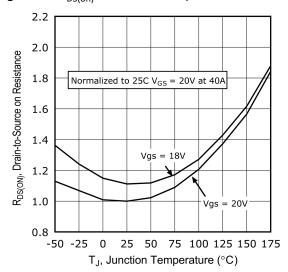


Figure 1-6. Gate Charge Characteristics

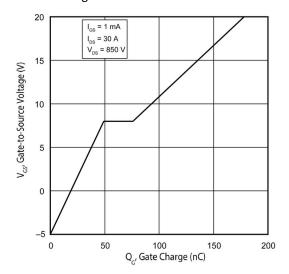


Figure 1-7. Capacitance vs. Drain-to-Source Voltage

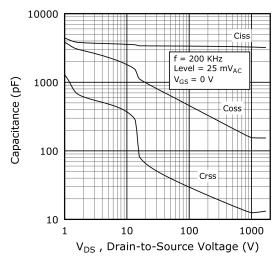


Figure 1-8. Output Charge vs. Drain-to-Source Voltage

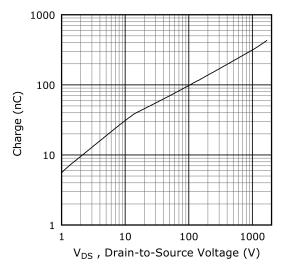




Figure 1-9. I_D vs. V_{DS} 3rd Quadrant Conduction

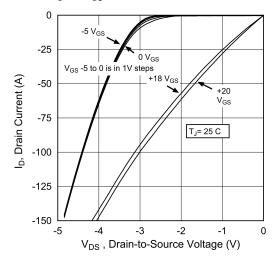


Figure 1-11. Switching Energy E_{ON} vs. $V_{DS} \& I_{D}$

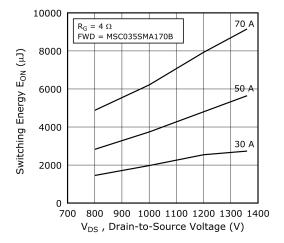


Figure 1-10. I_D vs. V_{DS} 3rd Quadrant Conduction

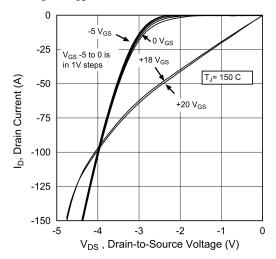


Figure 1-12. Switching Energy E_{OFF} vs. $V_{DS} \& I_{D}$

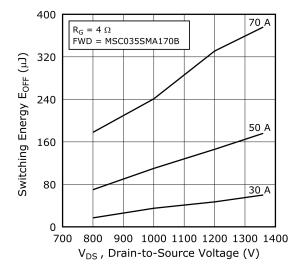




Figure 1-13. Switching Energy vs. R_G

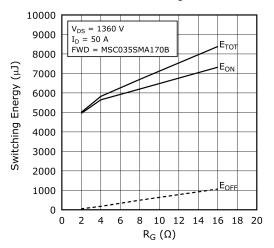


Figure 1-14. Switching Energy vs. Junction Temperature

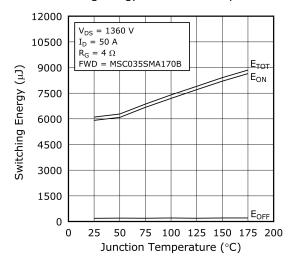
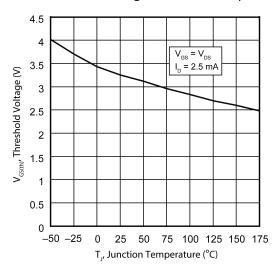
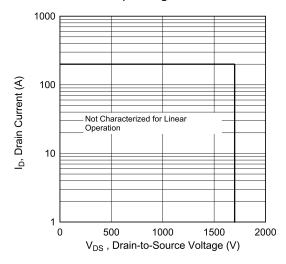


Figure 1-15. Threshold Voltage vs. Junction Temperature Figure 1-16. Forward Safe Operating Area





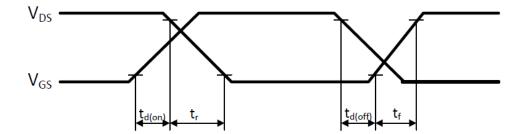


D = 0.9 Z_{OJC}, Thermal Impedance (C/W) 0.5 0.3 0.1 0.1 Note: 1 0.05 $^{\mathsf{P}}_{\mathsf{DM}}$ 0.01 Single Pulse Duty Factor D = t_1/t_2 Peak T $_{J} = P_{DM} \times Z_{\theta JC} + T_{C}$ 0.001 1×10⁻⁶ 1×10⁻⁵ 1×10⁻⁴ 1×10⁻³ 1×10⁻² 1×10⁻¹ 1×100

Figure 1-17. Maximum Transient Thermal Impedance

The following figure shows the switching waveform diagram of this device.

Figure 1-18. Switching Waveform



Pulse Duration (seconds)



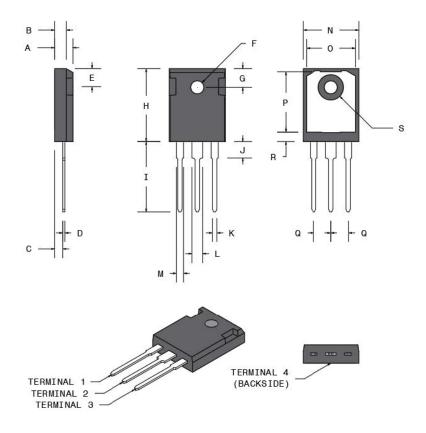
2. Package Specification

This section shows the package specification of this device.

2.1 Package Outline Drawing

The following figure illustrates the TO-247 package outline of this device.

Figure 2-1. Package Outline Drawing



The following table shows the TO-247 dimensions and should be used in conjunction with the package outline drawing.

Table 2-1. TO-247 Dimensions

Symbol	Min. (mm)	Max. (mm)	Min. (in.)	Max. (in.)
Α	4.69	5.31	0.185	0.209
В	1.49	2.49	0.059	0.098
С	2.21	2.59	0.087	0.102
D	0.40	0.79	0.016	0.031
E	5.38	6.20	0.212	0.244
F	3.50	3.81	0.138	0.150
G	6.15 BSC		0.242 BSC	
Н	20.80	21.46	0.819	0.845
I	19.81	20.32	0.780	0.800
J	4.00	4.50	0.157	0.177
K	1.01	1.40	0.040	0.055
L	2.87	3.12	0.113	0.123



continued					
Symbol	Min. (mm)	Max. (mm)	Min. (in.)	Max. (in.)	
М	1.65	2.13	0.065	0.084	
N	15.49	16.26	0.610	0.640	
0	13.50	14.50	0.531	0.571	
Р	16.50	17.50	0.650	0.689	
Q	5.45 BSC		0.215 BSC		
R	2.00	2.75	0.079	0.108	
S	7.10	7.50	0.280	0.295	
Terminal 1	Gate				
Terminal 2	Drain				
Terminal 3	Source				
Terminal 4	Drain			_	



3. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Table 3-1. Revision History

Revision	Date	Description
A 11/2023 The following changes are ma the document:		The following changes are made in this revision of the document:
		 Document migrated from Microsemi template to Microchip template; Assigned Microchip literature number DS-00005159A, which replaces the previous Microsemi literature number 050-7769. Updated figures in 1.3. Typical Performance Curves.
Initial release (Microsemi Revisi	ion A) 03/2020	Initial release



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