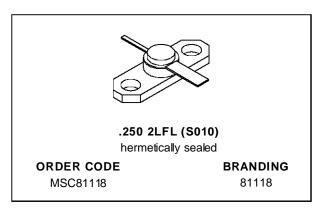


MSC81118

RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

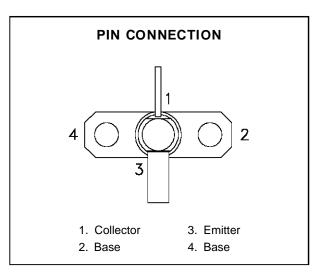
- EMITTER BALLASTED
- VSWR CAPABILITY ∞:1 @ RATED CONDITIONS
- HERMETIC STRIPAC® PACKAGE
- P_{OUT} = 2.0 W MIN. WITH 10 dB GAIN @ 1.0 GHz



DESCRIPTION

The MSC81118 is a common base hermetically sealed silicon NPN microwave transistor utilizing a fishbone, emitter ballasted geometry with a refractory/gold metallization system. This device is capable of withstanding an infinite load VSWR at any phase angle under rated conditions.

The MSC81118 was designed for Class C amplifier applications in the 0.4 - 1.2 GHz frequency range.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
Poiss	Power Dissipation* (T _C ≤ 75°C)	6.25	W
Ic	Device Current*	200	mA
V _{CC}	Collector-Supply Voltage*	35	V
TJ	Junction Temperature	200	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	20	°C/W

^{*}Applies only to rated RF amplifier operation

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ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

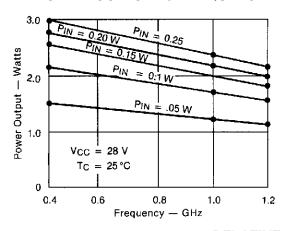
STATIC

Symbol	Took Conditions	Value			IImit		
	Test Conditions		Min.	Тур.	Max.	Unit	
BV _{CBO}	I _C = 1mA	$I_{E} = 0mA$		45	_	_	V
BV _{EBO}	I _E = 1mA	$I_C = 0mA$		3.5	_	_	V
BV _{CER}	IC = 5mA	$R_{BE} = 10\Omega$		45	_	_	V
Ісво	V _{CB} = 28V			_	_	0.5	mA
h _{FE}	V _{CE} = 5V	$I_C = 100mA$		15	_	120	_

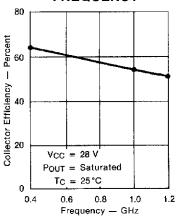
DYNAMIC

Cumbal	Test Conditions		Value			IImi4	
Symbol		rest Conditions			Тур.	Max.	Unit
Pout	f = 1.0 GHz	$P_{IN} = 0.2 \text{ W}$	$V_{CC} = 28 \ V$	2.0	2.2	_	W
ης	f = 1.0 GHz	$P_{IN} = 0.2 \text{ W}$	$V_{CC} = 28 \text{ V}$	50	55	_	%
G _P	f = 1.0 GHz	$P_{IN} = 0.2 \text{ W}$	$V_{CC} = 28 V$	10	10.4	_	dB
Сов	f = 1 MHz	V _{CB} = 28 V			_	3.2	pF

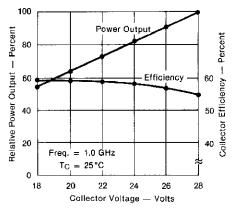
TYPICAL PERFORMANCE POWER OUTPUT vs FREQUENCY



COLLECTOR EFFICIENCY vs FREQUENCY

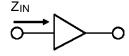


RELATIVE POWER OUTPUT vs COLLECTOR VOLTAGE

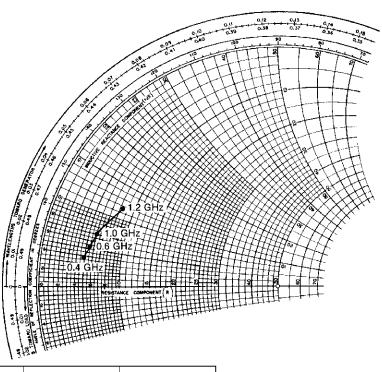


IMPEDANCE DATA



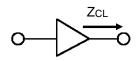


$$\begin{split} P_{IN} &= 0.2 \ W \\ V_{CC} &= 28 \ V \\ Normalized \ to \ 50 \ ohms \end{split}$$

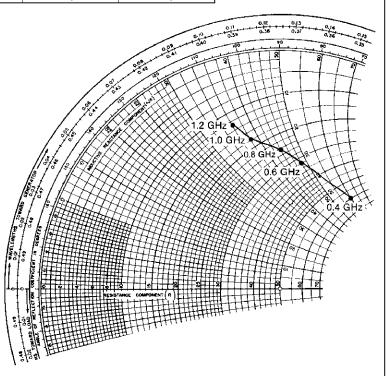


FREQ.	$Z_{IN}\left(\Omega\right)$	$Z_{CL}(\Omega)$
0.4 GHz	4.8 + j 3.7	60.0 + j 60.0
0.6 GHz	5.4 + j 5.3	32.0 + j 48.0
1.0 GHz	6.0 + j 7.0	18.0 + j 38.0
1.2 GHz	8.2 + j 11.6	12.8 + j 36.0

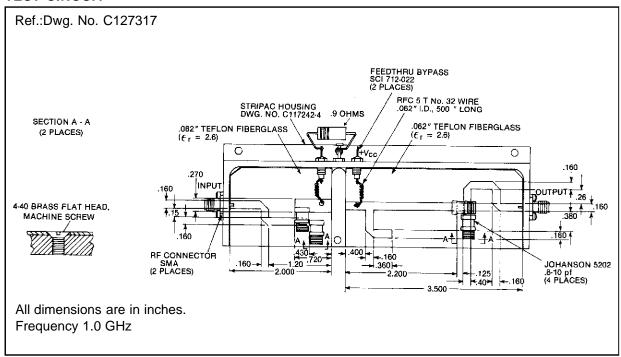
TYPICAL COLLECTOR LOAD IMPEDANCE



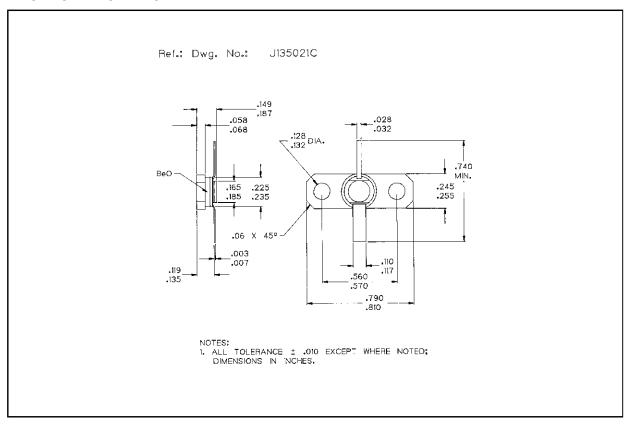
 $P_{OUT} = Saturated$ $V_{CC} = 28 \text{ V}$ Normalized to 50 ohms



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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