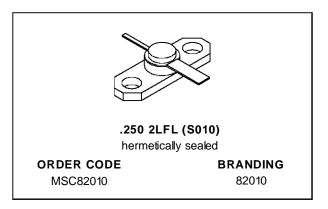


MSC82010

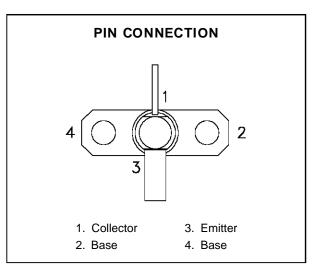
RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

- EMITTER BALLASTED
- VSWR CAPABILITY ∞:1 @ RATED CONDITIONS
- HERMETIC STRIPAC® PACKAGE
- Pout = 10 W MIN. WITH 5.2 dB GAIN @ 2.0 GHz



DESCRIPTION

The MSC82010 is a common base hermetically sealed silicon NPN microwave transistor utilizing a fishbone emitter ballasted geometry with a refractory/gold metallization system. This device is capable of withstanding an infinite load VSWR at any phase angle under rated rated conditions. The MSC82010 was designed for Class C amplifier applications in the 1.0 - 2.0 GHz frequency range.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
Poiss	Power Dissipation*	35	W
Ic	Device Current*	1.5	А
V _{CC}	Collector-Supply Voltage*	35	V
TJ	Junction Temperature	200	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	5.0	°C/W

^{*}Applies only to rated RF amplifier operation

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ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

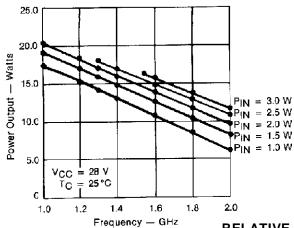
Cumb al		Took Conditions	Value			11:4
Symbol		Test Conditions	Min. Typ. Max.	Unit		
ВУсво	I _C = 5mA	$I_E = 0mA$	45	_	_	V
BV _{EBO}	I _E = 1mA	$I_C = 0mA$	3.5	_	_	V
BV _{CER}	IC = 15mA	$R_{BE} = 10\Omega$	45	_	_	V
Ісво	V _{CB} = 28V		_	_	5.0	mA
hFE	V _{CE} = 5V	$I_C = 1000 mA$	15	_	120	_

DYNAMIC

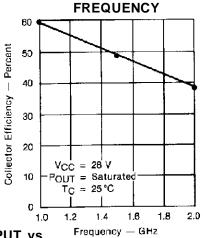
Cumbal		Toot Conditions		Value		IImi4		
Symbol		Test Conditions Min.				Max.	Unit	
Pout	f = 2.0 GHz	$P_{IN} = 3.0 \text{ W}$	$V_{CC}=28\ V$	10	11.5	_	W	
ης	f = 2.0 GHz	$P_{IN} = 3.0 \text{ W}$	$V_{CC} = 28 \text{ V}$	35	38	_	%	
GP	f = 2.0 GHz	$P_{IN} = 3.0 \text{ W}$	$V_{CC} = 28 V$	5.2	5.8	_	dB	
Сов	f = 1 MHz	$V_{CB} = 28 \text{ V}$		_	_	19	pF	

TYPICAL PERFORMANCE

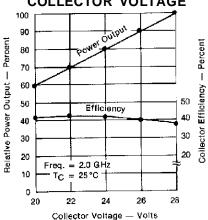
POWER OUTPUT vs FREQUENCY



COLLECTOR EFFICIENCY vs

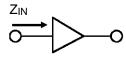


RELATIVE POWER OUTPUT vs COLLECTOR VOLTAGE

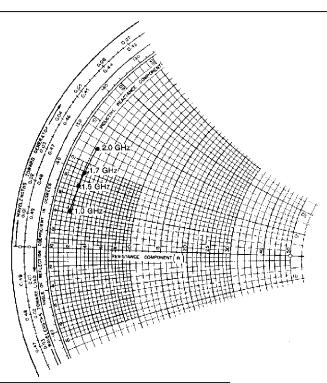


IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

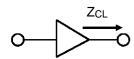


 $P_{IN} = 3.0 \text{ W}$ $V_{CC} = 28 \text{ V}$ Normalized to 50 ohms

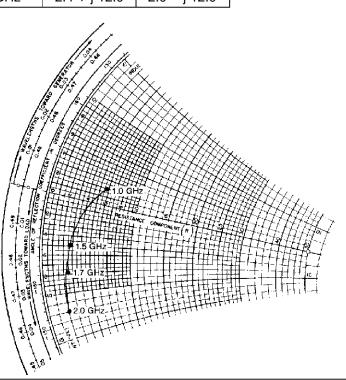


FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
1.0 GHz	1.7 + j 4.2	5.7 + j 1.9
1.5 GHz	2.0 + j 7.2	2.8 – j 5.0
1.7 GHz	2.2 + j 8.8	2.5 – j 7.8
2.0 GHz	2.4 + j 12.0	2.0 – j 12.0

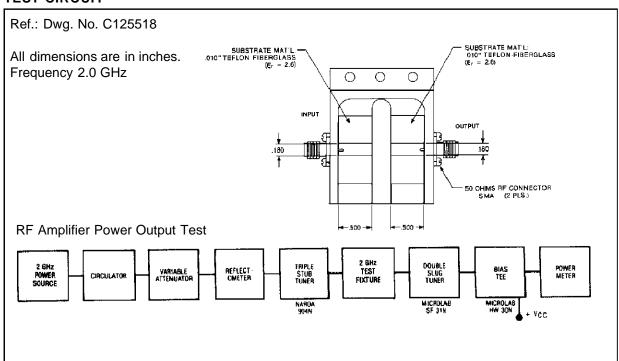
TYPICAL COLLECTOR LOAD IMPEDANCE



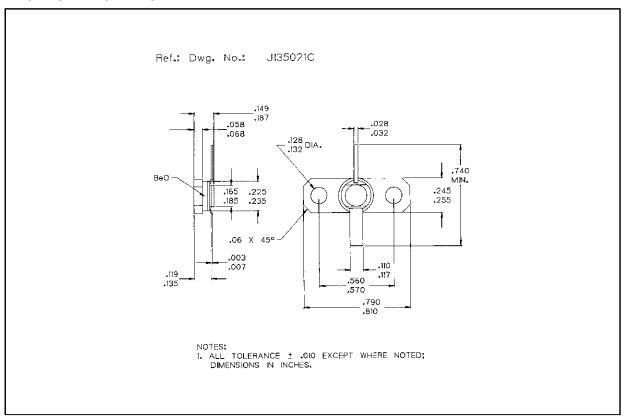
 $P_{OUT} = Saturated$ $V_{CC} = 28 \text{ V}$ Normalized to 50 ohms



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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