Three-Phase Bridge, Brake, Soft Start, and Solenoid Power Module

MSCSM70XM45CTYZBNMG

Product Overview

The MSCSM70XM45CTYZBNMG device is a three-phase bridge, brake, soft start, and solenoid power module.

The following figures show the electrical diagram and pinout location of the device.

Figure 1. Electrical Diagram

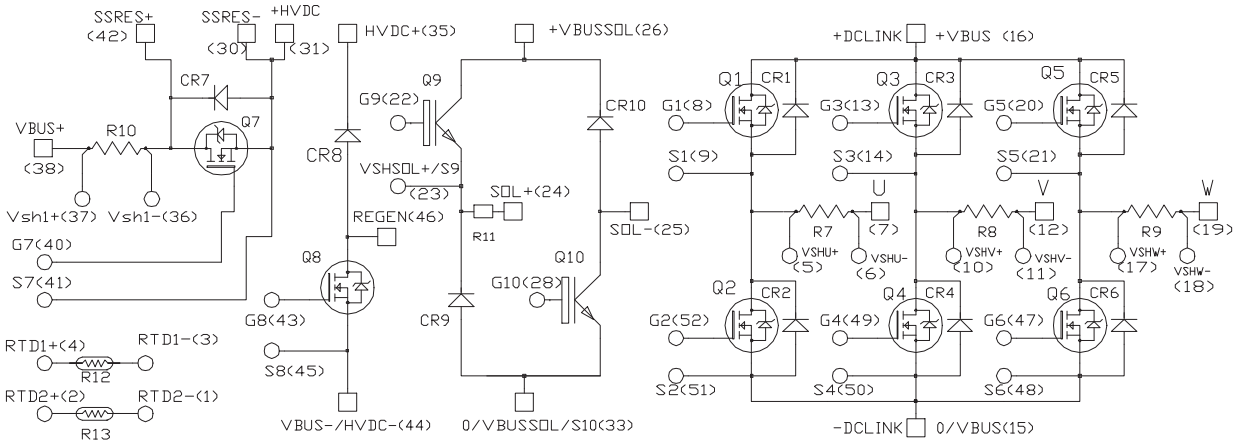
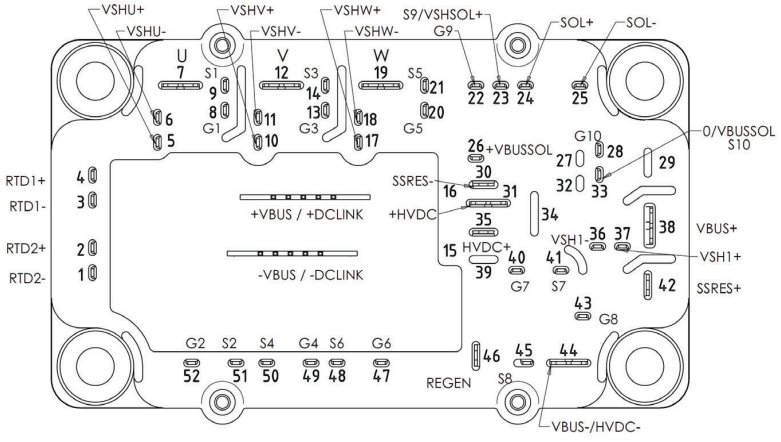
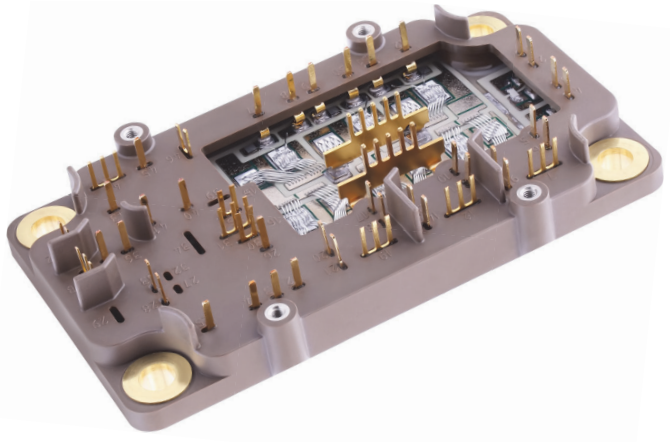


Figure 2. Pinout Location





Note: All ratings at T_J = 25 °C, unless otherwise specified.

⚠ CAUTION These devices are sensitive to electrostatic discharge. Proper handling procedures must be followed.

Features

The MSCSM70XM45CTYZBNMG device has the following key features:

- Silicon Carbide (SiC) MOSFET
- SiC Schottky Diode
- Low stray inductance
- Lead frames for power connections
- Si_3N_4 substrate for improved thermal performance
- AlSiC base plate for extended reliability and reduced weight
- Extended storage temperature range
- Internal thermistor for temperature monitoring

Benefits

The MSCSM70XM45CTYZBNMG device has the following benefits:

- Stable temperature behavior
- Very rugged
- Solderable terminals for easy PCB mounting
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- RoHS Compliant

Application

The MSCSM70XM45CTYZBNMG device has the following applications:

- Hybrid Power Device (HPD) for Electro-Mechanical Actuator (EMA) and Electro-Hydrostatic Actuator (EHA) systems
- High reliability Power Core Module (PCM)
- Modular power module for Power Drive Electronic (PDE)

1. Electrical Specification

The following sections describe the electrical specifications of the MSCSM70XM45CTYZBNMG device.

1.1 Q1 to Q6 and Q8 SiC MOSFETs (Per SiC MOSFET): Three-Phase Bridge and Brake

The following table lists the absolute maximum ratings (per SiC MOSFET) of the Q1 to Q6 and Q8 SiC MOSFETs.

Table 1-1. Absolute Maximum Ratings: Q1 to Q6 and Q8 SiC MOSFETs

Symbol	Parameter		Maximum Ratings	Unit
V_{DSS}	Drain-source voltage		700	V
I_D	Continuous drain current	T_C = 25 °C	52	A
		T_C = 80 °C	41	
I_{DM}	Pulsed drain current		100	
V_{GS}	Gate-source voltage		−10/23	V
$R_{DS(on)}$	Drain-source ON resistance		44	mΩ
P_D	Power dissipation	T_C = 25 °C	141	W

The following table lists the electrical characteristics (per SiC MOSFET) of the Q1 to Q6 and Q8 SiC MOSFETs.

Table 1-2. Electrical Characteristics: Q1 to Q6 and Q8 SiC MOSFETs

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
I_{DSS}	Zero gate voltage drain current	V_{GS} = 0V; V_{DS} = 700V		—	—	100	µA
$R_{DS(on)}$	Drain-source ON resistance	V_{GS} = 20V I_D = 30A	T_J = 25 °C	—	35	44	mΩ
			T_J = 175 °C	—	41	—	
$V_{GS(th)}$	Gate threshold voltage	V_{GS} = V_{DS}; I_D = 2 mA		1.9	2.7	—	V
I_{GSS}	Gate-source leakage current	V_{GS} = 20V; V_{DS} = 0V		—	—	150	nA

The following table lists the dynamic characteristics (per SiC MOSFET) of the Q1 to Q6 and Q8 SiC MOSFETs.

Table 1-3. Dynamic Characteristics: Q1 to Q6 and Q8 SiC MOSFETs

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
C_{iss}	Input capacitance	V_{GS} = 0V V_{DS} = 700V f = 1 MHz		—	2010	—	pF
C_{oss}	Output capacitance			—	247	—	
C_{rss}	Reverse transfer capacitance			—	17	—	
Q_g	Total gate charge	V_{GS} = –5V/20V V_{Bus} = 470V I_D = 30A		—	99	—	nC
Q_{gs}	Gate-source charge			—	33	—	
Q_{gd}	Gate-drain charge			—	18	—	
$T_{d(on)}$	Turn-on delay time	V_{GS} = –5V/20V V_{Bus} = 400V I_D = 40A R_{GON} = 54Ω R_{GOFF} =9.4Ω	T_J = 150 °C	—	40	—	ns
T_r	Rise time			—	35	—	
$T_{d(off)}$	Turn-off delay time			—	50	—	
T_f	Fall time			—	20	—	
E_{on}	Turn-on energy	V_{GS} = –5V/20V V_{Bus} = 400V I_D = 40A R_{GON} = 54Ω R_{GOFF} = 9.4Ω	T_J = 150 °C	—	272	—	µJ
E_{off}	Turn-off energy			—	93	—	
R_{Gint}	Internal gate resistance			—	6.13	—	Ω
R_{thJC}	Junction-to-case thermal resistance			—	—	1.06	°C/W

The following table lists the body diode ratings and characteristics (per SiC MOSFET) of the Q1 to Q6 and Q8 SiC MOSFETs.

Table 1-4. Body Diode Ratings and Characteristics: Q1 to Q6 and Q8 SiC MOSFETs

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode forward voltage	V_{GS} = 0V; I_{SD} = 30A	—	3.8	—	V
		V_{GS} = –5V; I_{SD} = 30A	—	4	—	
t_{rr}	Reverse recovery time	I_{SD} = 30A V_{GS} = –5V V_R = 400V di_F/dt = 1000 A/µs	—	75	—	ns
Q_{rr}	Reverse recovery charge		—	305	—	nC
I_{rr}	Reverse recovery current		—	11	—	A

1.2 CR1 to CR8 SiC Diodes (Per SiC Diode): Three-Phase Bridge, Brake, and Soft Start

The following table lists the ratings and characteristics (per SiC diode) of the CR1 to CR8 SiC diodes.

Table 1-5. Ratings and Characteristics: CR1 to CR8 SiC Diodes

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
V_{RRM}	Peak repetitive reverse voltage			—	—	700	V
I_{RM}	Reverse leakage current	V_R = 700V	T_J = 25 °C	—	15	200	µA
			T_J = 175 °C	—	250	—	
I_F	DC forward current	T_J = 175 °C	T_C = 60 °C	—	30	—	A
V_F	Diode forward voltage	I_F = 30A	T_J = 25 °C	—	1.5	1.8	V
			T_J = 175 °C	—	1.9	—	
Q_C	Total capacitive charge	V_R = 400V		—	83	—	nC
C	Total capacitance	f = 1 MHz, V_R = 200V		—	150	—	pF
		f = 1 MHz, V_R = 400V		—	128	—	
R_{thJC}	Junction-to-case thermal resistance			—	—	1.75	°C/W

1.3 Q7 SiC MOSFET: Soft Start

The following table lists the absolute maximum ratings of the Q7 SiC MOSFET.

Table 1-6. Absolute Maximum Ratings: Q7 SiC MOSFET

Symbol	Parameter		Maximum Ratings	Unit
V_{DSS}	Drain-source voltage		700	V
I_D	Continuous drain current	T_C = 25 °C	110	A
		T_C = 80 °C	88	
I_{DM}	Pulsed drain current		220	
V_{GS}	Gate-source voltage		−10/23	V
$R_{DS(on)}$	Drain-source ON resistance		19	mΩ
P_D	Power dissipation	T_C = 25 °C	292	W

The following table lists the electrical characteristics of the Q7 SiC MOSFET.

Table 1-7. Electrical Characteristics: Q7 SiC MOSFET

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
I_{DSS}	Zero gate voltage drain current	V_{GS} = 0V V_{DS} = 700V		—	—	100	µA
$R_{DS(on)}$	Drain-source ON resistance	V_{GS} = 20V I_D = 40A	T_J = 25 °C	—	15	19	mΩ
			T_J = 175 °C	—	18.8	—	
$V_{GS(th)}$	Gate threshold voltage	V_{GS} = V_{DS} I_D = 4 mA		1.9	2.4	—	V
I_{GSS}	Gate-source leakage current	V_{GS} = 20V V_{DS} = 0V		—	—	150	nA

The following table lists the dynamic characteristics of the Q7 SiC MOSFET.

Table 1-8. Dynamic Characteristics: Q7 SiC MOSFET

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
C_{iss}	Input capacitance	V_{GS} = 0V V_{DS} = 700V f = 1 MHz		—	4500	—	pF
C_{oss}	Output capacitance			—	510	—	
C_{rss}	Reverse transfer capacitance			—	29	—	
Q_g	Total gate charge	V_{GS} = −5/20V V_{Bus} = 470V I_D = 40A		—	215	—	nC
Q_{gs}	Gate-source charge			—	58	—	
Q_{gd}	Gate-drain charge			—	35	—	
$T_{d(on)}$	Turn-on delay time	V_{GS} = −5/20V V_{Bus} = 400V I_D = 80A R_{GON} = 27Ω R_{GOFF} = 4.7Ω	T_J = 150 °C	—	40	—	ns
T_r	Rise time			—	35	—	
$T_{d(off)}$	Turn-off delay time			—	50	—	
T_f	Fall time			—	20	—	
E_{on}	Turn-on energy	V_{GS} = −5/20V V_{Bus} = 400V I_D = 80A R_{GON} = 27Ω R_{GOFF} = 4.7Ω	T_J = 150 °C	—	545	—	µJ
E_{off}	Turn-off energy			—	186	—	
R_{Gint}	Internal gate resistance			—	5.69	—	Ω
R_{thJC}	Junction-to-case thermal resistance			—	—	0.513	°C/W

The following table lists the body diode ratings and characteristics of the Q7 SiC MOSFET.

Table 1-9. Body Diode Ratings and Characteristics: Q7 SiC MOSFET

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode forward voltage	V_{GS} = 0V; I_{SD} = 40A	—	3.4	—	V
		V_{GS} = −5V; I_{SD} = 40A	—	3.8	—	
t_{rr}	Reverse recovery time	I_{SD} = 40A V_{GS} = −5V V_R = 400V di_F/dt = 1000 A/µs	—	38	—	ns
Q_{rr}	Reverse recovery charge		—	318	—	nC
I_{rr}	Reverse recovery current		—	14.8	—	A

1.4 Q9 and Q10 IGBTs (Per IGBT): Solenoid

The following table lists the absolute maximum ratings (per IGBT) of the Q9 and Q10 IGBTs.

Table 1-10. Absolute Maximum Ratings: Q9 and Q10 IGBTs

Symbol	Parameter		Maximum Ratings	Unit
V_{CES}	Collector-emitter voltage		1200	V
I_C	Continuous collector current	T_C = 25 °C	27	A
		T_C = 80 °C	15	
I_{CM}	Pulsed collector current	T_C = 25 °C	30	
V_{GE}	Gate-emitter voltage		±20	V
P_D	Power dissipation	T_C = 25 °C	80	W

The following table lists the electrical characteristics (per IGBT) of the Q9 and Q10 IGBTs.

Table 1-11. Electrical Characteristics: Q9 and Q10 IGBTs

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
I_{CES}	Zero gate voltage collector current	V_{GE} = 0V V_{CE} = 1200V		—	—	100	µA
$V_{CE(sat)}$	Collector emitter saturation voltage	V_{GE} = 15V I_C = 8A	T_J = 25 °C	1.6	1.85	2.1	V
			T_J = 150 °C	—	2.25	—	
$V_{GE(th)}$	Gate threshold voltage	V_{GE} = V_{CE} I_C = 0.3 mA		5.3	5.8	6.3	
I_{GES}	Gate-emitter leakage current	V_{GE} = 15V V_{CE} = 0V		—	—	150	nA
C_{ies}	Input capacitance	V_{GE} = 0V V_{CE} = 25V f = 1 MHz		—	490	—	pF
C_{res}	Reverse transfer capacitance			—	30	—	
R_{thJC}	Junction-to-case thermal resistance			—	—	1.85	°C/W

1.5 CR9 and CR10 SiC Diodes (Per SiC diode): Solenoid

The following table lists the ratings and characteristics (per SiC diode) of the CR9 and CR10 SiC diodes.

Table 1-12. Ratings and Characteristics: CR9 and CR10 SiC Diodes

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
V_{RRM}	Peak repetitive reverse voltage			—	—	700	V
I_{RM}	Reverse leakage current	V_R = 700V	T_J = 25 °C	—	5	200	µA
			T_J = 175 °C	—	50	—	
I_F	DC forward current	T_J = 175 °C	T_C = 60 °C	—	10	—	A
V_F	Diode forward voltage	I_F = 10A	T_J = 25 °C	—	1.5	1.8	V
			T_J = 175 °C	—	1.9	—	
Q_C	Total capacitive charge	V_R = 400V		—	27	—	nC
C	Total capacitance	f = 1 MHz, V_R = 200V		—	49	—	pF
		f = 1 MHz, V_R = 400V		—	46	—	
R_{thJC}	Junction-to-case thermal resistance			—	—	4.69	°C/W

1.6 Electrical Shunt Characteristics

The following tables list the electrical shunt characteristics of the MSCSM70XM45CTYZBNMG device.

Table 1-13. Shunt (R7 to R9)

Symbol	Characteristic		Min.	Typ.	Max.	Unit
R_i	Resistance value	i = 7, 8, and 9	—	0.5	—	mΩ
T_{Ri}	Tolerance	TCR Max 20 ppm/°C (from 20 °C to 60 °C)	—	1	1.5	%
P_{Ri}	Load capacity		—	—	5	W
I_{Ri}	Current capacity		—	—	100	A

Table 1-14. Shunt (R10)

Symbol	Characteristic		Min.	Typ.	Max.	Unit
R_i	Resistance value	i = 10	—	0.3	—	mΩ
T_{Ri}	Tolerance	TCR Max 20 ppm/°C (from 20 °C to 60 °C)	—	1	1.5	%
P_{Ri}	Load capacity		—	—	5	W
I_{Ri}	Current capacity		—	—	129	A

Table 1-15. Shunt (R11)

Symbol	Characteristic		Min.	Typ.	Max.	Unit
R_i	Resistance value	i = 11	—	15	—	mΩ
RSoli	Resistance value with SOL+ connector [1]	TCR Max 50 ppm/°C (from 20 °C to 60 °C)	—	15.25	—	
T_{Ri}	Tolerance		—	1	1.5	%
P_{Ri}	Load capacity		—	—	3	W
I_{Ri}	Current capacity		—	—	14	A

Note:

1. Value that integrates the resistivity of the SOL+ connector considering the user PCB mounted on the spacers and soldered on the power module in accordance with IPC A610, class 3.

1.7 Temperature Sensor PTC

The following table lists the temperature sensor PTC of the MSCSM70XM45CTYZBNMG device.

Table 1-16. Temperature Sensor PTC

Symbol	Characteristic	Typ.	Unit
R_0	Resistance at 0 °C	1000	Ω
A	—	3.9083×10^{-3}	$°C^{-1}$
B	—	-5.775×10^{-7}	$°C^{-2}$
C	—	-4.183×10^{-12}	$°C^{-4}$
ΔT	—	±(0.3 + 0.005 × |T|)	°C

For temperature range of 0 °C up to 175 °C, $R_T = R_0 (1 + A \times T + B \times T^2)$

For temperature range of −55 °C up to 0 °C, $R_T = R_0 (1 + A \times T + B \times T^2 + C (T - 100) T^3)$

Where:

T: Temperature in °C

R_T: Thermistor value at T

Note: For more information, see APT0406—Using NTC Temperature Sensor Integrated into Power Module.

1.8 Thermal and Package Characteristics

The following table lists the thermal and package characteristics of the MSCSM70XM45CTYZBNMG device.

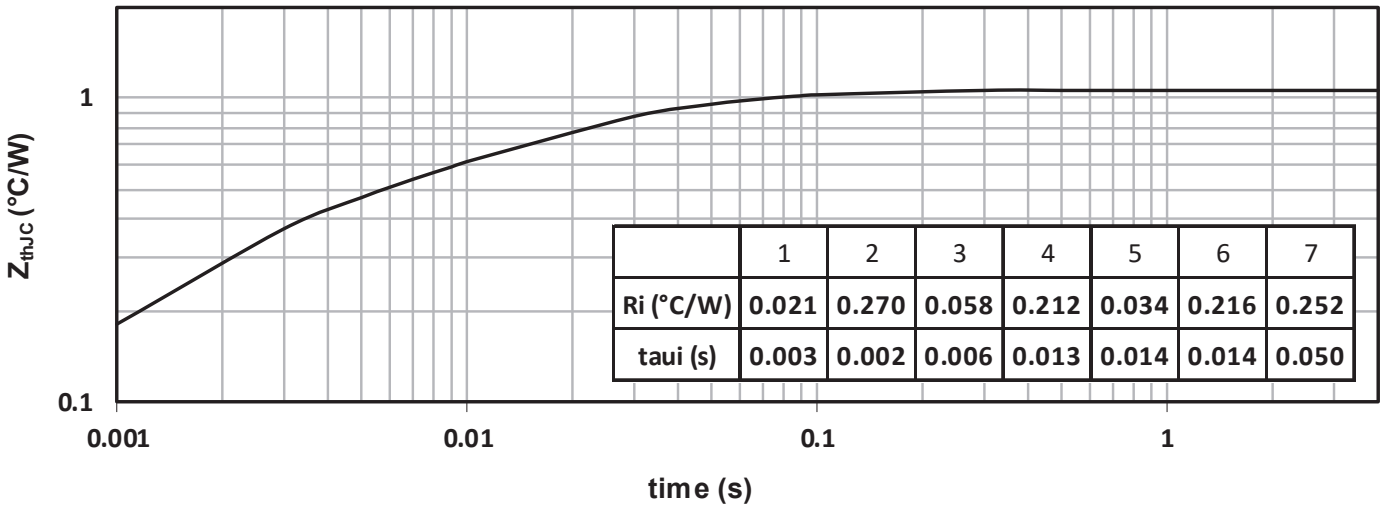
Table 1-17. Thermal and Package Characteristics

Symbol	Characteristic			Min.	Max.	Unit
V_{ISOL}	RMS isolation voltage, any terminal to case, t = 1 min, at 1 bar			4000	—	V
V_{ISOL}PTC	RMS isolation voltage, PTC to any other electrical terminals, t = 1 min at 1 bar, 50/60 Hz			1500	—	
T_J	Operating junction temperature range			–55	175	°C
T_{JOP}	Recommended junction temperature under switching conditions			–55	T_{Jmax} –25	
T_{STG}	Storage temperature range			–60	125	
T_C	Operating case temperature			–55	125	
Torque	Mounting torque	Insert	M2.5	—	0.3	N.m
		To heatsink	M6	3	5	
Wt	Package weight			—	150	g

1.9 Typical SiC MOSFET Performance Curve (Q1 to Q6 and Q8)

The following figures show the performance curves of the Q1 to Q6 and Q8 SiC MOSFETs.

Figure 1-1. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.021	0.270	0.058	0.212	0.034	0.216	0.252
taui (s)	0.003	0.002	0.006	0.013	0.014	0.014	0.050

Figure 1-2. Output Characteristics, T_J = 25 °C

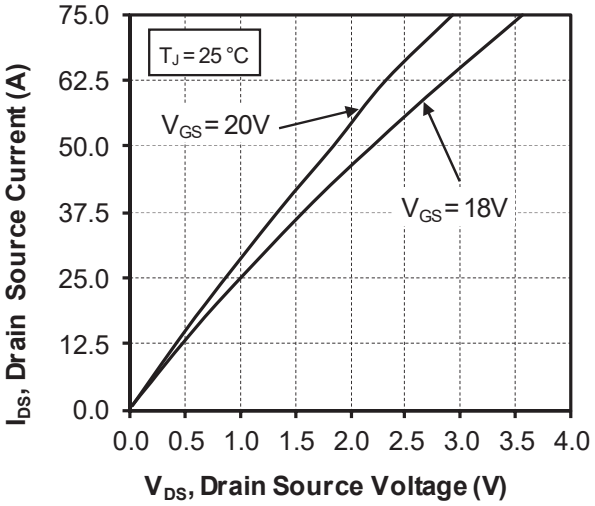


Figure 1-3. Output Characteristics, T_J = 175 °C

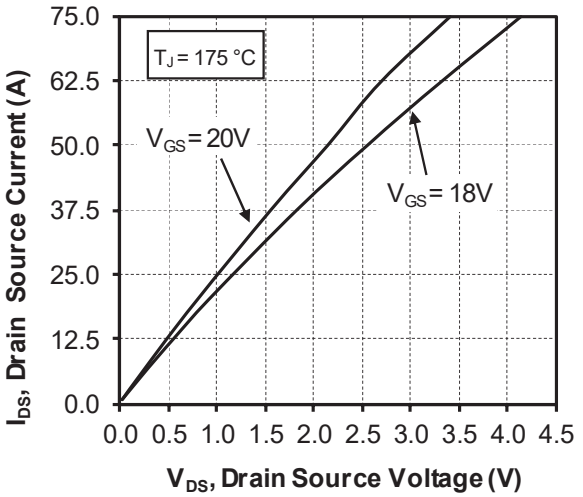


Figure 1-4. Normalized $R_{DS(on)}$ vs. Temperature

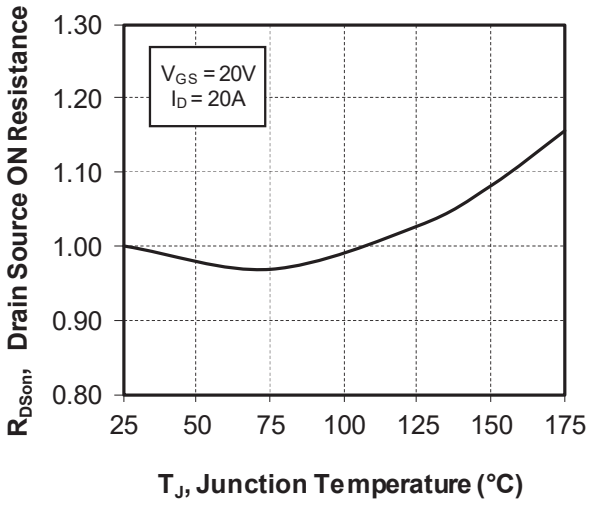


Figure 1-5. Transfer Characteristics

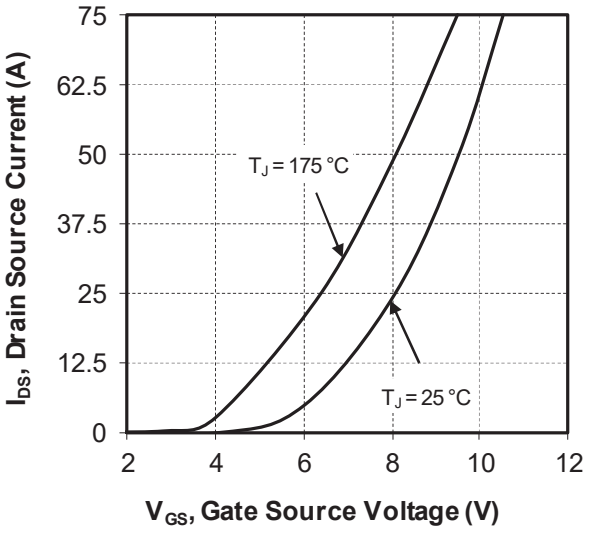


Figure 1-6. Capacitance vs. Drain Source Voltage

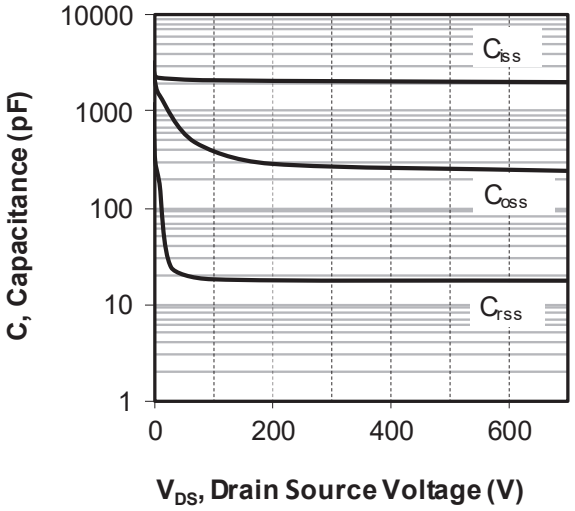


Figure 1-7. Gate Charge vs. Gate Source Voltage

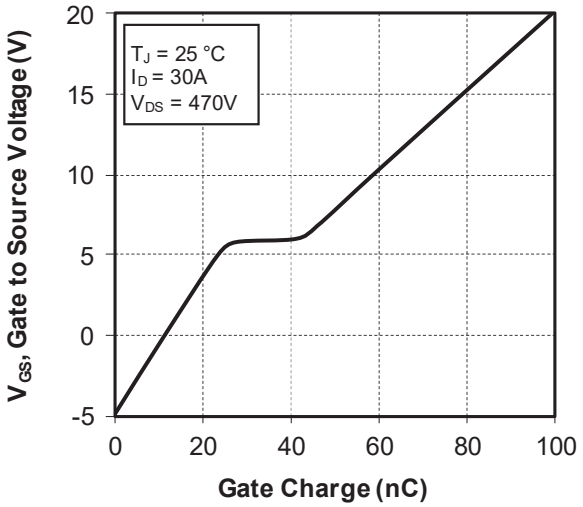


Figure 1-8. Body Diode Characteristics, T_J = 25 °C

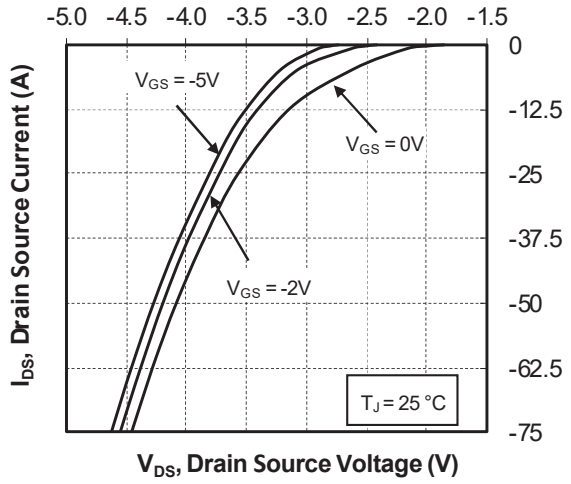


Figure 1-9. 3rd Quadrant Characteristics, T_J = 25 °C

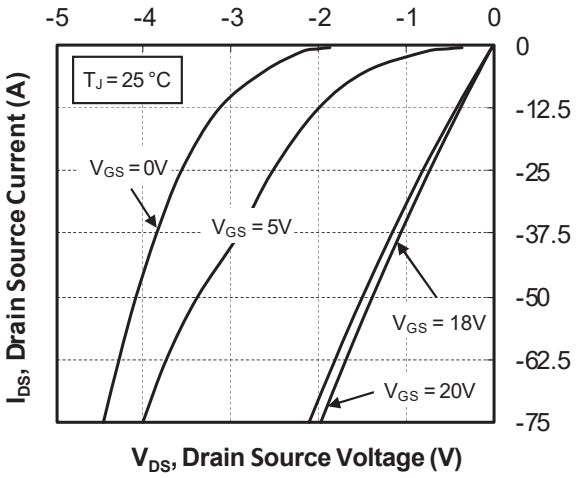


Figure 1-10. Body Diode Characteristics, T_J = 175 °C

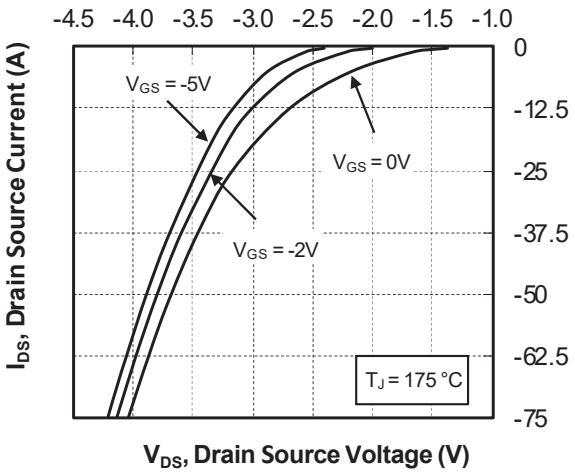


Figure 1-11. 3rd Quadrant Characteristics, T_J = 175 °C

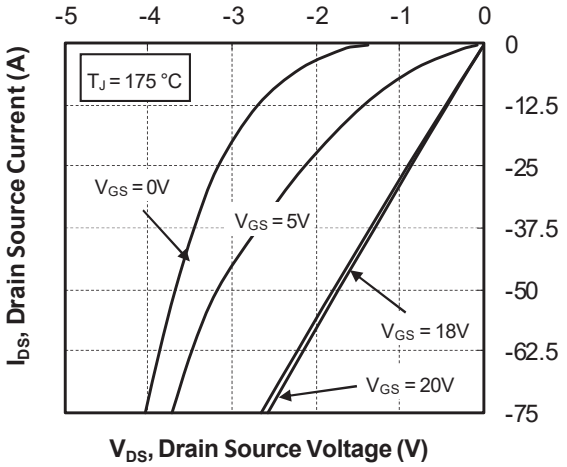


Figure 1-12. Switching Energy vs. Current

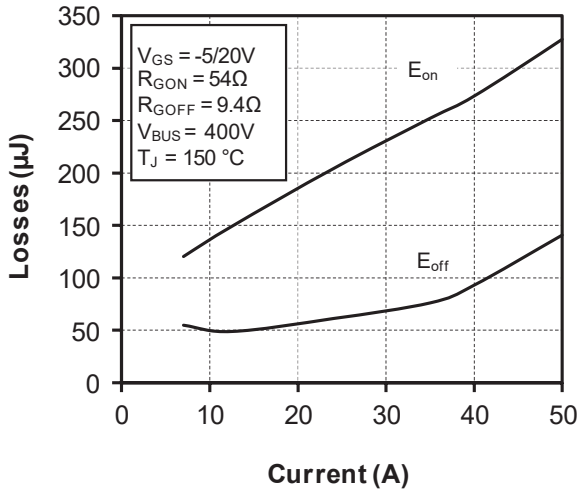


Figure 1-13. Turn-On Energy vs. Rg

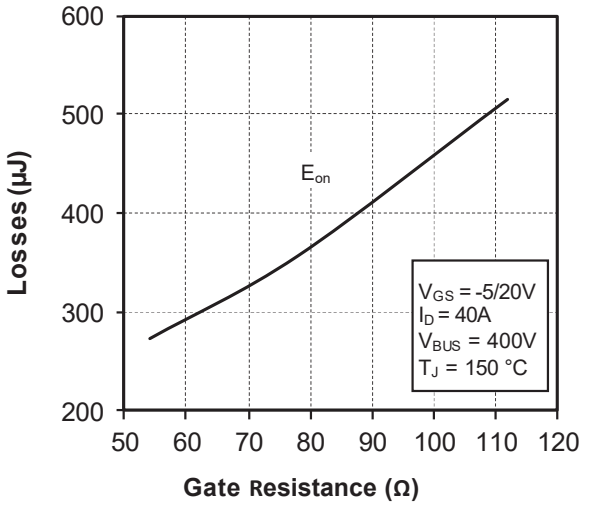


Figure 1-14. Turn-Off Energy vs. Rg

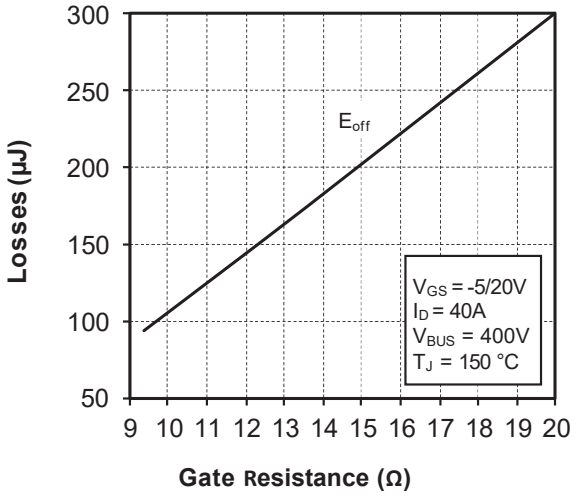
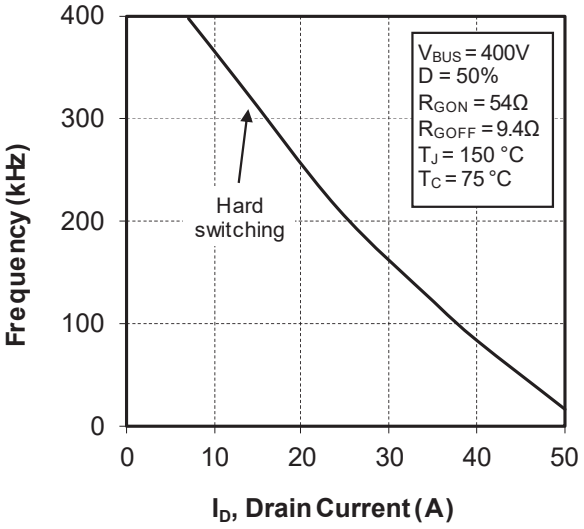


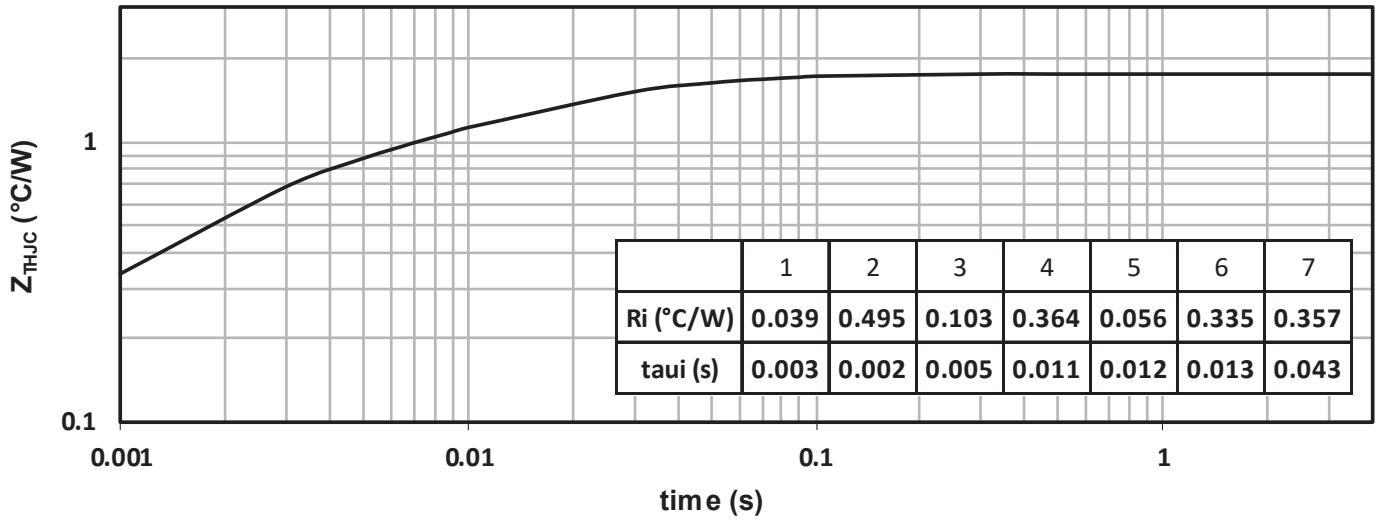
Figure 1-15. Operating Frequency vs. Drain Current



1.10 Typical SiC Diode Performance Curve (CR1 to CR8)

The following figures show the performance curves of the CR1 to CR8 SiC diodes.

Figure 1-16. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.039	0.495	0.103	0.364	0.056	0.335	0.357
taui (s)	0.003	0.002	0.005	0.011	0.012	0.013	0.043

Figure 1-17. Forward Characteristics

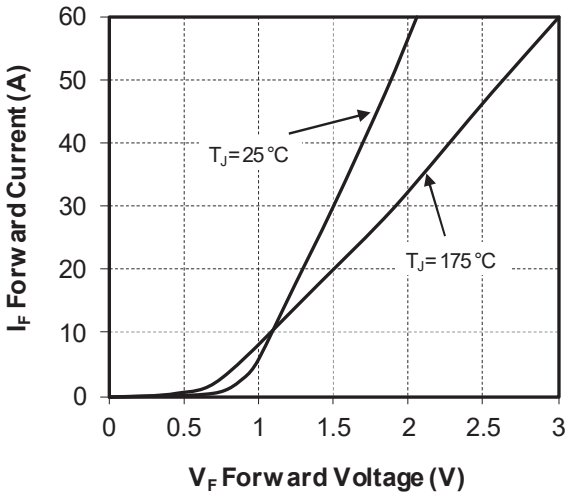
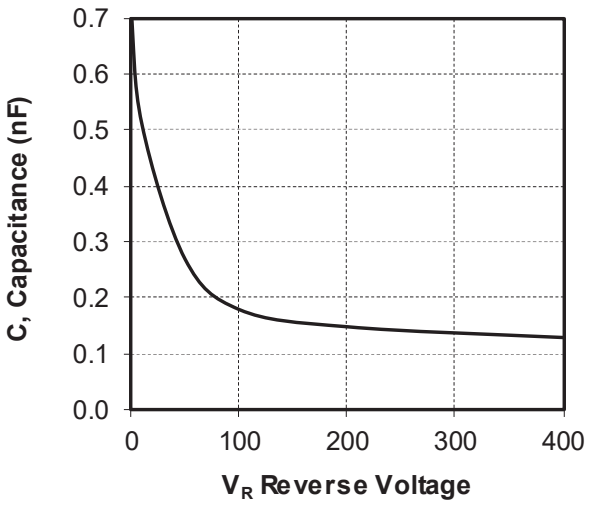


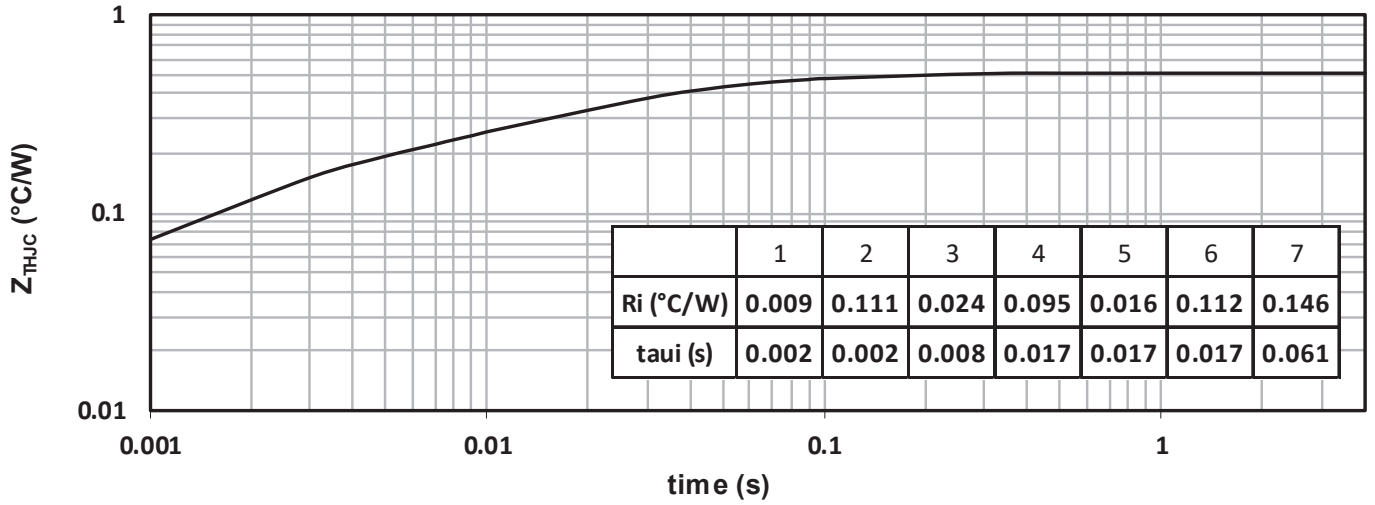
Figure 1-18. Capacitance vs. Reverse Voltage



1.11 Typical SiC MOSFET Performance Curve (Q7)

The following figures show the performance curves of the Q7 SiC MOSFETs.

Figure 1-19. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.009	0.111	0.024	0.095	0.016	0.112	0.146
taui (s)	0.002	0.002	0.008	0.017	0.017	0.017	0.061

Figure 1-20. Output Characteristics, T_J = 25 °C

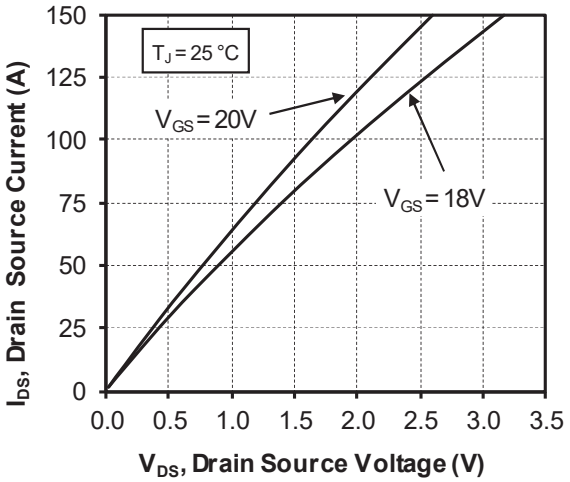


Figure 1-21. Output Characteristics, T_J = 175 °C

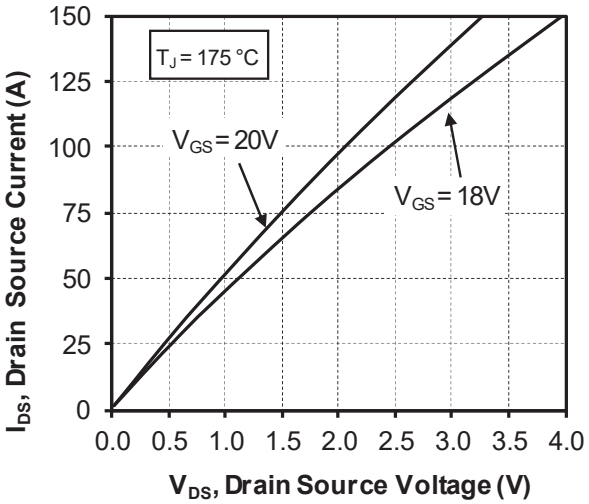


Figure 1-22. Normalized $R_{DS(on)}$ vs. Temperature

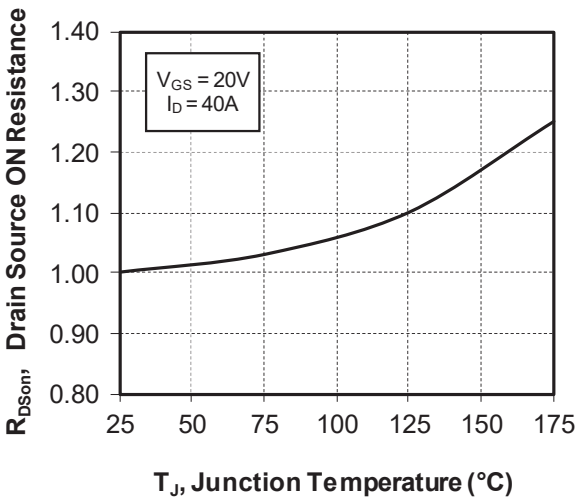


Figure 1-23. Transfer Characteristics

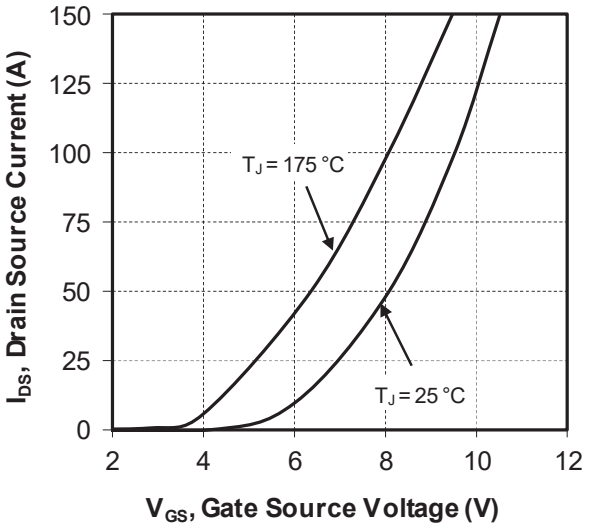


Figure 1-24. Capacitance vs. Drain Source Voltage

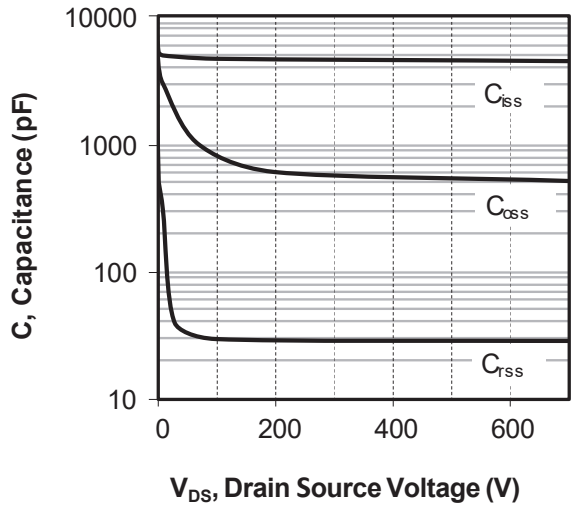


Figure 1-25. Gate Charge vs. Gate Source Voltage

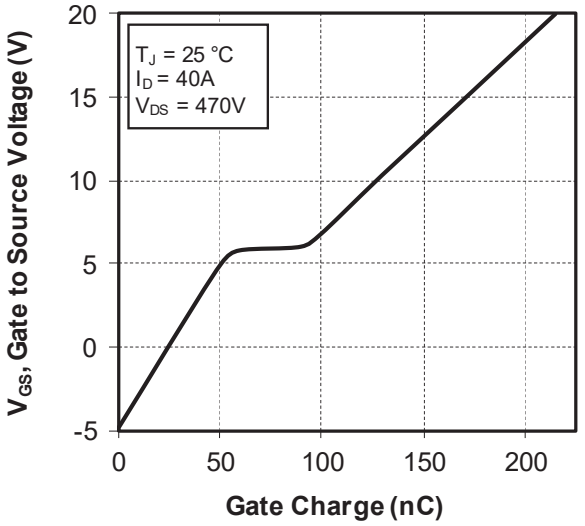


Figure 1-26. Body Diode Characteristics, T_J = 25 °C

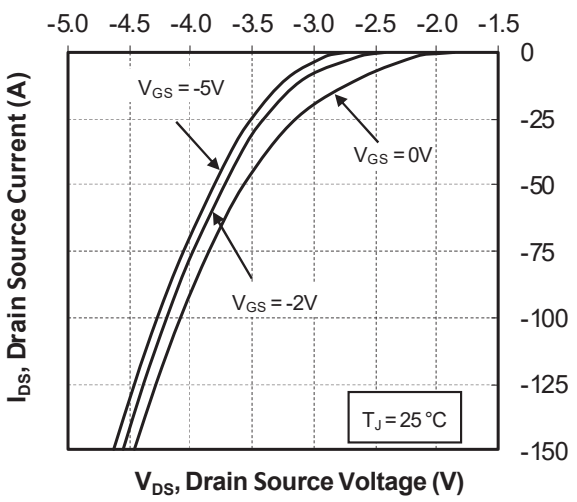


Figure 1-27. 3rd Quadrant Characteristics, T_J = 25 °C

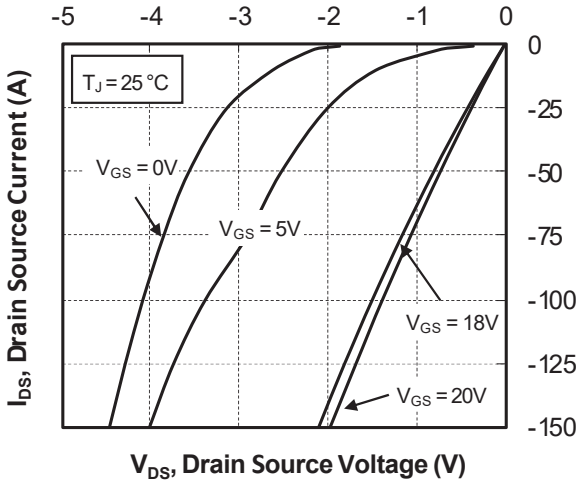


Figure 1-28. Body Diode Characteristics, T_J = 175 °C

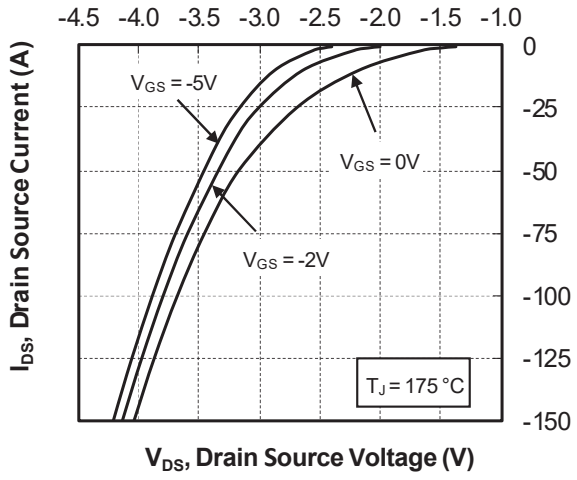


Figure 1-29. 3rd Quadrant Characteristics, T_J = 175 °C

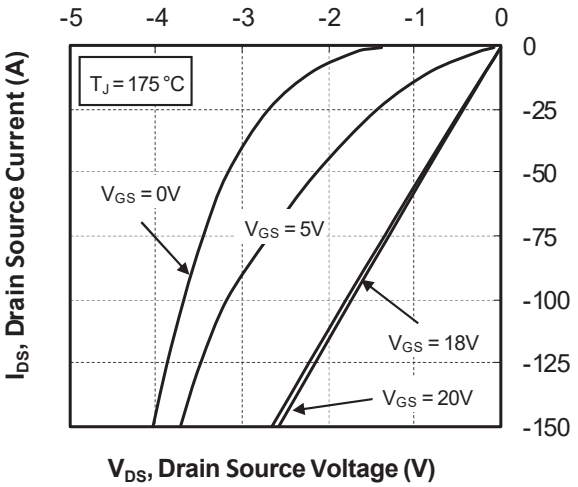


Figure 1-30. Switching Energy vs. Current

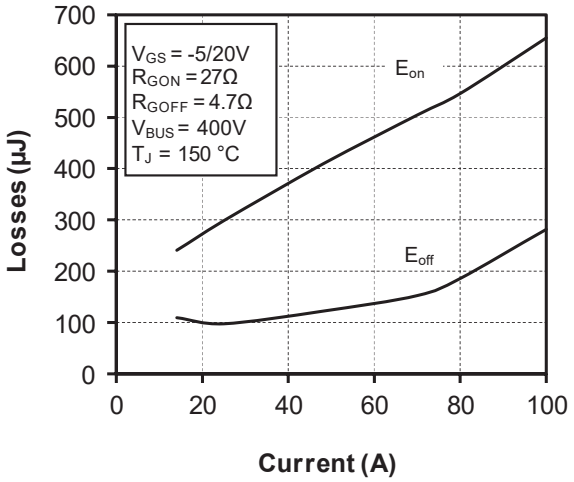


Figure 1-31. Turn-On Energy vs. Rg

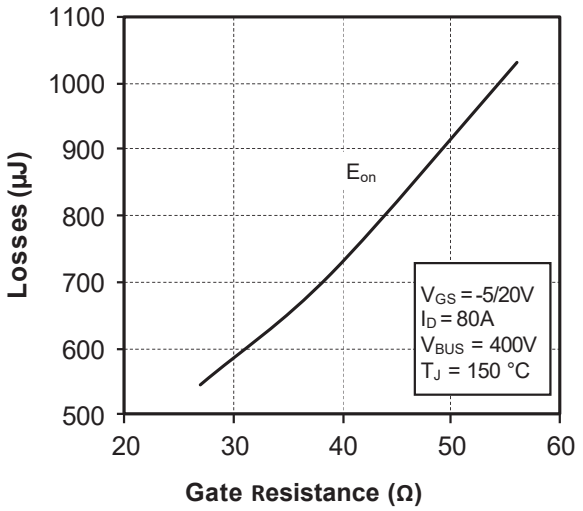


Figure 1-32. Turn-Off Energy vs. Rg

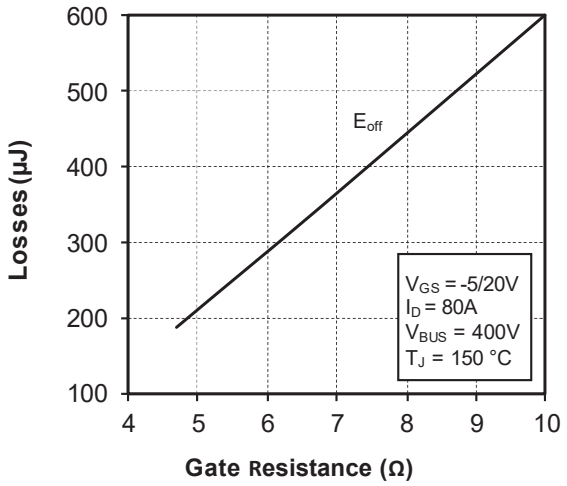
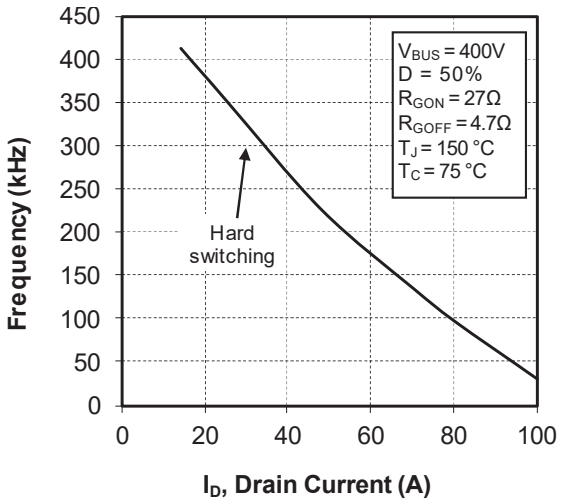


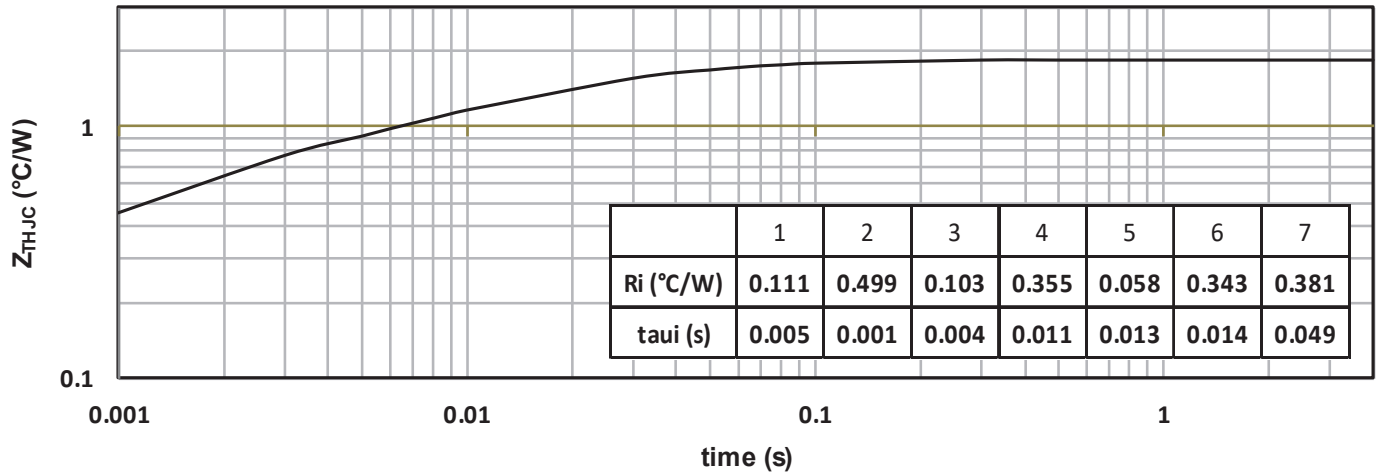
Figure 1-33. Operating Frequency vs. Drain Current



1.12 Typical IGBT Performance Curve (Q9 and Q10)

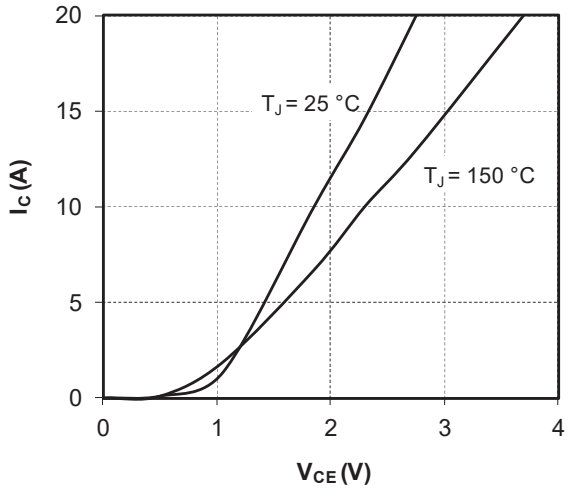
The following figures show the performance curves of the Q9 and Q10 IGBTs.

Figure 1-34. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.111	0.499	0.103	0.355	0.058	0.343	0.381
taui (s)	0.005	0.001	0.004	0.011	0.013	0.014	0.049

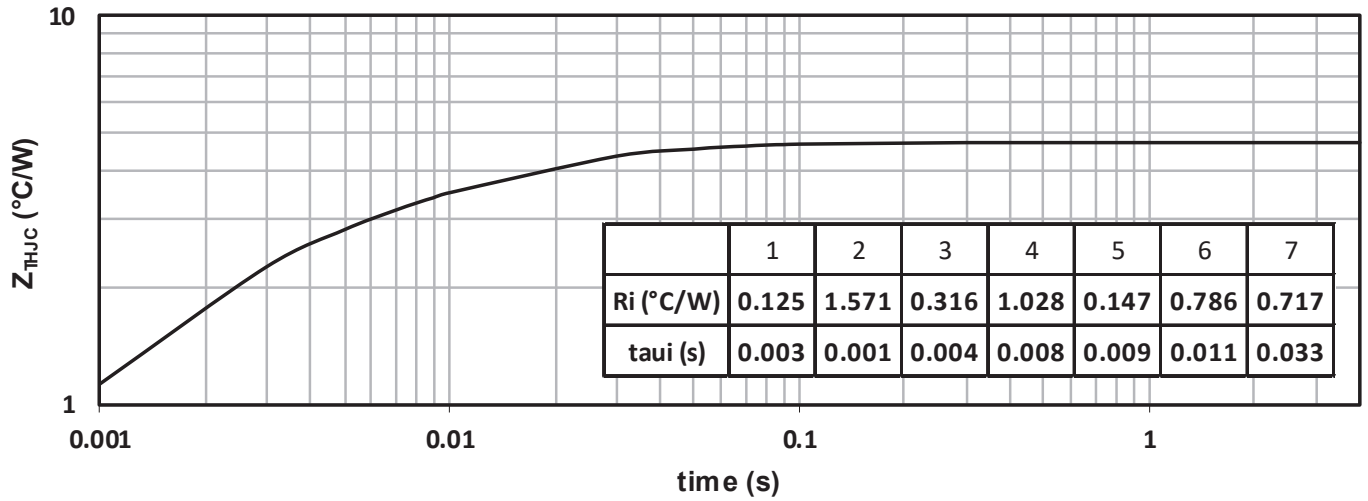
Figure 1-35. Output Characteristics (V_{GE} = 15V)



1.13 Typical SiC Diode Performance Curve (CR9 and CR10)

The following figures show the performance curves of the CR9 and CR10 SiC diodes.

Figure 1-36. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.125	1.571	0.316	1.028	0.147	0.786	0.717
taui (s)	0.003	0.001	0.004	0.008	0.009	0.011	0.033

Figure 1-37. Forward Characteristics

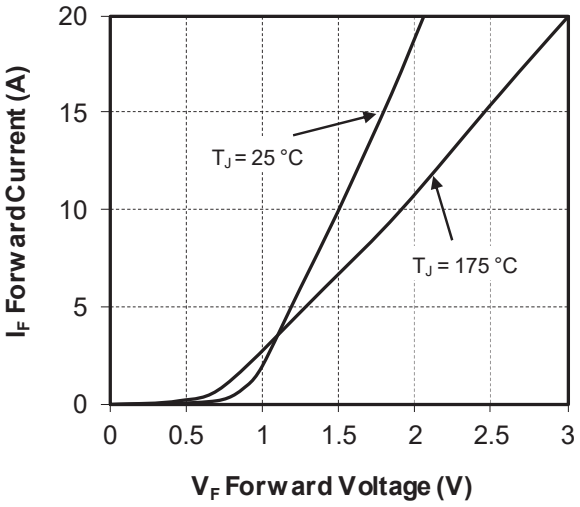
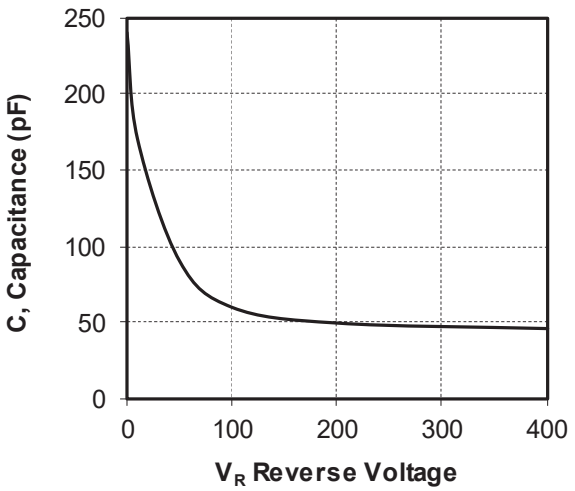


Figure 1-38. Capacitance vs. Reverse Voltage



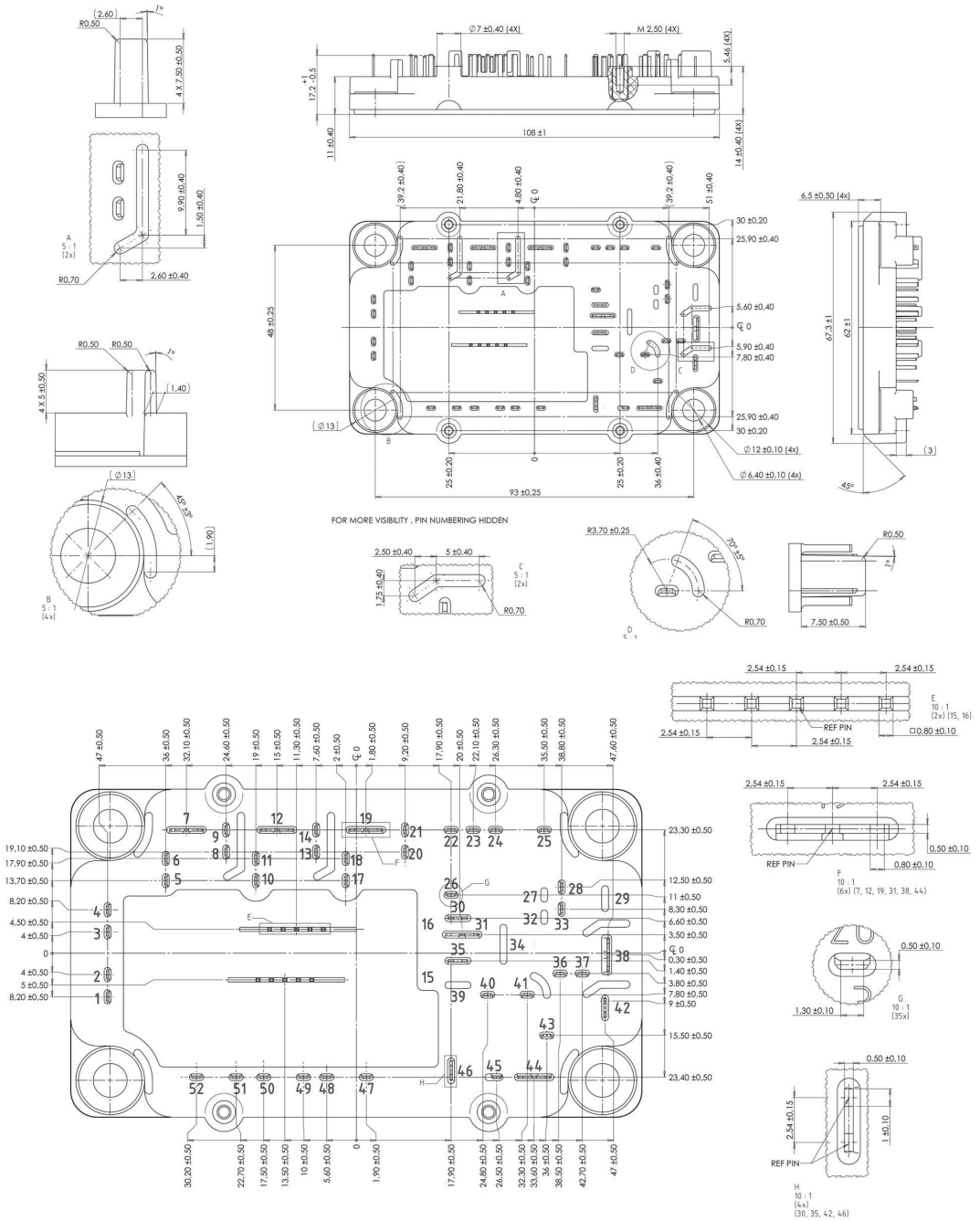
2. Package Specifications

The following section describes the package specification of the MSCSM70XM45CTYZBNMG device.

2.1 Package Outline

The following figure shows the package outline drawing of the MSCSM70XM45CTYZBNMG device. The dimensions in the following figure are in millimeters.

Figure 2-1. Package Outline Drawing



3. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	04/2023	Initial revision

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