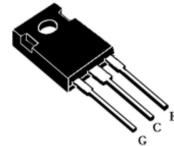


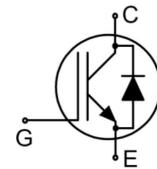
Features

- Fast Switching & Low $V_{CE(sat)}$
- High Input Impedance
- $V_{CE(sat)} = 1.88V @ IC = 40A$
- High Input Impedance
- Short circuit withstand time 10 μs



Applications

- PFC
- UPS
- Inverter
- Welding Machine



Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Collector-emitter voltage	Gate-emitter voltage	V_{CES}	650	V
Collector current		V_{GES}	± 20	
Collector current	$T_C=25^\circ C$	I_C	80	A
	$T_C=100^\circ C$		40	
Pulsed collector current, pulse time limited by T_{jmax}	Diode forward current @ $T_C = 100^\circ C$	I_{CM}	120	
Diode pulsed current, Pulse time limited by T_{jmax}		I_{FM}	20	
Power dissipati	$T_C=25^\circ C$	P_D	60	W
	$T_C=100^\circ C$		280	
Operating Junction and storage temperature rang		T_J	-55 to 175	°C
		T_{stg}	-55 to 175	

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance junction-to-ambien	$R_{\theta JA}$	62.5	°C/W
Thermal resistance junction-to-case for IGBT	$R_{\theta JC}$	0.75	
Thermal resistance junction-to-case for Diod	$R_{\theta JD}$	1.8	

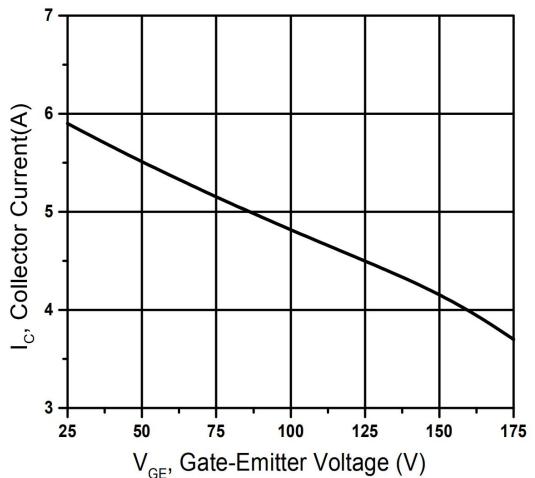
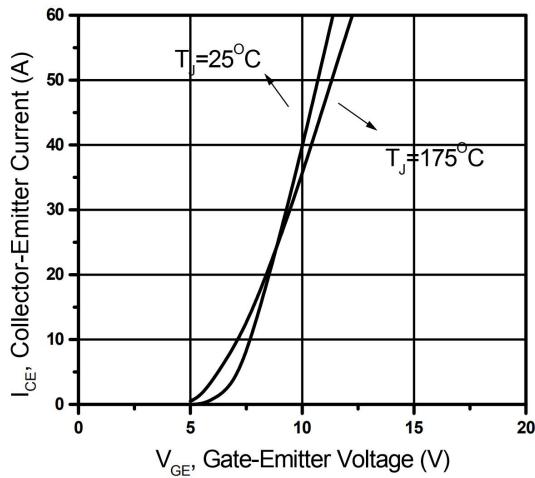
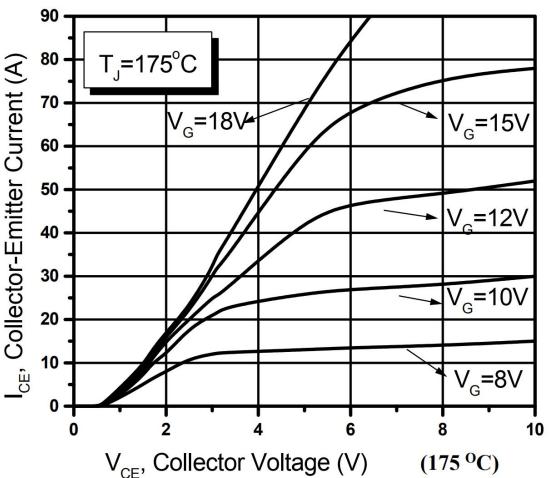
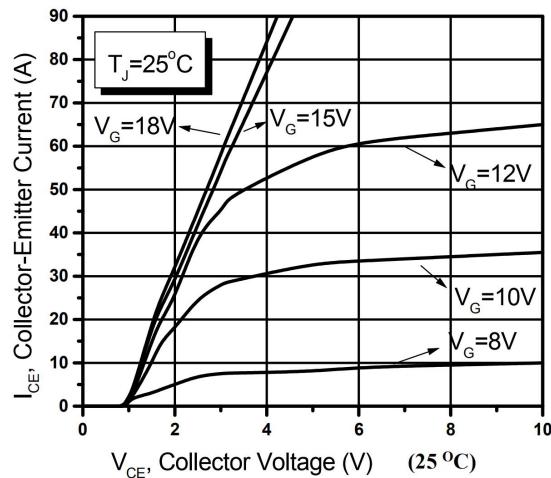
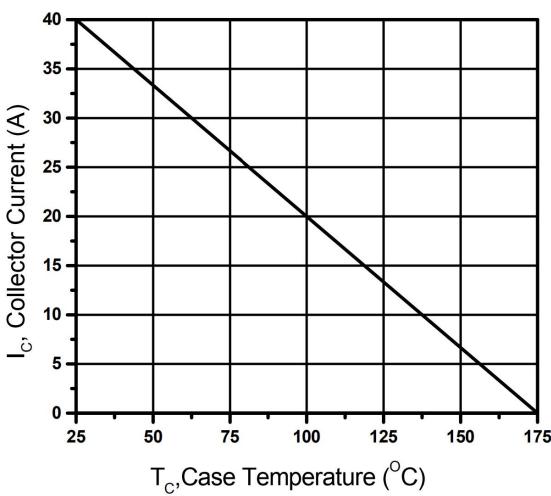
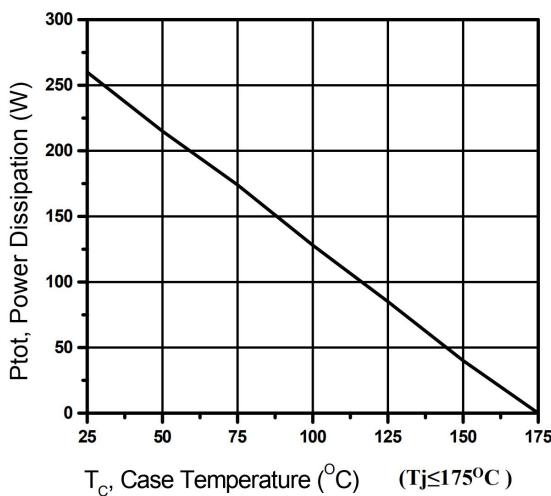
① These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heat sink, assuming maximum junction temperature of $T_{J(MAX)}=175^\circ C$.

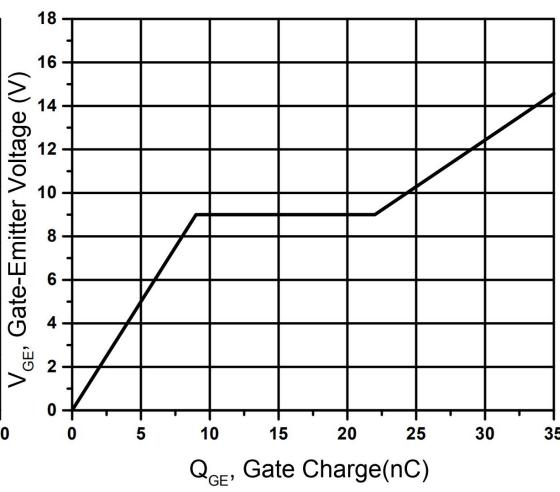
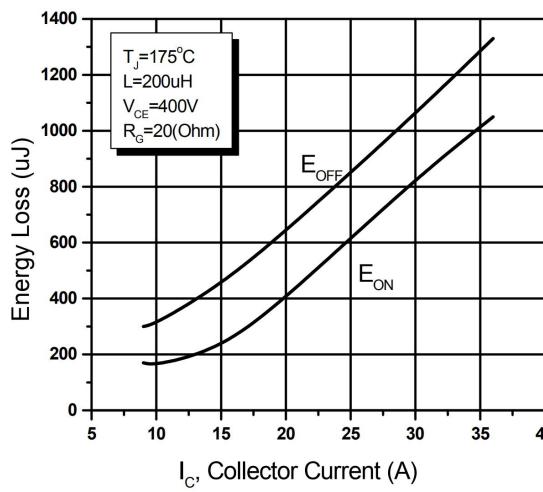
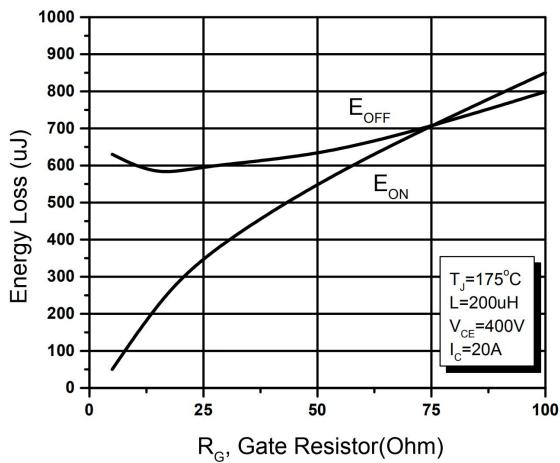
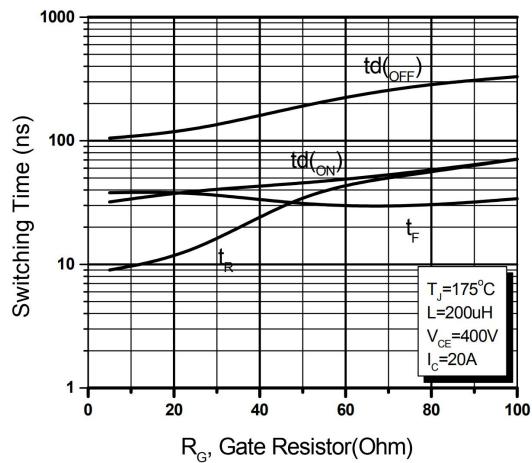
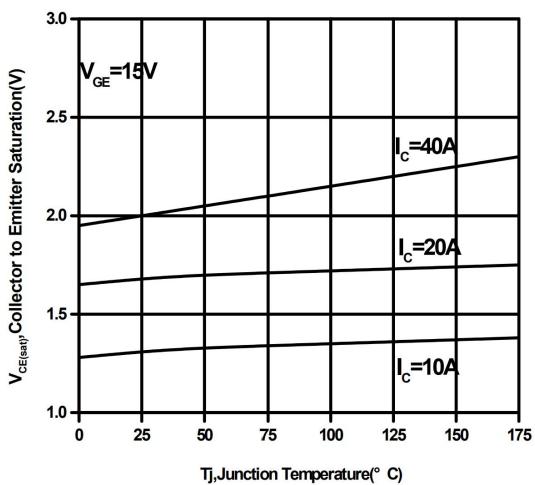
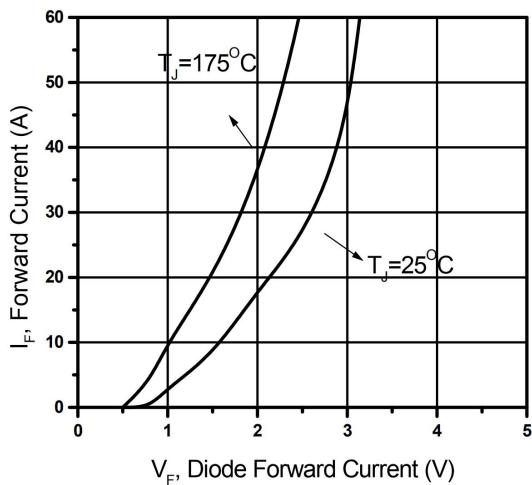
② The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.

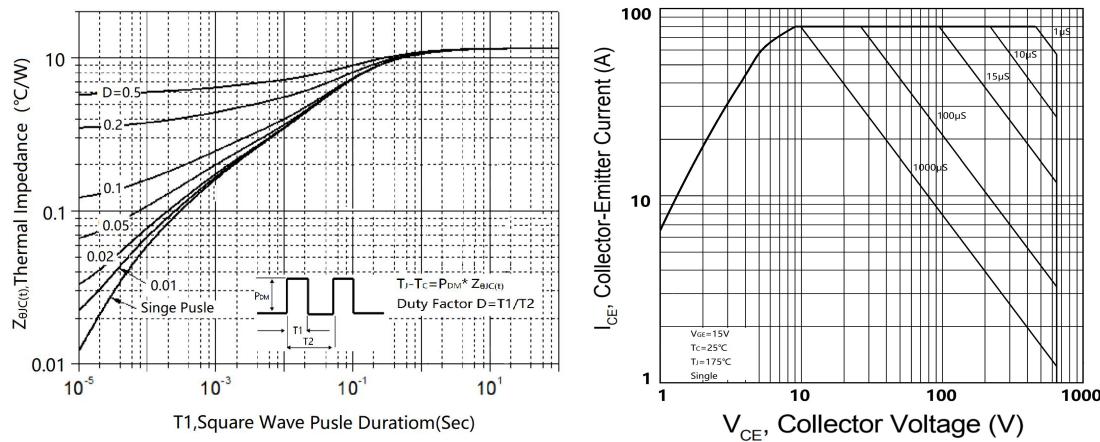
Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Collector-emitter breakdown voltage	BV_{CES}	$I_C = 500 \mu\text{A}, V_{GE} = 0\text{V}$	650	-	-	V
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE} = V_{GE}, I_C = 250 \mu\text{A}$	4.0	5.7	7	
Zero gate voltage collector current	I_{CES}	$V_{CE} = 650\text{V}, V_{GE} = 0\text{V}$	-		1000	μA
Gate-emitter leakage current	I_{GES}	$V_{GE} = 20\text{V}, V_{CE} = 0\text{V}$	-	-	± 250	nA
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$I_C = 40\text{A} V_{GE} = 15\text{V} T_c = 25^\circ\text{C}$	-	1.88	2.36	V
		$I_C = 40\text{A}, V_{GE} = 15\text{V}, T_c = 150^\circ\text{C}$	-	2.4	-	
Dynamic and Switching Characteristi						
Total gate charg	Q_g	$V_{CE} = 400\text{V}, I_C = 40\text{A}, V_{GE} = 15\text{V}$	-	165	-	nC
Input capacitanc	C_{ies}	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$	-	3155	-	pF
Reverse transfer capacitanc	C_{res}		-	81.5	-	
Output capacitance	C_{oes}		-	175	-	
Turn-on delay time	$t_{d(\text{on})}$	$V_{GE} = 15\text{V}, V_{CC} = 400\text{V}, I_C = 40\text{A}, R_G = 10\Omega$ Inductive Load, $T_c = 25^\circ\text{C}$	-	45	-	nS
Rise tim	t_r		-	50	-	
Turn-off delay time	$t_{d(\text{off})}$		-	210	-	
Fall time	t_f		-	55	-	
Turn-on switching energy	E_{on}		-	1.6	-	mJ
Turn-off switching energy	E_{off}		-	0.7	-	
Total switching energ	E_{ts}		-	2.3	-	
Turn-on delay time	$t_{d(\text{on})}$	$V_{GE} = 15\text{V}, V_{CC} = 400\text{V}, I_C = 40\text{A}, R_G = 10\Omega$ Inductive Load, $T_c = 125^\circ\text{C}$	-	75	-	nS
Rise tim	t_r		-	80	-	
Turn-off delay time	$t_{d(\text{off})}$		-	305	-	
Fall time	t_f		-	108	-	
Turn-on switching energy	E_{on}		-	2.1	-	mJ
Turn-off switching energ	E_{off}		-	1.4	-	
Total switching energ	E_{ts}		-	1.5	-	
Short circuit safer operation area	SCSOA	$T_J=125^\circ\text{C} V_{CE}=300\text{V} I_C=40\text{A}$ $R_g=22\text{ohm} V_{GE}=0-15\text{V}$	10	-	-	μs
Diode Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)						
Forward voltag	V_F	$I_F=20\text{A}, T_c=25^\circ\text{C}$	-	1.8	-	V
		$I_F=20\text{A}, T_c=125^\circ\text{C}$	-	1.6	-	
Reverse recovery time	t_{rr}	$I_F=20\text{A}, di/dt=200\text{A}/\mu\text{s}$ $T_c=25^\circ\text{C}$	-	41	-	nS
Reverse recovery current	I_{rr}		-	13.3	-	A
Reverse recovery charge	Q_{rr}		-	310	-	nC

Typical Performance Characteristic







Package outline dimension

