MIL-PRF-38534 CERTIFIED



HIGH CURRENT, VERY LOW OUTPUT SURFACE MOUNT VOLTAGE REGULATORS

4707 Dey Road Liverpool, N.Y. 13088

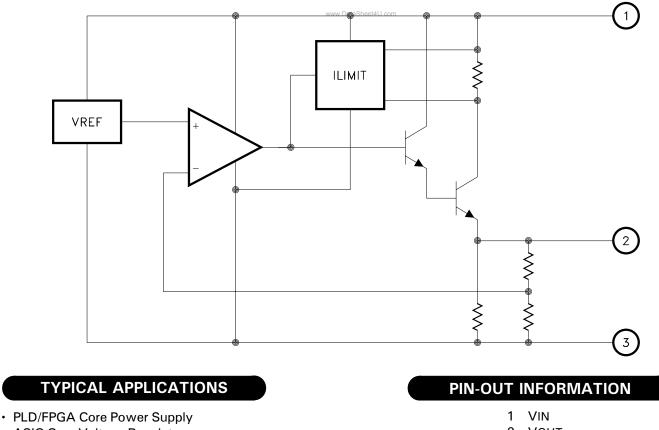
FEATURES:

- Ultra-Fast Transient Response
- Very Low Output Voltage
- Available in 0.8V, 0.9V, 1.0V, 1.2V, 1.3V and 1.5V
- On Board Thermal Shut Down
- **Reverse Battery and Load Dump Protection**
- 1% Maximum Guaranteed Accuracy
- · Output Current to 5 Amps
- Alternate Output Voltages Available
- Ultra-Low Package Height 0.110" Max.
- Contact MSK for MIL-PRF-38534 Qualification Status

DESCRIPTION:

The MSK 5251 series voltage regulators offer high current and low output voltage capability ideal for use with low voltage microprocessors. Low output impedance, very fast transient response and minimal output capacitance requirements make the MSK 5251 an excellent choice for ASIC and FPGA core voltage supplies. The device is available in +0.8V, +0.9V, +1.0V, +1.2V, +1.3V and +1.5V output configurations with output accuracy guaranteed to 1% maximum. The MSK 5251 series is packaged in a low profile 3 pin hermetically sealed power surface mount ceramic package.

EQUIVALENT SCHEMATIC



- ASIC Core Voltage Regulator
- System Power Supplies
- Switching Power Supply Post Regulators
- Battery Powered Equipment

- 2 VOUT
- 3 Ground

(315) 701-6751

SERIES

ABSOLUTE MAXIMUM RATINGS

- V_{IN} Input Voltage +6.5V
- PD Power Dissipation Internally Limited
- Tst Storage Temperature Range -65 °C to +150 °C

ELECTRICAL SPECIFICATIONS

| Parameter | Test Conditions (1) (3) | Group A | MSK 5251 H/E SERIES | | MSK 5251 SERIES | | | Units | |
|--------------------------|--|----------|---------------------|------------|-----------------|------------|------------|-------|------|
| | | Subgroup | Min. | Typ. | Max. | Min. | Тур. | Max. | 00 |
| Input Voltage Range | VOUT < 1V | - | + 3.0 | - | +6.5 | + 3.0 | - | +6.5 | V |
| | VOUT <u>></u> 1V | - | VOUT + 2.1 | - | +6.5 | VOUT + 2.1 | - | +6.5 | V |
| Quiescent Current 2 | IL=0mA | 1 | - | 48 | 60 | - | 48 | 70 | mA |
| | IL = 5.0A | 1 | - | 70 | 120 | - | 70 | 130 | mA |
| Output Voltage Tolerance | VIN = 5V; IOUT = 10mA | 1 | - | ±0.5 | ±1.0 | - | ±0.5 | ±1.2 | % |
| | | 2B,3 | - | ±1.0 | ±2.0 | - | - | - | % |
| Dropout Voltage 2 | \triangle VOUT = -1%; IOUT = 2.5A | 1 | - | 1.5 | 2.0 | - | - | - | V |
| | \triangle VOUT = -1%; IOUT = 5A | 1 | - | 1.7 | 2.1 | - | - | - | V |
| Load Regulation (8) | 10mA <u><</u> IOUT <u><</u> 5.0A | 1 | - | ±0.2 | ±1.0 | - | ±0.2 | ±1.2 | % |
| | | 2,3 | - | ±0.3 | ±2.0 | - | ±0.3 | - | % |
| Line Regulation | VIN Min to +6V | 1 | - | ± 0.05 | ±0.5 | - | ± 0.05 | ±0.6 | % |
| | IOUT = 10mA | 2,3 | - | ±0.5 | ±1.0 | - | ±0.5 | - | % |
| Output Current Limit 28 |) VOUT = 0V | - | - | 7.5 | 9.5 | - | 7.5 | 9.5 | А |
| Thermal Resistance 2 | Junction to Case @ 125°C | - | - | 1.6 | 2.0 | - | 1.6 | 2.2 | °C/W |
| Thermal Shutdown | TJ | - | - | 140 | - | - | 140 | - | °C |
| Turn On Time 2 | COUT = 10uF | 4 | - | 10 | 45 | - | 10 | 55 | uS |

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| PART NUMBER | OUTPUT VOLTAGE ⑦ | | | | |
|----------------|------------------------|--|--|--|--|
| MSK5251-0.8 | +0.8V | | | | |
| MSK5251-0.9 | +0.9V | | | | |
| MSK5251-1.0 | +1.0V | | | | |
| MSK5251-1.2 | +1.2V | | | | |
| MSK5251-1.3 | +1.3V | | | | |
| MSK5251-1.5 | +1.5V | | | | |

NOTES:

① Output decoupled to ground using 10μ F minimum capacitor unless otherwise specified.

- (2) This parameter is guaranteed by design but need not be tested.
- Typical parameters are representative of actual device performance but are for reference only.
- (3) All output parameters are tested using a low duty cycle pulse to maintain TJ = TC.
- ④ Industrial grade and "E" suffix devices shall be tested to subgroup 1 unless otherwise specified.
- ⑤ Military grade devices ('H' suffix) shall be 100% tested to subgroups 1,2,3.

(6) Subgroup 1 $TC = +25^{\circ}C$

Subgroup 2 $TJ = +125^{\circ}C$

Subgroup 2B TC = $+135^{\circ}C$

Subgroup 3 TA = $-55^{\circ}C$

O Please consult the factory if alternate output voltages are required.

(8) Due to internal thermal shutdown, maximum output current may not be available at all values of VIN-VOUT and temperatures. See typical performance curves for clarification.

(9) Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.

 ⁽¹⁰ Seconds Soldering)
TJ Operating Temperature MSK 5251 Series -40°C to +85°C MSK 5251H/E Series -55°C to +125°C

APPLICATION NOTES

REGULATOR PROTECTION:

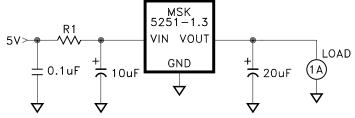
The MSK 5251 series are high performance linear regulators for high current, low voltage applications requiring fast transient response. The devices are fully protected from damage due to fault conditions, offering constant current limiting and thermal shutdown. The thermal shutdown junction temperature is typically 140°C and is 100% tested to verify thermal shutdown occurs above 130°C.

INPUT SUPPLY VOLTAGE:

The input voltage must be maintained at a minimum of 3.0 volts for proper operation for devices with output voltage below 1.0 volt. With an output voltage of 1.0 volt or higher, the input voltage must be a minimum of 2.1 volts above the output.

MINIMIZING POWER DISSIPATION:

To maximize the performance and reduce power dissipation of the MSK 5251 series devices, Vin should be maintained as close to dropout as possible. See Input Supply Voltage requirements. A series resistor can be used to lower Vin close to the dropout specification, lowering the input to output voltage differential. In turn, this will decrease the power that the device is required to dissipate. Knowing peak current requirements and worst case voltages, a resistor can be selected that will drop a portion of the excess voltage and help to distribute the heating. The circuit below illustrates this method.



The maximum resistor value can be calculated from the following:

R1 max = <u>Vin min - (Vout max + Vdrop)</u> lout peak + Quiescent Current

Where: Vin min = Minimum input voltage

Vout max = Maximum output voltage across the full temperature range

Vdrop = Worst case dropout voltage (Typically 2.1 Volts) lout peak = Maximum load current

Quiescent Current = Max. quiescent current at lout peak

INPUT CAPACITOR:

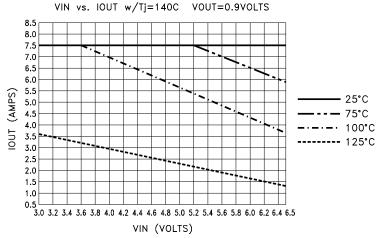
If the device is to be located more than 4 inches from the bulk supply capacitance, a minimum 1uF capacitor should be placed as close to the input pin as possible for proper bypassing. A smaller value capacitor such as 0.01uF should be placed in parallel with the larger value capacitor. Larger input capacitor values will help to improve ripple rejection.

OUTPUT CAPACITOR:

The MSK 5251 series devices require a minimum of external components to maintain stability. A minimum of output capacitance is necessary for stable operation. Due to the wide bandwidth design, the device will operate with a wide range of capacitance and ESR values. For most applications, a 10uF ceramic capacitor will suffice. Ideally, this should be an X7R ceramic capacitor or a tantalum capacitor due to their thermal performance. There is no upper limit to the amount of output capacitance that may be used.

THERMAL SHUTDOWN:

The MSK 5251 series of devices is equipped with a thermal shutdown circuit that will turn off the device when the junction temperature reaches approximately 140°C. It is important for the user to be aware that high temperature operation will limit the current capability of the device due to this thermal shutdown protection. In cases of maximum input voltage and high case temperature, the output current available may be less than 3 Amps. See curve below for clarification.



PACKAGE CONNECTIONS:

The MSK 5251 series are highly thermally conductive devices and the thermal path from the package heat sink to the internal junctions is very short. Standard surface mount soldering techniques should be used when mounting the device. Some applications may require additional heat sinking of the device.

HEAT SINK SELECTION:

To select a heat sink for the MSK 5251, the following formula for convective heat flow may be used:

First, the power dissipation must be calculated as follows:

Power Dissipation = (Vin - Vout) x lout + Vin x Quiescent Current

Next, the user must select a maximum junction temperature. The equation may now be arranged to solve for the required heat sink to ambient thermal resistance (R θ sa).

EXAMPLE:

An MSK 5251-1.3 is configured for Vin = +3.4V and Vout = +1.3V. lout is a continuous 5Amp DC level. Under these conditions the maximum quiescent current would be 120mA. The ambient temperature is +25 °C and the maximum junction temperature is 125 °C.

 $R\theta jc = 2.0 \circ C/W$ and $R\theta cs = 0.5 \circ C/W$ typically.

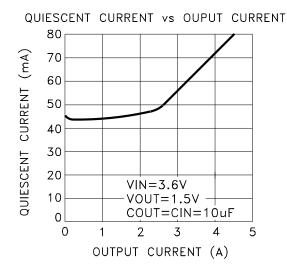
Power Dissipation = $(3.4V - 1.3V) \times (5A + 3.4 \times 120mA)$

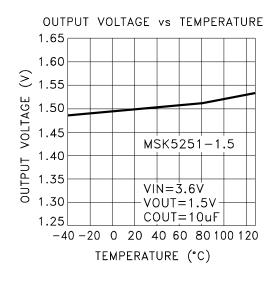
Solve for R θ sa: = 10.9 Watts

$$R\theta sa = \left[\frac{125^{\circ}C - 25^{\circ}C}{1.7W}\right] - 2.0^{\circ}C/W - 0.5^{\circ}C/W$$
$$= 6.67^{\circ}C/W$$

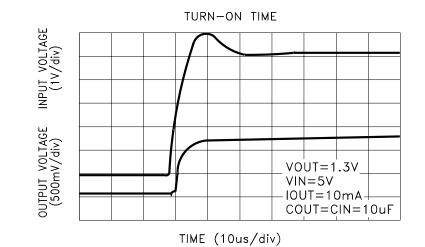
In this example, a heat sink with a thermal resistance of no more than 6.6 °C/W must be used to maintain a junction temperature of no more than 125 °C.

TYPICAL PERFORMANCE CURVES

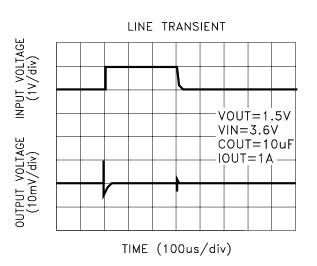




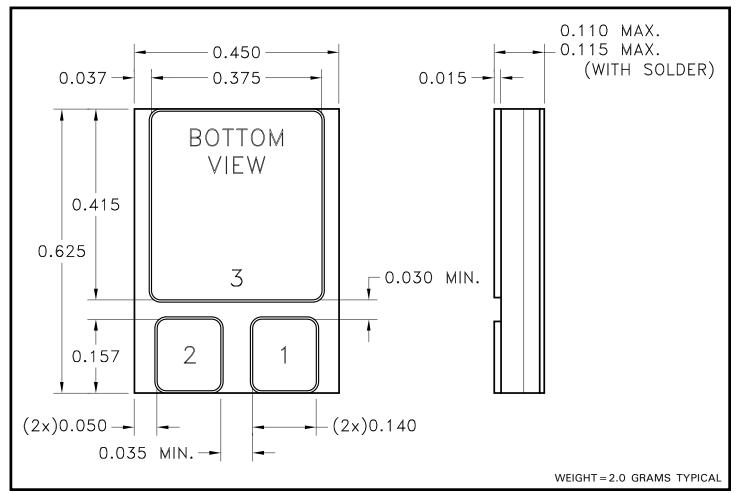
CURRENT LIMIT vs INPUT VOLTAGE 10 9 OUTPUT CURRENT (A) 8 7 6 5 4 3 2 VOUT=0V 1 COUT=10uF 0∟ 3 3.5 4 4.5 5 5.5 INPUT VOLTAGE (V)



TIME (10us/div)

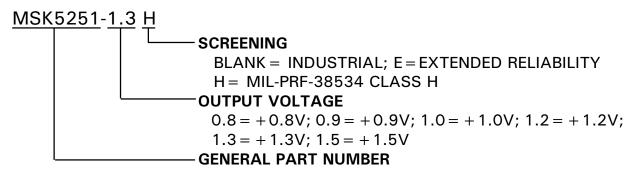


MECHANICAL SPECIFICATIONS



NOTE: ALL DIMENSIONS ARE ±0.010 INCHES UNLESS OTHERWISE LABELED.

ORDERING INFORMATION



The above example is a +1.3V, Military regulator.

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Contact MSK for MIL-PRF-38534 qualification status.