

# **N-Channel Enhancement Mode Field Effect Transistor**

## **FEATURES**

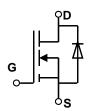
- Super high dense cell design for low RDS(ON)
- Rugged and reliable
- Simple drive requirement
- SOT-23 package

PRODUCT SUMMARY									
$V_{DSS}$ $I_{D}$ $R_{DS(ON)}$ $(m \Omega)$ $Typ$									
20V	151	30@ VGS=4.5V							
20 V	4.5A	50@ VGS=2.5V							



NOTE: The MT2312 is available in a lead-free package





# ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	20	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous <sup>a</sup> @Tj=125℃	ID	3.6	A
- Pulse $d^{b}$	Ідм	12	A
Drain-source Diode Forward Current <sup>a</sup>	Is	1.25	A
Maximum Power Dissipation <sup>a</sup>	PD	1.25	W
Operating Junction and Storage Temperature Range	TJ,Tstg	-55 to 150	$^{\circ}$

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to Ambient <sup>a</sup>	Rth JA	100	°C/W



# ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise noted)

Parameter	Symbol	Symbol Condition			Max	Unit		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V,Id=250µA	20			V		
Zero Gate Voltage Drain Current	Idss	VDS=16V,VGS=0V			1	μД		
Gate-Body Leakage	Igss	V <sub>G</sub> S=±8V,V <sub>D</sub> S=0V			±100	nA		
ON CHARACTERITICS								
Gate Threshold Voltage	V <sub>G</sub> s(th)	$V_{DS}=V_{GS},I_{D}=-250\mu A$	0.5	0.8	1.5	V		
Drain-Source On-State Resistance	Process	Vgs=4.5V,Id=2.8A		30	45	mΩ		
Drain-Source Oil-State Resistance	RDS(ON)	Vgs=2.5V,Id=2.0A		50	60	111 22		
Forward Transconductance	gFS	Vgs=5V,Id=5A		5		S		
DAYNAMIC CHARACTERISTICS								
Input Capacitance	Ciss			586		pF		
Output Capacitance	Coss	$V_{DS}=10V, V_{GS}=0V$ f=1.0MHz		101		pF		
Reverse Transfer Capacitance	Crss	1 1,01,112		59		pF		
SWITCHING CHARACTERISISTICS								
Turn-On Delay Time	td(ON)	V <sub>DD</sub> =10V		6.5		ns		
Rise Time	tr	ID=3.6A,		32.1		ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	V <sub>GEN</sub> =4.5V R <sub>L</sub> =10ohm		58.4		ns		
Fall Time	tf	RGEN=10ohm		48		ns		
Total Gate Charge	<b>Q</b> g			6		nC		
Gate-Source Charge	Qgs	Vds=10V,Id=1A Vgs=4.5V		1.35		nC		
Gate-Drain Charge	Qgd	. 65 115 1		1.5		nC		

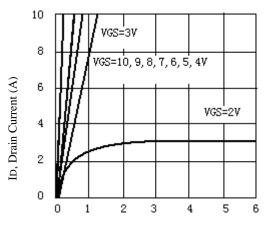


# ELECTRICAL CHARACTERICS (TA=25°C unless otherwise noted)

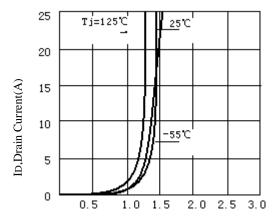
Parameter	Symbol	Condition	Min Typ		Max	Unit				
DRAIN-SOURCE DIODE CHARACTERISTICS										
Diode Forward Voltage	Vsd	Vgs=0V,Is=1.25A		0.84	1.2	V				

#### Notes

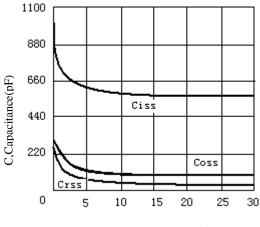
- a. Surface Mounted on FR4 Board,  $t \le 10$ sec
- b. Pulse Test: Pulse Width ≤ 300Us, Duty Cycle ≤ 2%
- c. Guaranteed by design, not subject to production testing.



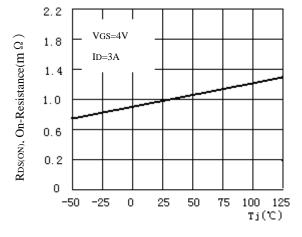
VDS, Drain-to-Source Voltage (V) Figure 1. Output Characteristics



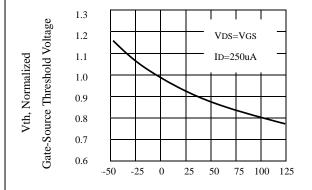
VGS, Gate-to-source Voltage (V) Figure 2. Transfer Characteristics



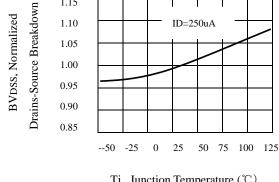
VGS, Drain-to Source Voltage Figure 3. Capacitance



 $\label{eq:continuous} Figure 4. \ \ On-Resistance \ \ Variation \ \ with \\ Temperature$ 

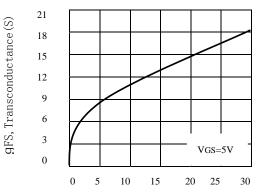


Tj,. Junction Temperature( $^{\circ}$ C) Figure 5. Gate Threshold Variation With Temperature



1.15

Tj, .Junction Temperature (°C) Figure 6. Breakdown Voltage Variation With Temperature



IDS, Drain-Source Current (A) Figure 7. Transconductance Variation

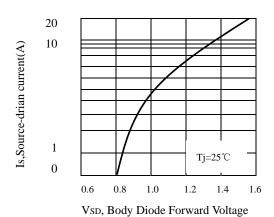


Figure 8. Body Diode Forward Voltage Variation with Source Current

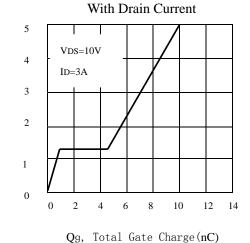
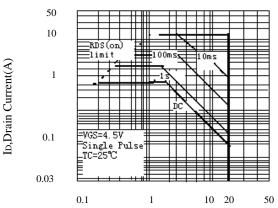
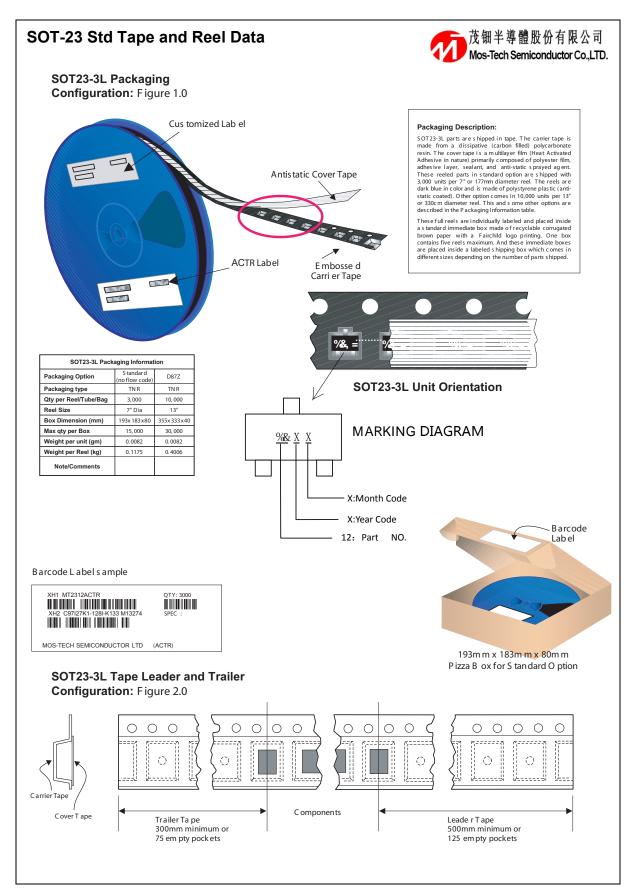


Figure 9. Gate Charge



VDS, Drain-Source Voltage(V) Figure 10. Maximum Safe Operating Area

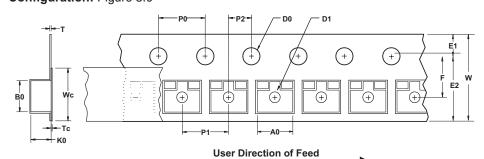
VGS, Gate to Source Voltage



## SOT-23 Std Tape and Reel Data, continued



**SOT23-3L Embossed Carrier Tape Configuration:** Figure 3.0



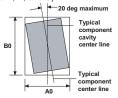
Dimensions are in millimeter														
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	Т	Wc	Тс
<b>SOT-23</b> (8mm)	3.15 +/-0.10	2.77 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.30 +/-0.10	0.228 +/-0.013	5.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).

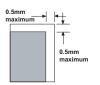


Sketch A (Side or Front Sectional View)

Component Rotation



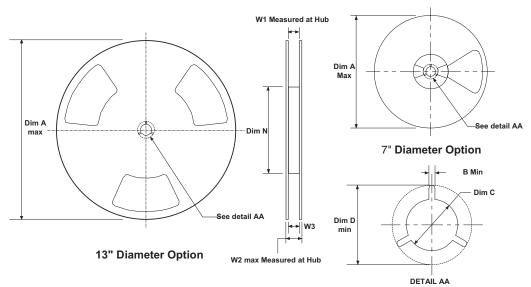
Sketch B (Top View)
Component Rotation



Sketch C (Top View)

Component lateral movement

### SOT23-3L Reel Configuration: Figure 4.0



	Dimensions are in inches and millimeters											
Tape Size Reel Option Dim A Dim B				Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)			
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10. 9			
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10. 9			

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