

MT3205A N-Channel Power MOSFET *\$V, 180A, '.*mΩ

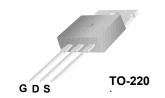
Features

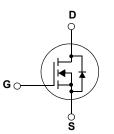
- $R_{DS(on)} = 3.6 m\Omega$ (Typ.)@ $V_{GS} = 10V$, $I_D = 100A$
- High performance trench technology for extermly low R_{DS(on)}
- High power and current handing capability
- · RoHS compliant

Description

 This N-Channel MOSFET is produced using MOS-TECH Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.







MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol		Ratings	Units		
V _{DSS}	Drain to Source Voltage			60	V
V _{GSS}	Gate to Source Voltage			±20	V
I _D	Drain Current	-Continuous (T _C = 25 ^o C)	(Note 1)	120	A
I _{DM}	Drain Current	- Pulsed		470	A
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	397	mJ
P _D	Devuer Dissingtion	(T _C = 25 ^o C)		250	W
	Power Dissipation	- Derate above 25°C		1.0	W/ºC
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +175	°C

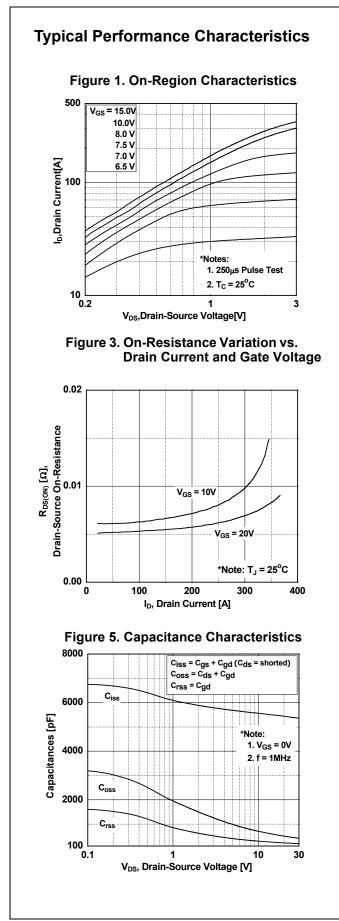
Thermal Characteristics

Symbol	Parameter	Ratings	Units	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case 0.65		°C/W	
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	35	°C/W	

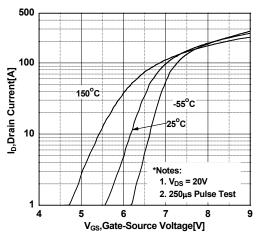
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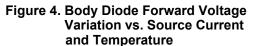
Device MarkingDevicePackateMT3205AMT3205ATO-22		Packa	ge	Reel Size	Ta	ape Width		Quantity		
					-		50units			
Electrica	l Chara	acteristics								
Symbol		Parameter		Т	est Conditi	ons	Min.	Тур.	Max.	Units
, Off Charac	teristic	S								
BV _{DSS}	1	Source Breakdown V	oltage	In = 250uA	, V _{GS} = 0V,	T ₁ = 25	60	-	-	V
			$V_{\rm DS} = 44V, V_{\rm GS} = 0V$			-	_	25		
I _{DSS}	Zero Gate Voltage Drain Current		$V_{DS} = 44V, T_{C} = 150^{\circ}C$			_	_	250	μA	
I _{GSS}	Gate to Body Leakage Current			$V_{\rm DS} = \pm 20V, V_{\rm DS} = 0V$			-	-	±100	nA
On Charac	teristics	5								1
V _{GS(th)}		reshold Voltage		VGS = VDS, I	D = 250µA		2		4	V
30(0)			V _{GS} = 10V, I _D = 100A				3.6	4.5	-	
R _{DS(on)}	Static D	Static Drain to Source On Resistance			$V_{GS} = 10V, I_D = 56A$ $T_J = 175^{\circ}C$			10	-	mΩ
Dynamic C	haracte	eristics								
C _{iss}	Input Capacitance						-	3520	4360	pF
C _{oss}	Output 0	out Capacitance		──V _{DS} = 25V, V _{GS} = 0V f = 1MHz			-	550	760	pF
C _{rss}	Reverse	e Transfer Capacitance	Э			-	340	470	pF	
R _G	Gate Re	esistance		V _{GS} = 0V, f = 1MHz		3	4	5	Ω	
Q _{g(tot)}	Total Ga	ite Charge at 10V		V_{GS} = 0V to 10V V_{GS} = 0V to 2V V_{DS} = 44V		-	121	145	nC	
Q _{g(th)}	Thresho	ld Gate Charge				-	35	46	nC	
Q _{gs}	Gate to	Source Gate Charge				I _D = 59A	-	45	-	nC
Q _{gs2}	Gate Ch	arge Threshold to Pla	iteau	l _g = 1mA			18	-	nC	
Q _{gd}	Gate to Drain "Miller" Charge					-	39	-	nC	
Switching	Charact	teristics								
t _{ON}	Turn-On	Time					-	99	137	ns
t _{d(on)}	Turn-On	Delay Time		V _{DD} = 28V, I _D = 59A V _{GS} = 10V, R _{GEN} = 2.5Ω			-	19	38	ns
t _r		Rise Time					-	127	251	ns
t _{d(off)}	Turn-Off	Delay Time					-	47	73	ns
t _f	Turn-Off	Fall Time					-	19	49	ns
t _{OFF}	Turn-Off	Time					-	67	89	ns
Drain-Sou	rce Dioc	le Characteristic	S							
V _{SD}		Source Diode Forwar		V _{GS} = 0V, I _{SD} = 59A			-	-	1.2	V
t _{rr}	Reverse	Recovery Time		V _{GS} = 0V,	_{SD} = 59A		-	49	-	ns
Q _{rr}	Reverse	Recovery Charge		$dI_F/dt = 100A/\mu s$			-	78	-	nC

Notes:
1: Calculated continuous current based on maximum allowable junction temperature. Package limited to 75A continuous, see Figure 9.
2: L = 0.21mH, I_{AS} = 59A, V_{DD} = 50V, V_{GS} = 10V, R_G = 25Ω, Starting T_J = 25°C









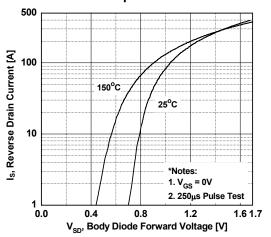
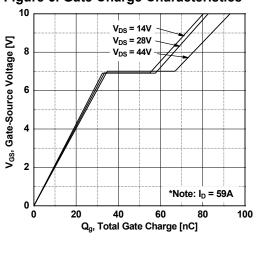
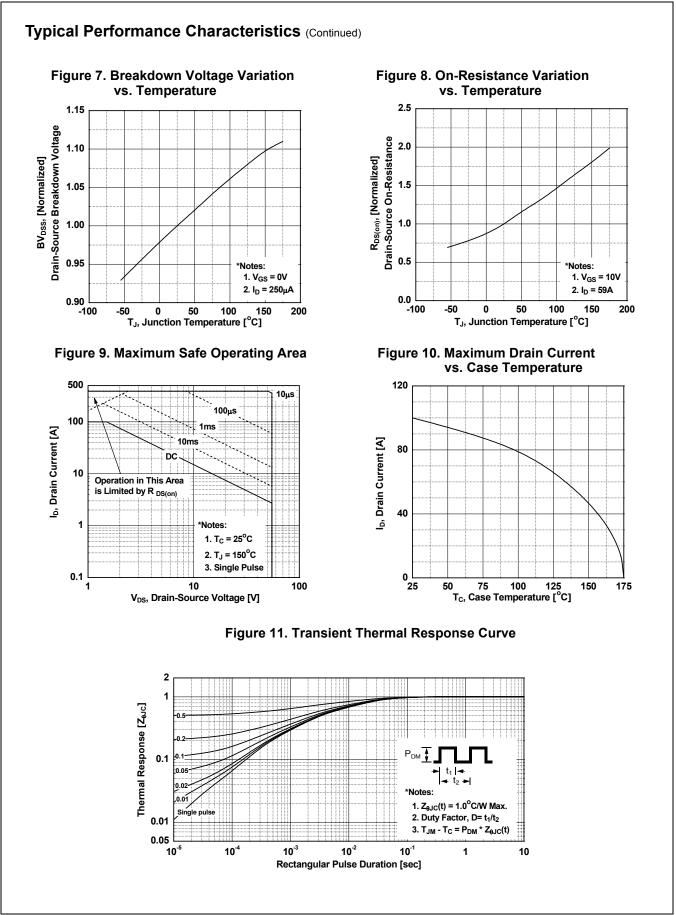
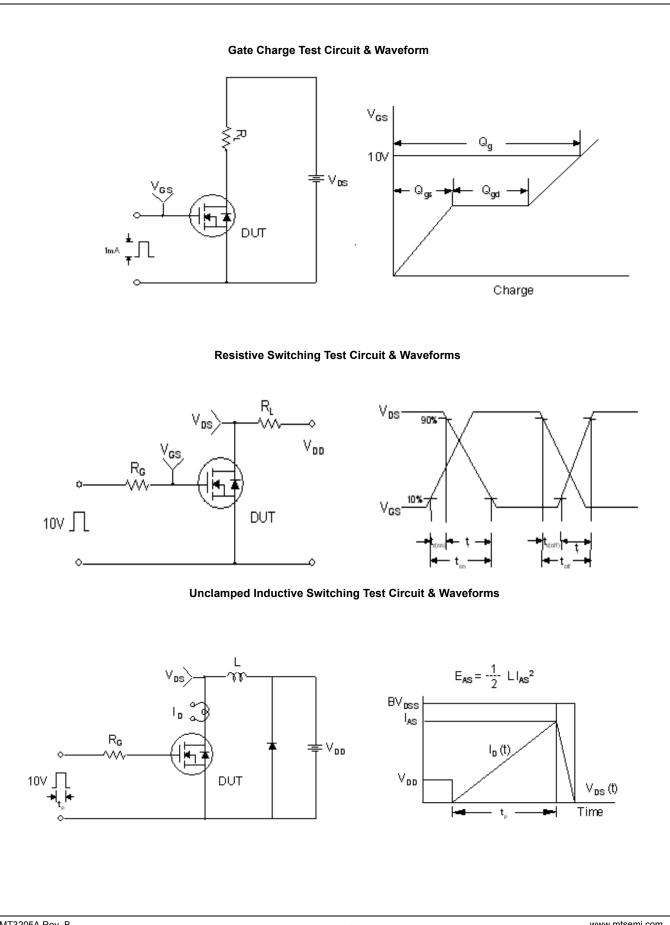


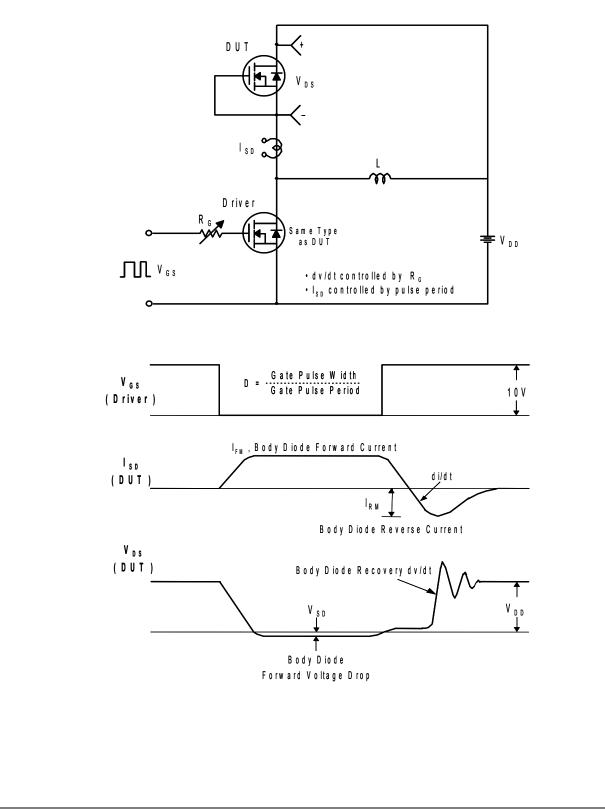
Figure 6. Gate Charge Characteristics

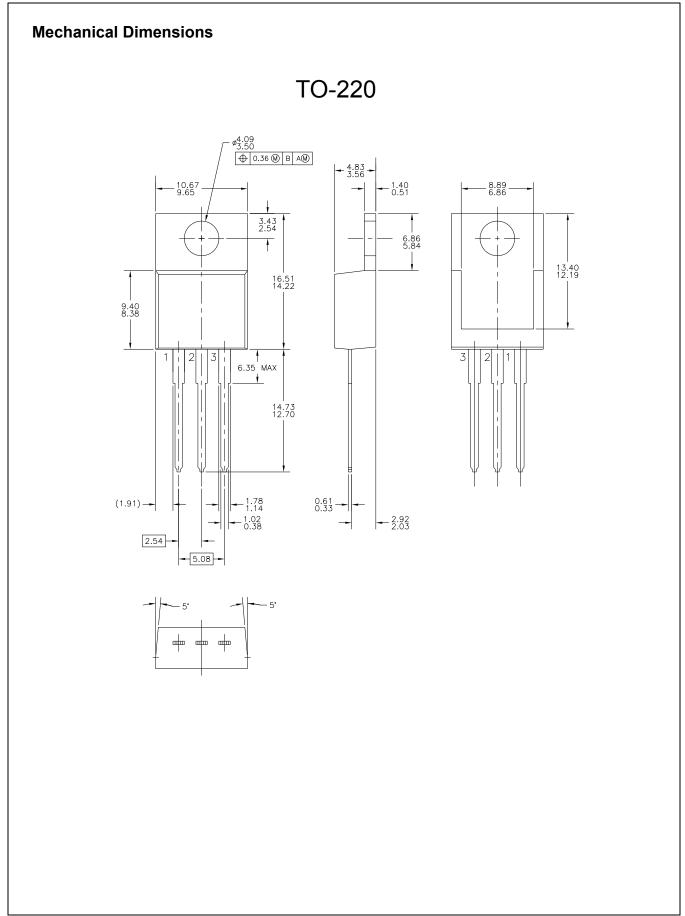


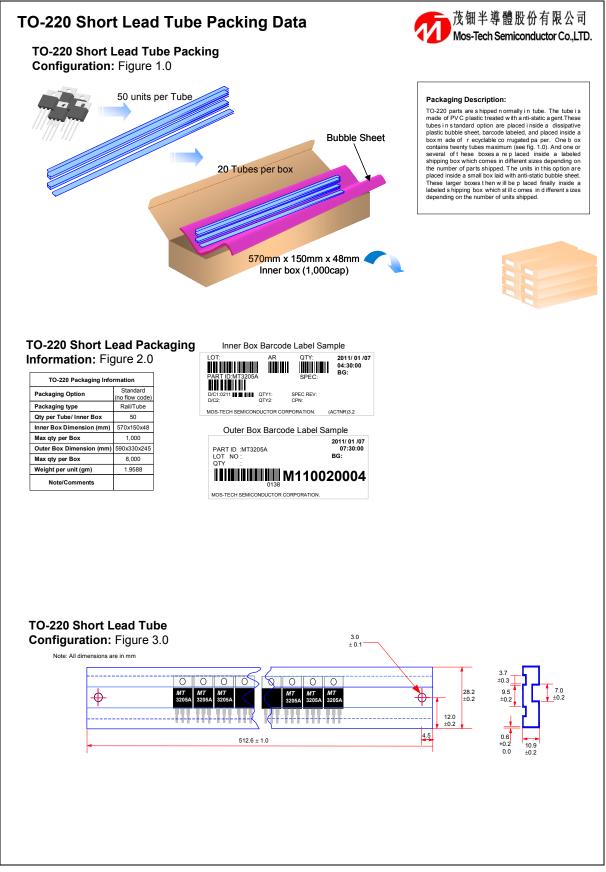














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