MT3(&1 Single P-Channel Power MOSFET

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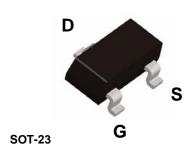
General Description

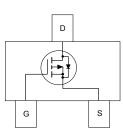
This P-Channel Power MOSFET is produced using MOS-TECH Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state r esistance and yet maintain low gate charge for superior switching performance.

These devices are well suit ed for portable electronics applications: load s witching and power management, battery charging circuits and DC/DC conversion.

Features

- -4.3 A, -25 V. $R_{DS(ON)}$ = 0.08 Ω @ V_{GS} = -4.5 V $R_{DS(ON)}$ = 0.11 Ω @ V_{GS} = -2.5 V
- Low gate charge (3.6 nC typical)
- High performance trench technology for extremely
 low R_{DS(ON)}
- SuperSOTTM -23 provides low R_{DS(ON)} and 30% higher power handling capability than SOT23 in the same footprint





Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		-25	V
V _{GSS}	Gate-Source Voltage		±12	V
I _D	Drain Current – Continuous	(Note 1a)	-4.3	A
	– Pulsed		-10	
PD	Maximum Power Dissipation	(Note 1a)	0.5	W
		(Note 1b)	0.46	
T _J , T _{STG}	Operating and Storage Junction Tem	perature Range	-55 to +150	°C

Thermal Characteristics

R _{0JA}	Thermal Resistance, Junction-to-Ambient	(Note 1a)	250	°C/W
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	75	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity	
010X	MT3421	7"	8mm	3000 units	

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Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHAR	ACTERISTICS		·			
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-25			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient	$I_{\rm D}$ = -250 µA, Referenced to 25 °C		-16		mV /°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{\rm DS} = -16 \text{ V}, \ V_{\rm GS} = 0 \text{ V}$			-1	μA
		T _J = 55°C			-10	μA
I _{GSSF}	Gate - Body Leakage, Forward	V _{GS} = 8 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate - Body Leakage, Reverse	$V_{GS} = -8 V, V_{DS} = 0 V$			-100	nA
ON CHARA	CTERISTICS (Note 2)		•			<u>.</u>
V _{GS(th)}	Gate Threshold Voltage	$V_{\rm DS} = V_{\rm GS}, \ I_{\rm D} = -250 \ \mu {\rm A}$	-0.6	-0.8	-1.2	V
$\Delta V_{GS(th)} / \Delta T_J$	Gate Threshold Voltage Temp. Coefficient	$I_{\rm D}$ = -250 µA, Referenced to 25 °C		3		mV /°C
R _{DS(ON)}	Static Drain-Source On-Resistance	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -1.3 \text{ A}$		0.8	0.9	Ω
		T _J =125°C		0.12	0.15	-
		V _{GS} = -2.5 V, I _D = -1.1 A		0.11	0.15	
I _{D(ON)}	On-State Drain Current	$V_{GS} = -4.5 V, V_{DS} = -5 V$	-4.3			А
g _{FS}	Forward Transconductance	$V_{DS} = -4.5 \text{ V}, \ \text{I}_{D} = -2 \text{ A}$		4		S
DYNAMIC C	HARACTERISTICS					
C _{iss}	Input Capacitance	$V_{DS} = -10 V, V_{GS} = 0 V,$ f = 1.0 MHz		330		pF
C _{oss}	Output Capacitance	f = 1.0 MHz		80		pF
C _{rss}	Reverse Transfer Capacitance			35		pF
SWITCHING	CHARACTERISTICS (Note 2)					
t _{D(on)}	Turn - On Delay Time	$V_{DD} = -5 V, I_{D} = -0.5 A,$		7	15	ns
t,	Turn - On Rise Time	V_{GS} = -4.5 V, R_{GEN} = 6 Ω		12	22	ns
t _{D(off)}	Turn - Off Delay Time			16	26	ns
t _r	Turn - Off Fall Time			5	12	ns
Q _g	Total Gate Charge	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -2 \text{ A},$ $V_{GS} = -4.5 \text{ V}$		3.6	5	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = -4.5 V$		0.8		nC
Q_{gd}	Gate-Drain Charge			0.7		nC
DRAIN-SOU	IRCE DIODE CHARACTERISTICS AND MAX					
I _s	Maximum Continuous Drain-Source Diode Fo	rward Current			-0.42	А
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_{S} = -0.42 A$ (Note)		-0.7	-1.2	V

Note:

1. R_{8,4} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the s older mounting surface of the drain pins. R_{8,c} is guaranteed by design while $\mathsf{R}_{_{\theta CA}}$ is determined by the user's board design.



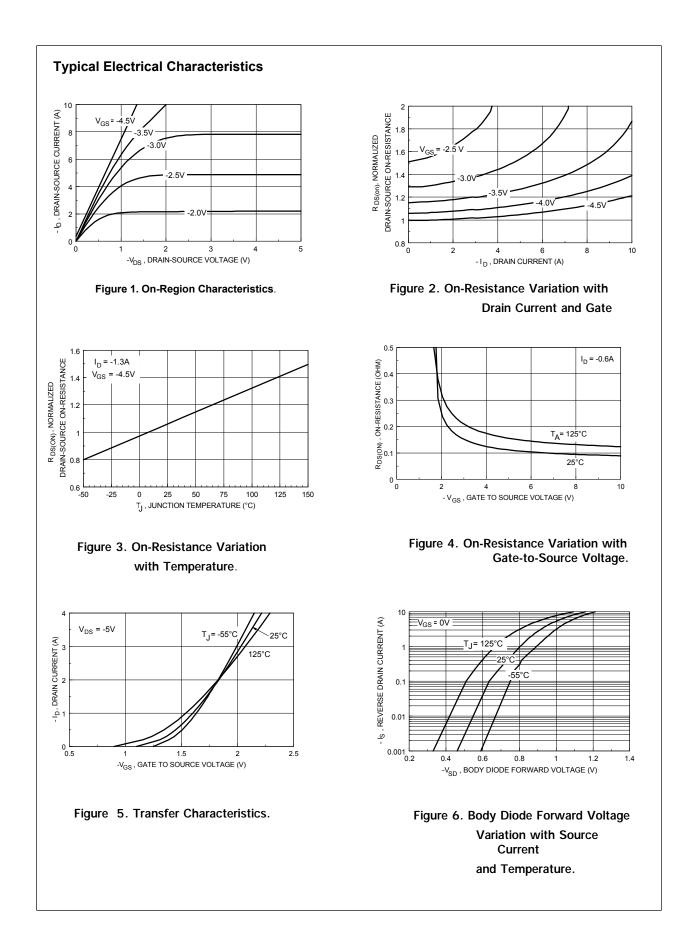
a. 250°C/W when mounted on a 0.02 in² pad of 2oz Cu.

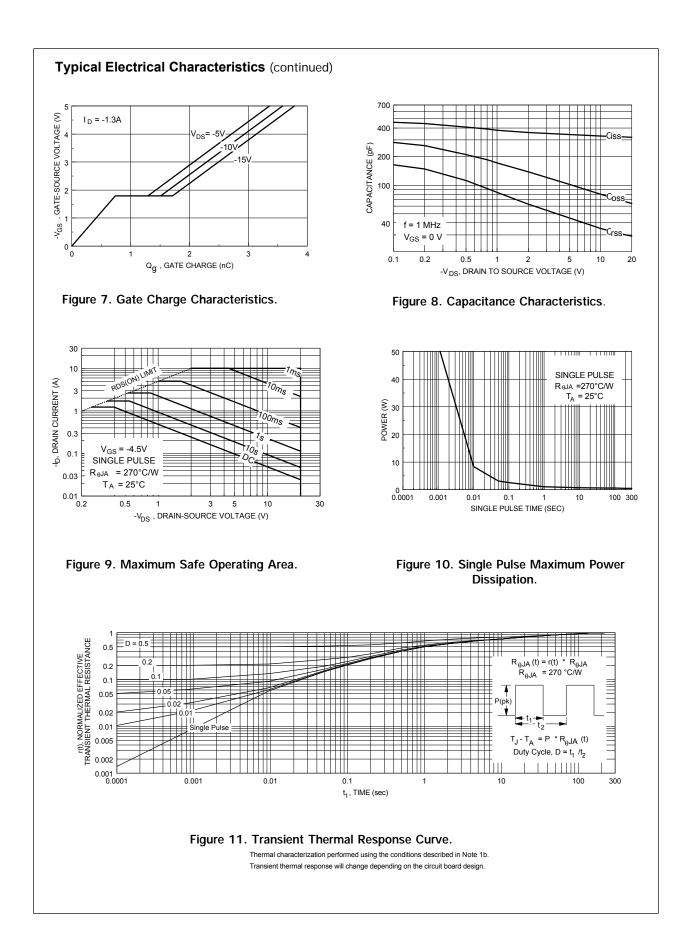


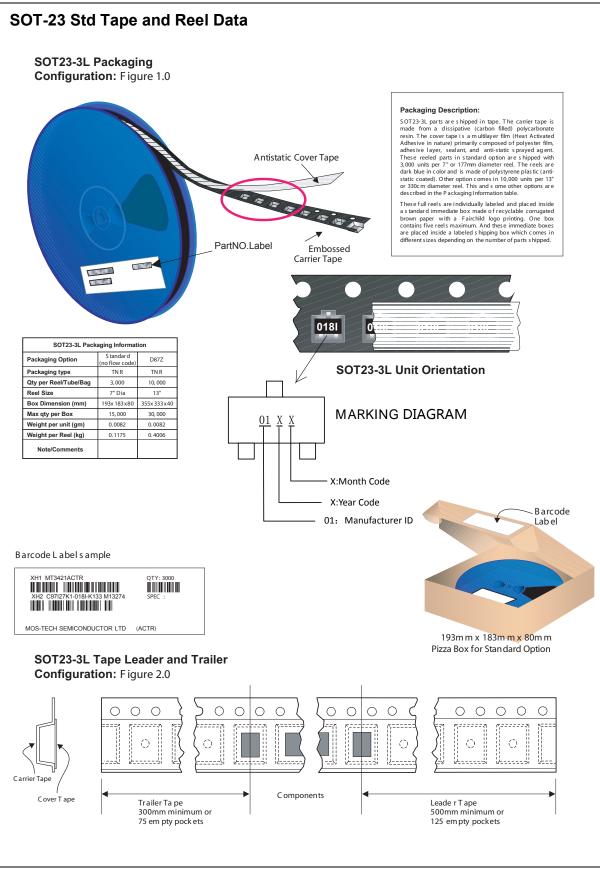
b. 270°C/W when mounted on a 0.001 in² pad of 2oz Cu.

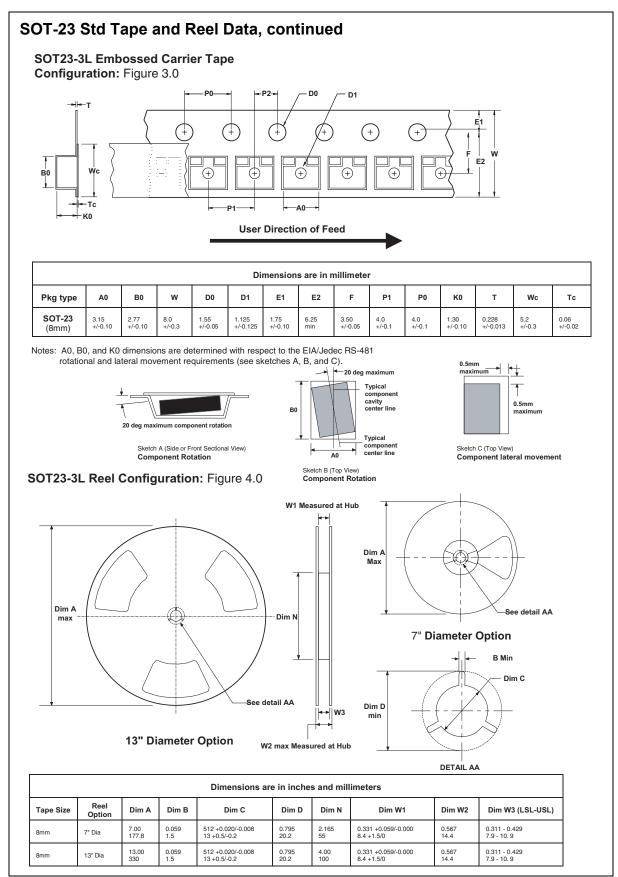
Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2.0%.









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