# MT3( &1 Single P-Channel Power MOSFET

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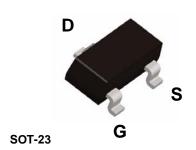
### **General Description**

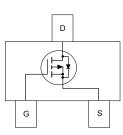
This P-Channel Power MOSFET is produced using MOS-TECH Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state r esistance and yet maintain low gate charge for superior switching performance.

These devices are well suit ed for portable electronics applications: load s witching and power management, battery charging circuits and DC/DC conversion.

#### Features

- -4.3 A, -25 V.  $R_{DS(ON)}$  = 0.08  $\Omega$  @ V<sub>GS</sub> = -4.5 V  $R_{DS(ON)}$  = 0.11  $\Omega$  @ V<sub>GS</sub> = -2.5 V
- Low gate charge (3.6 nC typical)
- High performance trench technology for extremely
  low R<sub>DS(ON)</sub>
- SuperSOT<sup>TM</sup> -23 provides low R<sub>DS(ON)</sub> and 30% higher power handling capability than SOT23 in the same footprint





### Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		-25	V
V <sub>GSS</sub>	Gate-Source Voltage		±12	V
I <sub>D</sub>	Drain Current – Continuous	(Note 1a)	-4.3	A
	– Pulsed		-10	
PD	Maximum Power Dissipation	(Note 1a)	0.5	W
		(Note 1b)	0.46	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Tem	perature Range	-55 to +150	°C

## **Thermal Characteristics**

R <sub>0JA</sub>	Thermal Resistance, Junction-to-Ambient	(Note 1a)	250	°C/W
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	75	°C/W

# Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity	
010X	MT3421	7"	8mm	3000 units	

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Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHAR	ACTERISTICS		·			
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA	-25			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient	$I_{\rm D}$ = -250 µA, Referenced to 25 °C		-16		mV /°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{\rm DS} = -16 \text{ V}, \ V_{\rm GS} = 0 \text{ V}$			-1	μA
		T <sub>J</sub> = 55°C			-10	μA
I <sub>GSSF</sub>	Gate - Body Leakage, Forward	V <sub>GS</sub> = 8 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate - Body Leakage, Reverse	$V_{GS} = -8 V, V_{DS} = 0 V$			-100	nA
ON CHARA	CTERISTICS (Note 2)		•			<u>.</u>
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{\rm DS} = V_{\rm GS}, \ I_{\rm D} = -250 \ \mu {\rm A}$	-0.6	-0.8	-1.2	V
$\Delta V_{GS(th)} / \Delta T_J$	Gate Threshold Voltage Temp. Coefficient	$I_{\rm D}$ = -250 µA, Referenced to 25 °C		3		mV /°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -1.3 \text{ A}$		0.8	0.9	Ω
		T <sub>J</sub> =125°C		0.12	0.15	-
		V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -1.1 A		0.11	0.15	
I <sub>D(ON)</sub>	On-State Drain Current	$V_{GS} = -4.5 V, V_{DS} = -5 V$	-4.3			А
<b>g</b> <sub>FS</sub>	Forward Transconductance	$V_{DS} = -4.5 \text{ V}, \ \text{I}_{D} = -2 \text{ A}$		4		S
DYNAMIC C	HARACTERISTICS					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = -10 V, V_{GS} = 0 V,$ f = 1.0 MHz		330		pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		80		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			35		pF
SWITCHING	CHARACTERISTICS (Note 2)					
t <sub>D(on)</sub>	Turn - On Delay Time	$V_{DD} = -5 V, I_{D} = -0.5 A,$		7	15	ns
t,	Turn - On Rise Time	$V_{GS}$ = -4.5 V, $R_{GEN}$ = 6 $\Omega$		12	22	ns
t <sub>D(off)</sub>	Turn - Off Delay Time			16	26	ns
t <sub>r</sub>	Turn - Off Fall Time			5	12	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -2 \text{ A},$ $V_{GS} = -4.5 \text{ V}$		3.6	5	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = -4.5 V$		0.8		nC
$Q_{gd}$	Gate-Drain Charge			0.7		nC
DRAIN-SOU	IRCE DIODE CHARACTERISTICS AND MAX					
I <sub>s</sub>	Maximum Continuous Drain-Source Diode Fo	rward Current			-0.42	А
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_{S} = -0.42 A$ (Note)		-0.7	-1.2	V

Note:

1. R<sub>8,4</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the s older mounting surface of the drain pins. R<sub>8,c</sub> is guaranteed by design while  $\mathsf{R}_{_{\theta CA}}$  is determined by the user's board design.



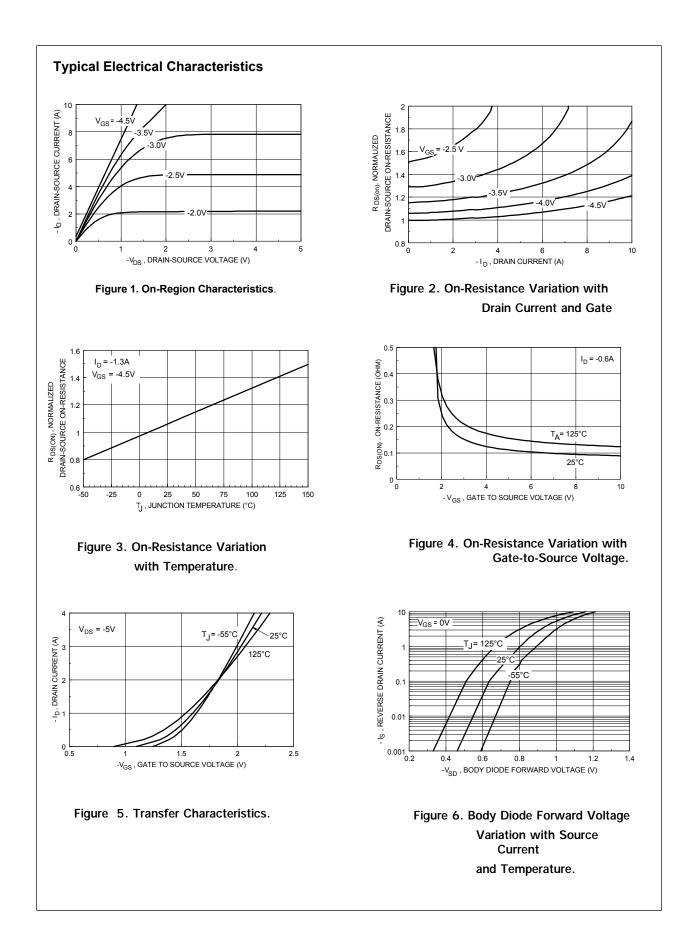
a. 250°C/W when mounted on a 0.02 in² pad of 2oz Cu.

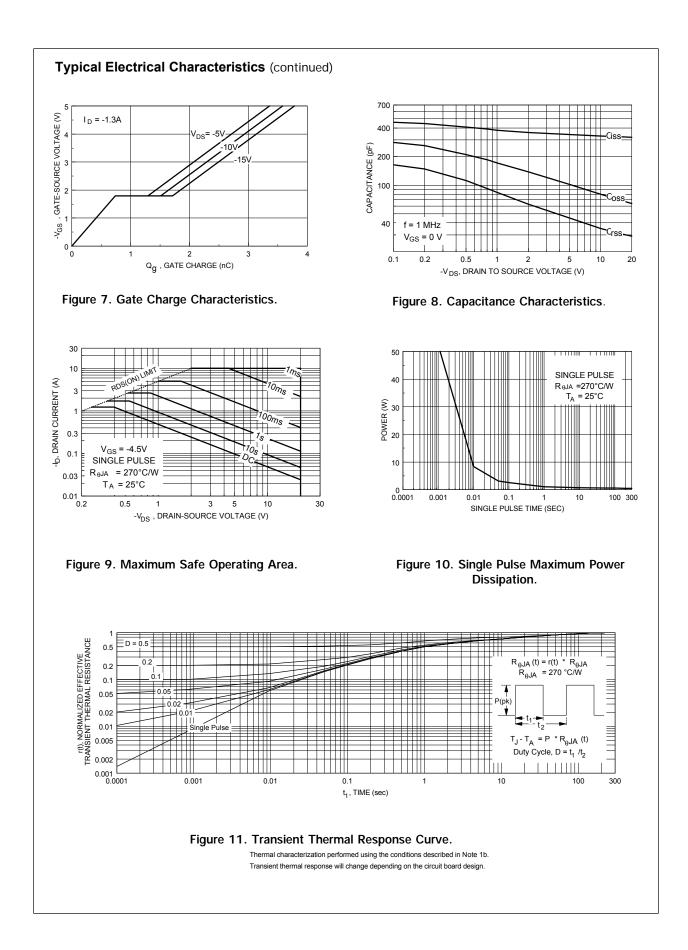


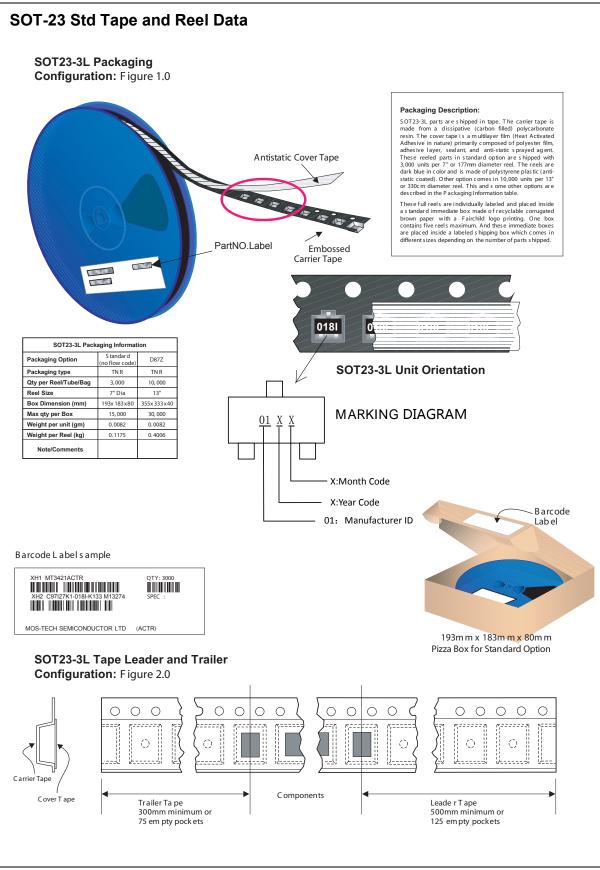
b. 270°C/W when mounted on a 0.001 in<sup>2</sup> pad of 2oz Cu.

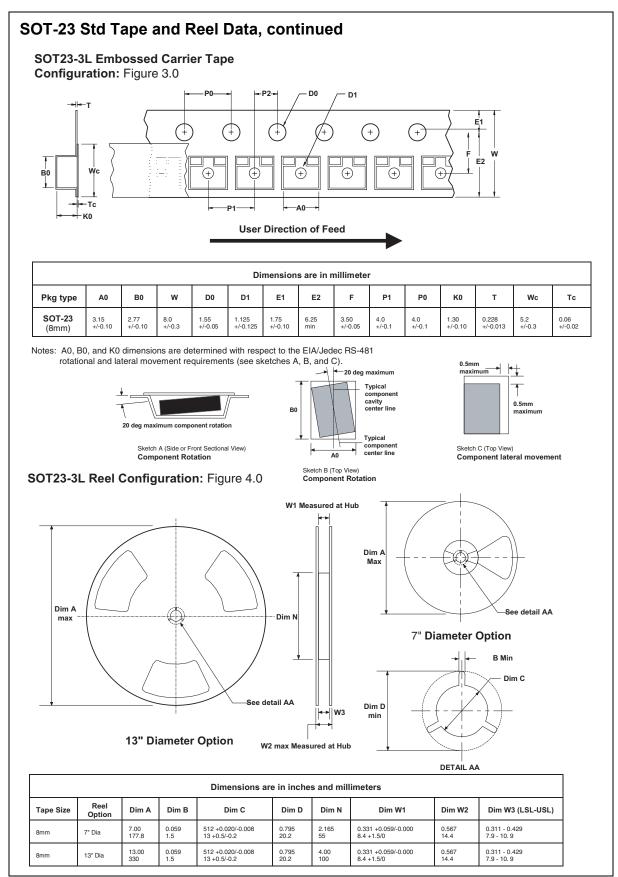
Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2.0%.









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