

ABSOLUTE MAXIMUM RATINGS

VDD	-0.3V to 20V
AUX	-150V to 60V
DRAIN	-0.3V to 600V
CS	-0.3V to 6V
P _{DMAX} (Maximum Power)	0.45W
Storage Temperature	-55°C to 150°C
Junction Temperature (T _j)	150°C

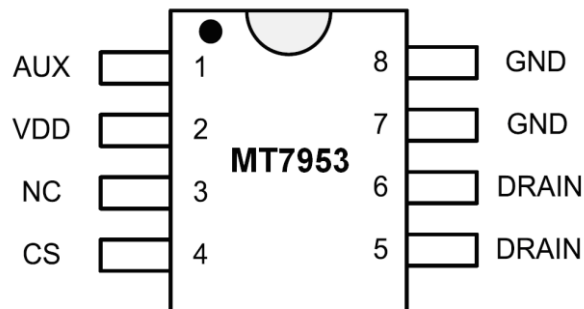
Recommended operating conditions

Supply voltage	7.5V to 16V
Operating Temperature	-40°C to 105°C

Thermal resistance

Case to ambient (R _{θCA})	128°C/W
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PIN CONFIGURATIONS



PIN DESCRIPTION

Name	Pin No.	Description
AUX	1	Sensing Auxiliary winding's voltage waveform. Internal with a 90Kohm:15Kohm resistor string to ground.
VDD	2	Power Supply.
NC	3	No Connection
CS	4	Current sense pin. A sense resistor connected between CS and GND pin.
DRAIN	5, 6	Drain of internal 600V NMOSFET
GND	7, 8	Ground

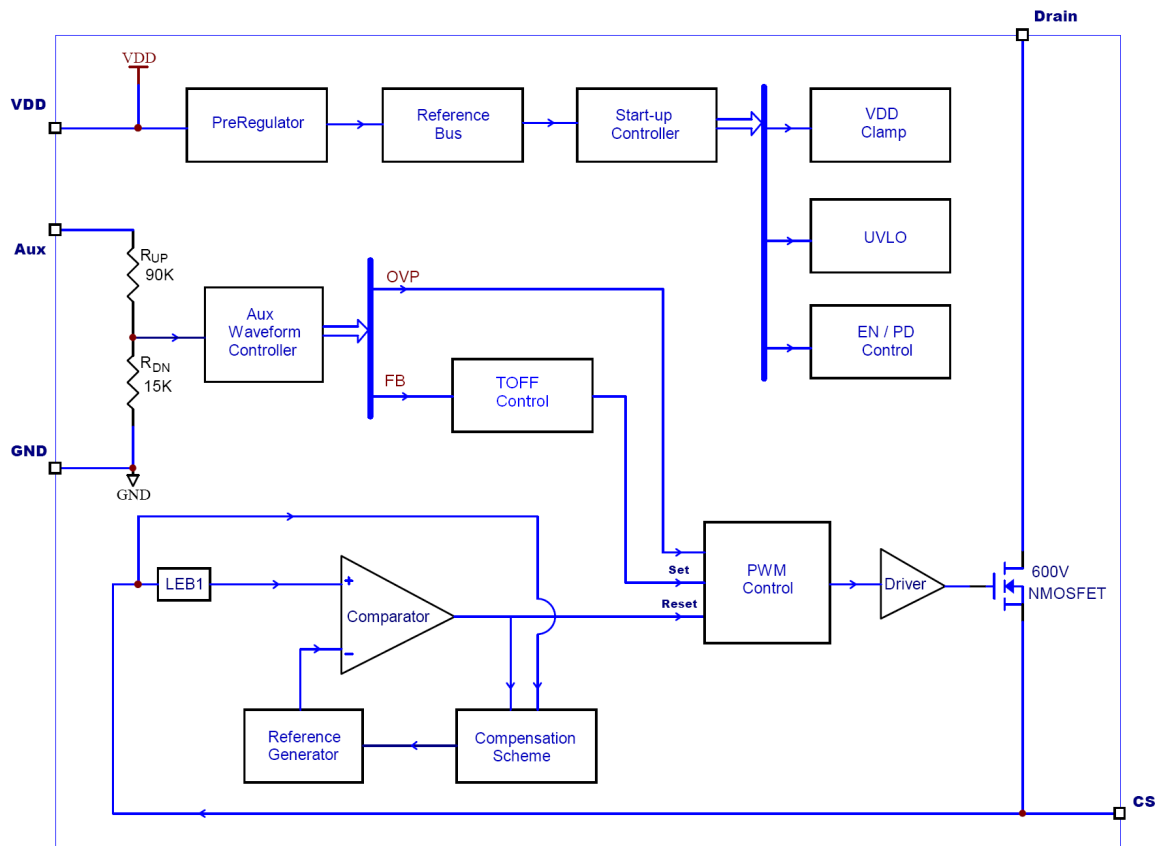
ELECTRICAL CHARACTERISTICS

(Test conditions: $V_{DD}=12V$, $T_A=25^{\circ}C$ unless otherwise stated.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Start-up & Power supply						
I_{START}	Start-up Current			25	50	μA
UVLO	Lower Threshold Voltage of V_{DD}	V_{DD} Pin ramp down from 18V	6.6	7.2	7.5	V
V_{START}	Start-up Voltage	V_{DD} Pin ramp up from 0V	15	16	17	V
$V_{DD-CLAMP}$	VDD clamp voltage	$I_{DD}=10mA$	18.6	19.5	20.4	V
Operation Current						
I_Q	Operation current	$F_s=40KHz$		1.5		mA
Current Sense						
V_{CS-TH}	Threshold Voltage of Peak Current Protection		490	500	510	mV
LEB1	Leading Edge Blanking at CS Pin			500		nS
Auxiliary Winding Detection						
R_{UP}	The upper resistor at Auxiliary Pin	Note (1)		90K		Ohm
R_{DN}	The bottom resistor at Auxiliary Pin	Note (1)		15K		Ohm
V_{OV-TH}	The over voltage threshold for Auxiliary Winding		2.15	2.3	2.5	V
LEB2	The Leading Edge Blanking at AUX Pin			2.0		μS
Over Temperature Protection						
OTP	Over temperature protection threshold			155		$^{\circ}C$
	Over temperature protection release thysteresis			20		$^{\circ}C$
Power MOSFET						
R_{DSON}	Static drain-source on-resistance	$V_{GS}=10V/I_{DS}=0.5A$		10	12	Ω
BV_{DSS}	Drain-source breakdown voltage	$V_{GS}=0V/I_{DS}=250\mu A$	600			V
I_{DSS}	Drain-source leakage current	$V_{GS}=0V/V_{DS}=600V$			1	μA

NOTE:

(1) Refer to Block Diagram in Page 4

BLOCK DIAGRAM

APPLICATION INFORMATION

MT7953 is a high performance power switch specially designed for LED lighting. It uses Maxic proprietary constant current regulation and compensation technology to achieve accurate LED current without opto-coupler and secondary side feedback circuit. MT7953 works in Discontinuous Conduction Mode (DCM). It integrates a 600V power MOSFET, minimizes the external component count, lower the total BOM cost.

Start Up

During start-up process, VDD is charged through a start-up resistor. As VDD reaches 16V, the control logic starts to work, and the power MOSFET begins to switch, as show in Fig.1. The power supply is taken over by the auxiliary

winding once the voltage of this winding is high enough.

MT7953 will shut down if VDD goes below 7.2V (UVLO threshold voltage).

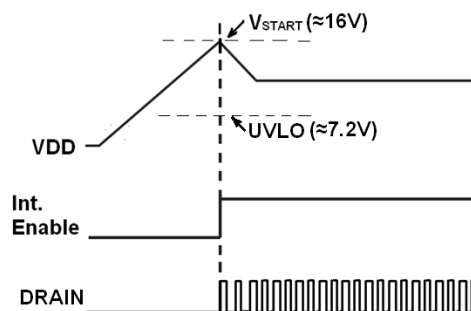


Fig.1 Start up sequence

Constant Current Control and Output Current Setup

Cycle-by-cycle current sense is offered in MT7953, the CS is connected to the current sense comparator, and the voltage on CS is

compared with the internal 500mV reference voltage, the MOSFET is turned off when the voltage on the CS reaches the threshold. The comparator also includes a 500nS leading edge blanking time to block the transient noise as the power switch just turned on.

The primary side peak current is given by:

$$I_{P_PK} = \frac{500}{R_{CS}} (mA)$$

where R_{CS} is the peak current sensing resistor, i.e. the resistor R1 in the application circuit in page 1.

The current in LED can be calculated by the following equation:

$$I_{LED} = \frac{I_{P_PK}}{4} \times \frac{N_P}{N_S} = \frac{500}{4 \times R_{CS}} \times \frac{N_P}{N_S} (mA)$$

where N_P is the turns of the primary winding, N_S is the turns of the secondary winding, I_{P_PK} is the primary side peak current. Shown in the above equation, the output current is determined by the turns ratio of the transformer and the current sense resistor value, insensitive to the inductance of the transformer.

Switching Frequency

MT7953 is designed to operating in discontinuous conduction mode and no external loop compensation is needed to maintain system stability. The maximum duty cycle is limited to 42%. It's highly recommended to limit the maximum switching frequency less than 100KHz and the minimum switching frequency more than 20KHz.

The switching frequency can be set by formula:

$$f_{SW} = \frac{N_P^2 \times V_{LED}}{8 \times N_S^2 \times L_p \times I_{LED}}$$

where, N_P is the turns of the primary winding, N_S is the turns of the secondary winding, L_p is the transformer primary winding inductance. Customer should set the switching frequency

between 40KHz to 80KHz through properly design transformer parameters.

Auxiliary Winding Feedback and Sensing

MT7953 detects the secondary side output current through the feedback of the auxiliary winding. The auxiliary winding directly ties to the AUX pin, then divided by the internal resistor string. The upper resistor is 90Kohm (R_{UP} in the block diagram in page 4), the bottom resistor is 15Kohm (R_{DN} in the block diagram in page 4), results in a 6:1 ratio. To block the switching noise, a 2uS leading edge blanking time is embedded inside the chip. Refer to Fig.2.

MT7953 features over-voltage protection (OVP), LED open circuit protection, turn-off time control functions. Those functions are triggered by sensing the auxiliary winding waveform information through AUX pin.

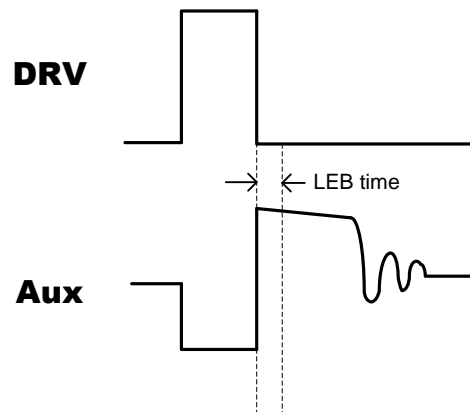


Fig.2 Auxiliary Signal Sensing

Over-voltage (LED open circuit) Protection

MT7953 is implemented with two over-voltage protection schemes: (1) If AUX pin's voltage is detected above pre-determined threshold for four times, MT7953 turns off the PWM switching signal, and VDD voltage gradually drops to UVLO threshold, and the system will be re-started. The threshold voltage of over-voltage protection V_{OUT_OV} , can be easily defined as (refer to the application circuit in page 1 and block diagram in page 4):

$$\begin{aligned}
 V_{\text{OUT_OV}} &= 2.3 \times \left(1 + \frac{R_{\text{UP}}}{R_{\text{DN}}}\right) \times \frac{N_s}{N_a} - V_{D2} \\
 &= 2.3 \times \left(1 + \frac{90K}{15K}\right) \times \frac{N_s}{N_a} - V_{D2} \\
 &= 16.1 \times \frac{N_s}{N_a} - V_{D2}
 \end{aligned}$$

where N_s is the secondary winding, N_a is auxiliary winding, V_{D2} is the forward bias of the secondary side rectifier diode.

(2) If VDD pin's voltage exceeds 19.5V, the clamp circuit in MT7953 wakes up, clamps VDD voltage at 19.5V. It is highly recommended to set up the VDD voltage between 7.5V and 16V by designed a proper N_a to N_s ratio of the transformer.

Over-current Protection

MT7953 immediately turns off the power MOSFET once the voltage at CS pin exceeds 500mV. This cycle by cycle current limitation scheme prevents the relevant components, such as power MOSFET, transformer, etc. from damage.

PCB Layout

The following rules should be followed in MT7953 PCB layout:

Bypass Capacitor

The bypass capacitor on VDD should be as close as possible to the VDD pin.

Ground Path

The power ground path for current sense should be short, and the power ground path should be separated from small signal ground path before the negative of the bulk capacitor.

The Area of Power Loop

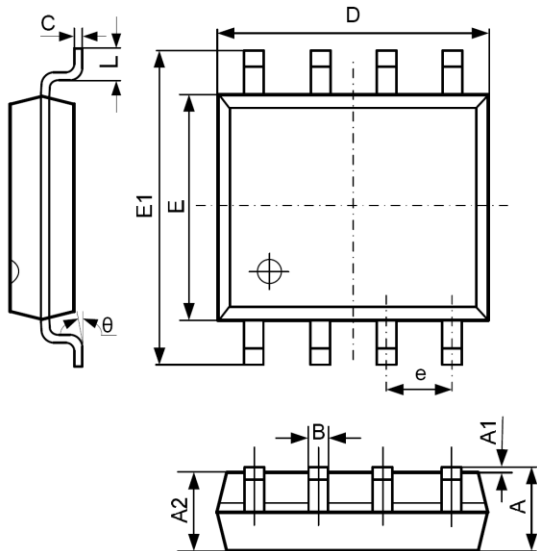
The area of main current loop should be as small as possible to reduce EMI radiation, such as the primary current loop, the snubber circuit and the secondary rectifying loop.

DRAIN pin

Increase the copper area of the drain terminal for thermal consideration.

PACKAGE INFORMATION

SOP-8 PACKAGE OUTLINE AND DIMENSIONS



SYMBOL	DIMENSION IN MILLIMETERS		DIMENSION IN INCHES	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
B	0.330	0.510	0.013	0.020
C	0.190	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
e	1.270 TYP		0.050 TYP	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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