14V N-Channel Enhancement Mode MOSFET

MTA020N01S3

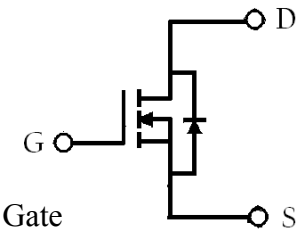
BV_{DSS}	14V
I_D@V_{GS}=4.5V, T_A=25°C	2.3A
R_{DSON}@V_{GS}=4.5V, I_D=2.3A	37mΩ(typ)
R_{DSON}@V_{GS}=2.5V, I_D=2.3A	46mΩ(typ)

Features

- Simple drive requirement
- Small package outline
- Pb-free lead plating and halogen-free package

Symbol

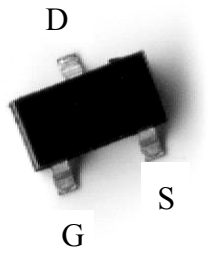
MTA020N01S3



G：Gate
S：Source
D：Drain

Outline

SOT-323



Ordering Information

Device	Package	Shipping
MTA020N01S3-0-T1-G	SOT-323 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

Environment friendly grade : S for RoHS compliant products, G for RoHS compliant and green compound products

Packing spec, T1 : 3000 pcs / tape & reel,7” reel

Product rank, zero for no rank products

Product name

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	14	V
Gate-Source Voltage	V_{GS}	±8	
Continuous Drain Current @ T_A=25°C, V_{GS}=4.5V (Note 3)	I_D	2.3	A
Continuous Drain Current @ T_A=70°C, V_{GS}=4.5V (Note 3)		1.8	
Pulsed Drain Current (Notes 1, 2)	I_{DM}	10	
Maximum Power Dissipation@ T_A=25℃ (Note 3)	P_D	0.34	W
Operating Junction and Storage Temperature Range	Tj ; Tstg	-55~+150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max (Note3)	$R_{\theta JA}$	367	°C/W
Thermal Resistance, Junction-to-Case, max	$R_{\theta JC}$	250	

Note : 1. Pulse width limited by maximum junction temperature.
2. Pulse width≤ 300μs, duty cycle≤2%.
3. Surface mounted on 1 in² copper pad of FR-4 board; 270°C/W when mounted on minimum copper pad

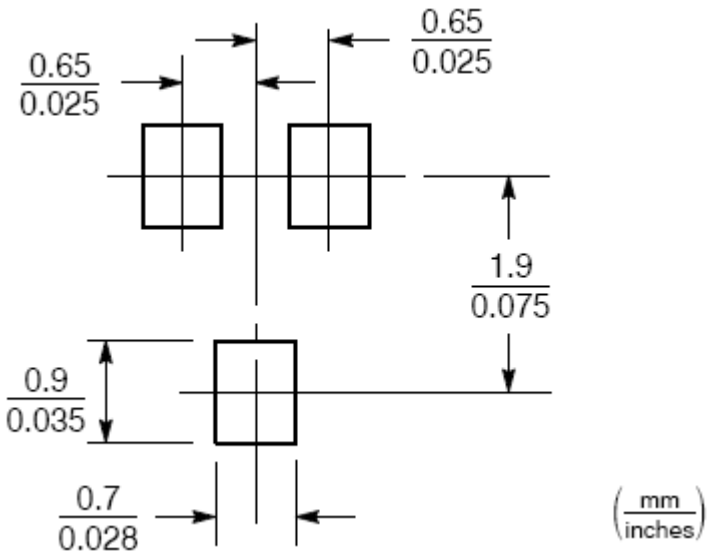
Electrical Characteristics (Tj=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	14	-	-	V	V_{GS}=0V, I_D=250μA
$V_{GS(th)}$	0.5	-	1.0		V_{DS}=V_{GS}, I_D=250μA
I_{GSS}	-	-	±100	nA	V_{GS}=±8V, V_{DS}=0V
I_{DSS}	-	-	1	μA	V_{DS}=14V, V_{GS}=0V
	-	-	25		V_{DS}=10V, V_{GS}=0V(Tj=70°C)
*$R_{DS(ON)}$	-	37	52	mΩ	V_{GS}=4.5V, I_D=2.3A
	-	46	65		V_{GS}=2.5V, I_D=2.3A
*G_{FS}	-	6	-	S	V_{DS}=5V, I_D=2.3A
Dynamic					
Ciss	-	381	-	pF	V_{DS}=10V, V_{GS}=0V, f=1MHz
Coss	-	113	-		
Crss	-	98	-		
$t_{d(ON)}$	-	6	-	ns	V_{DS}=10V, I_D=2.3A, V_{GS}=5V, R_G=3.3Ω
t_r	-	19	-		
$t_{d(OFF)}$	-	24.2	-		
t_f	-	9.2	-		
Qg	-	6.4	-	nC	V_{DS}=10V, I_D=2.3A, V_{GS}=4.5V
Qgs	-	0.7	-		
Qgd	-	2.4	-		

Rg	-	2.3	-	Ω	f=1MHz
Source-Drain Diode					
*I_S	-	-	1.6	A	
*I_{SM}	-	-	6.4		
*V_{SD}	-	0.8	1.2	V	V_{GS}=0V, I_S=2.3A
Trr	-	11	-	ns	V_{GS}=0V, I_F=2.3A, dI_F/dt=100A/μs
Qrr	-	1.9	-	nC	

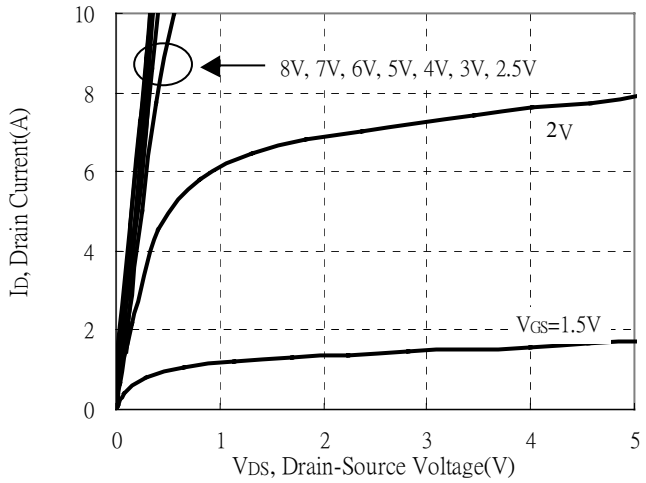
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended Soldering Footprint

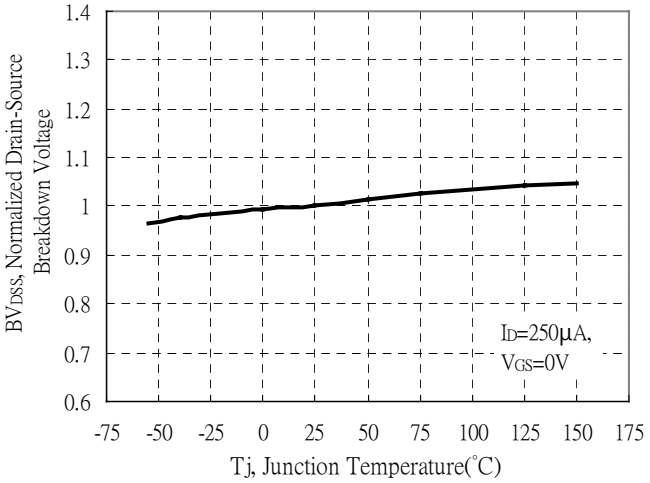


Typical Characteristics

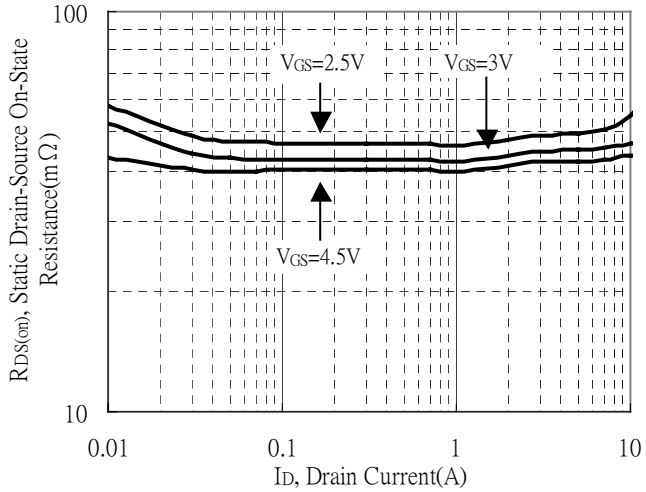
Typical Output Characteristics



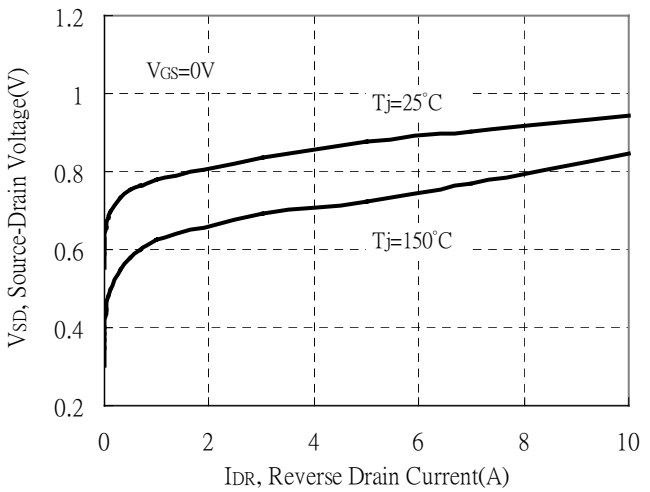
Brekdown Voltage vs Ambient Temperature



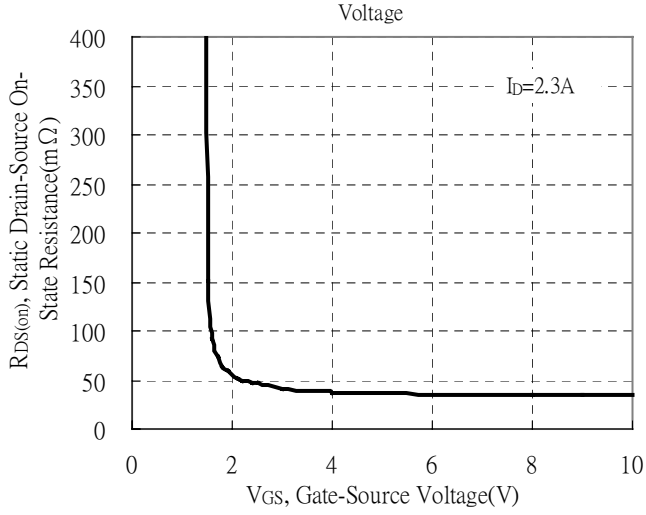
Static Drain-Source On-State resistance vs Drain Current



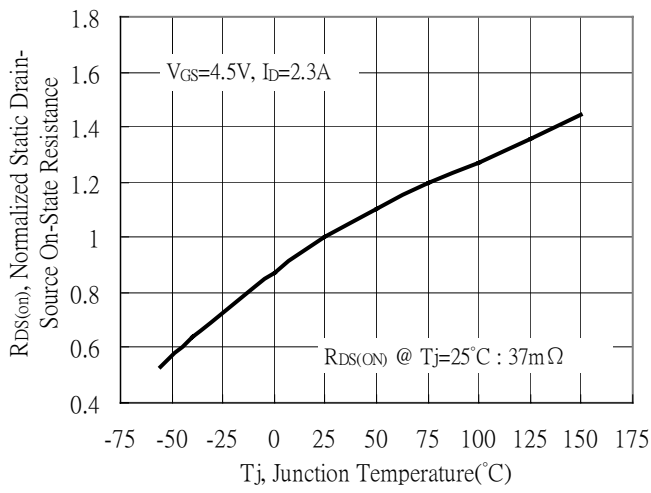
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

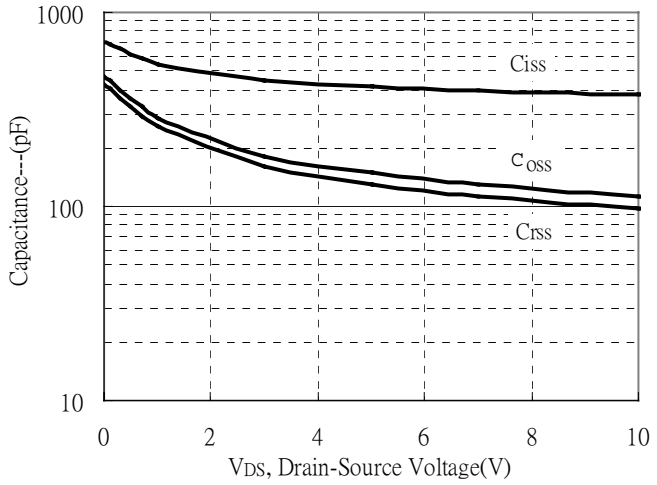


Drain-Source On-State Resistance vs Junction Tempearture

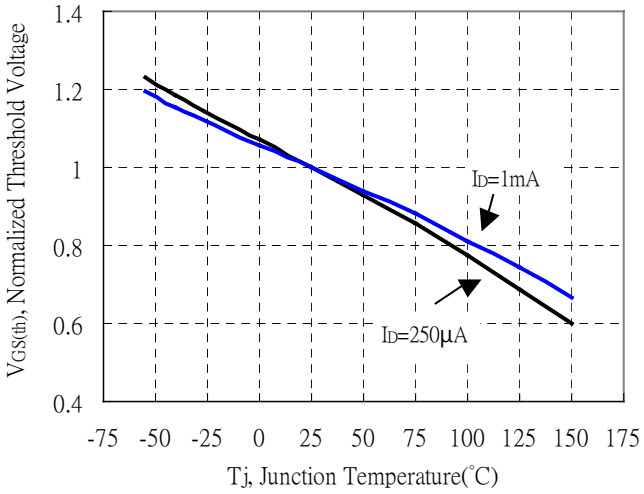


Typical Characteristics(Cont.)

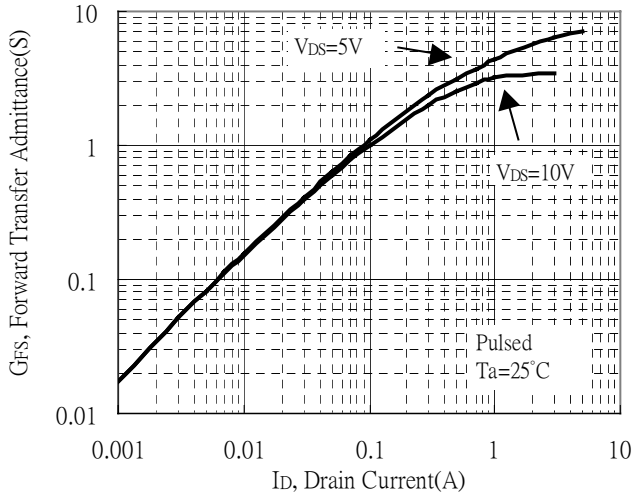
Capacitance vs Drain-to-Source Voltage



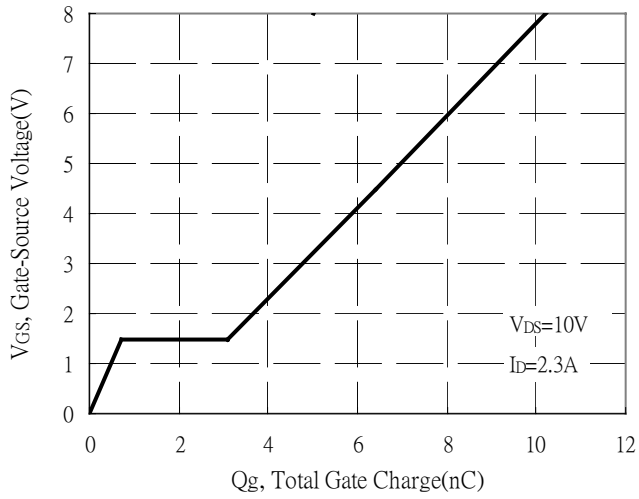
Threshold Voltage vs Junction Tempearture



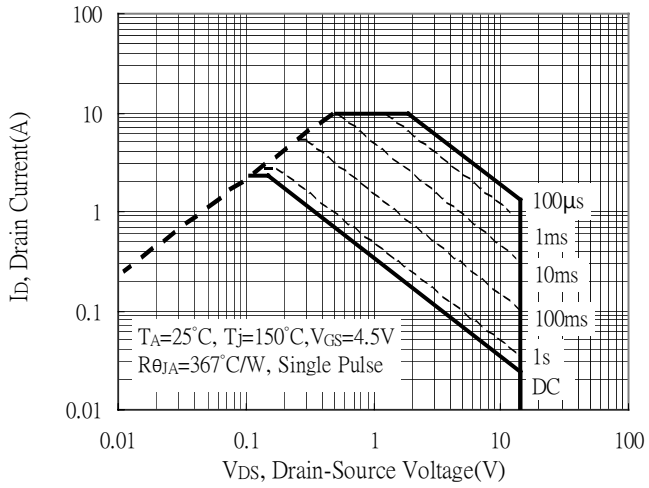
Forward Transfer Admittance vs Drain Current



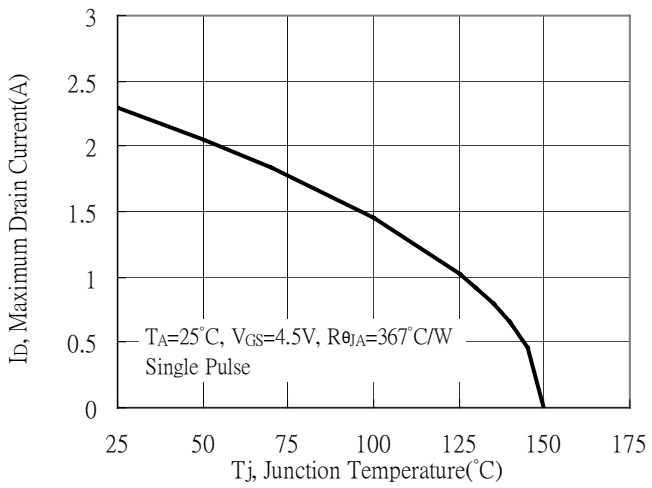
Gate Charge Characteristics



Maximum Safe Operating Area

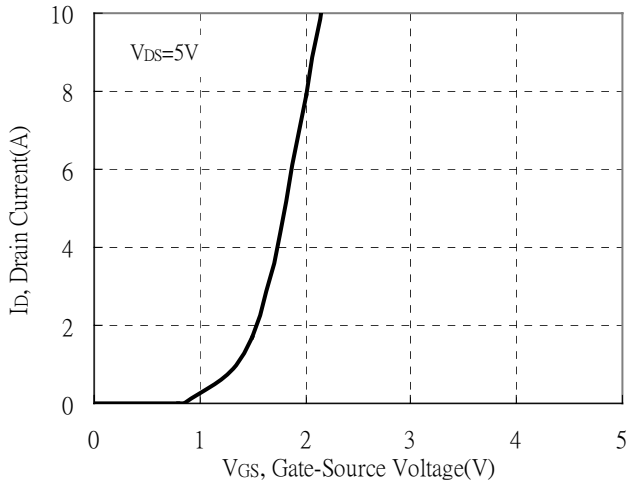


Maximum Drain Current vs JunctionTemperature

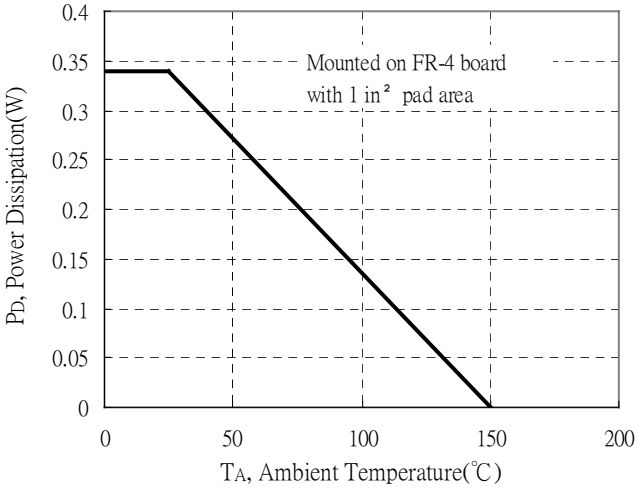


Typical Characteristics(Cont.)

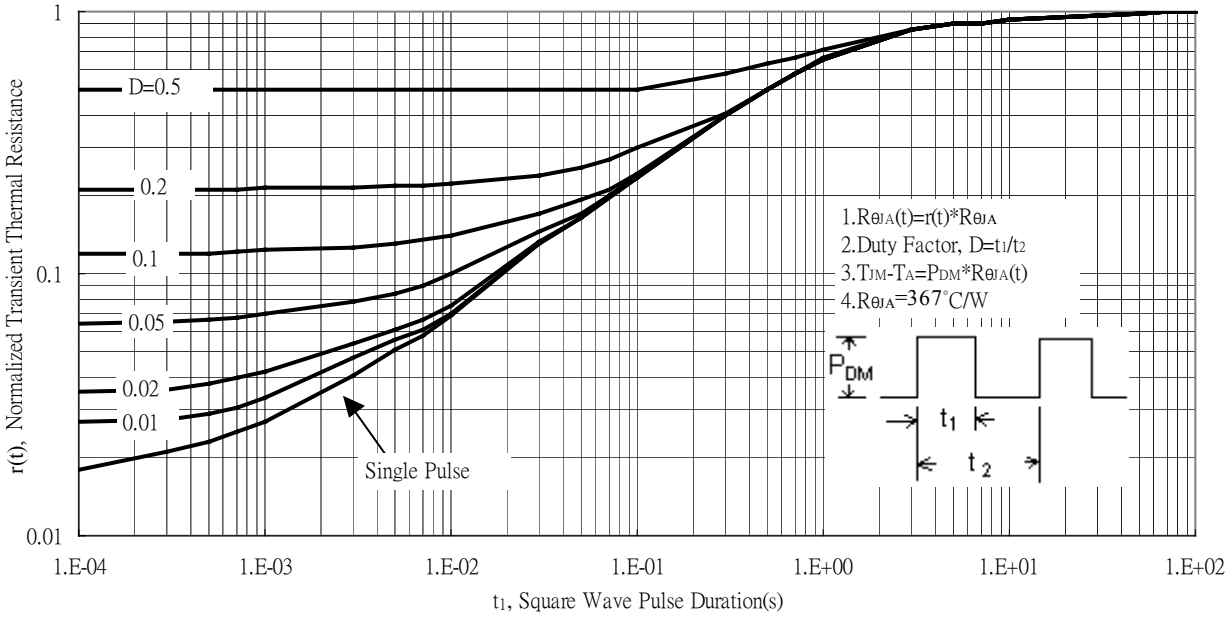
Typical Transfer Characteristics



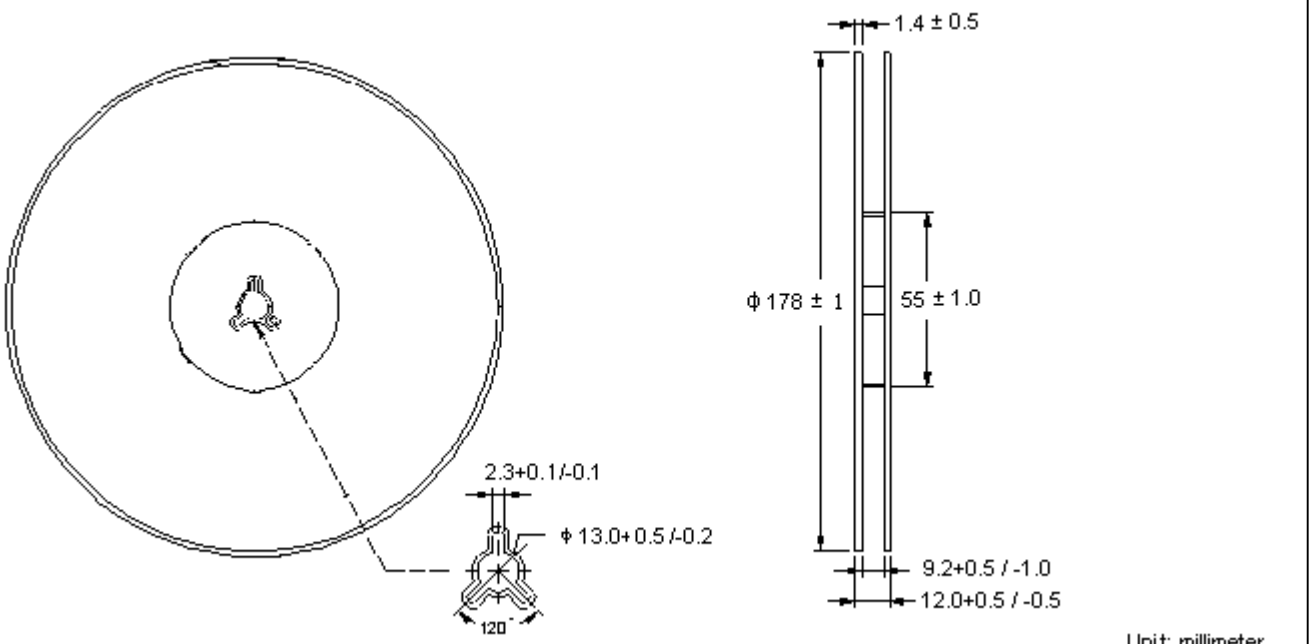
Power Derating Curve



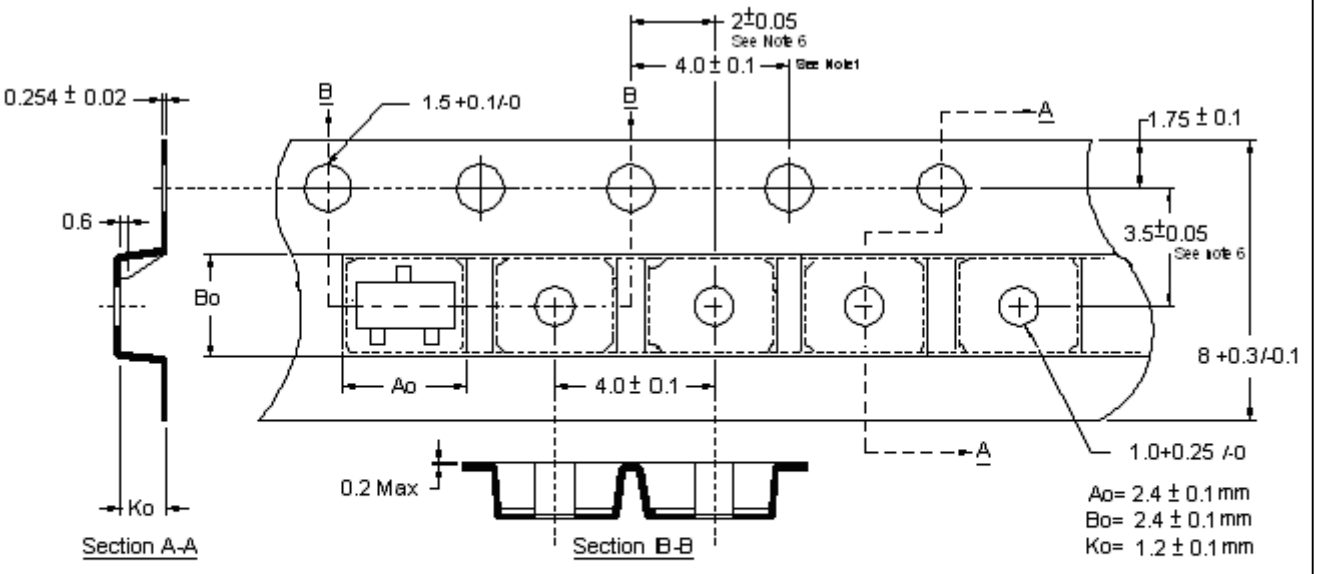
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



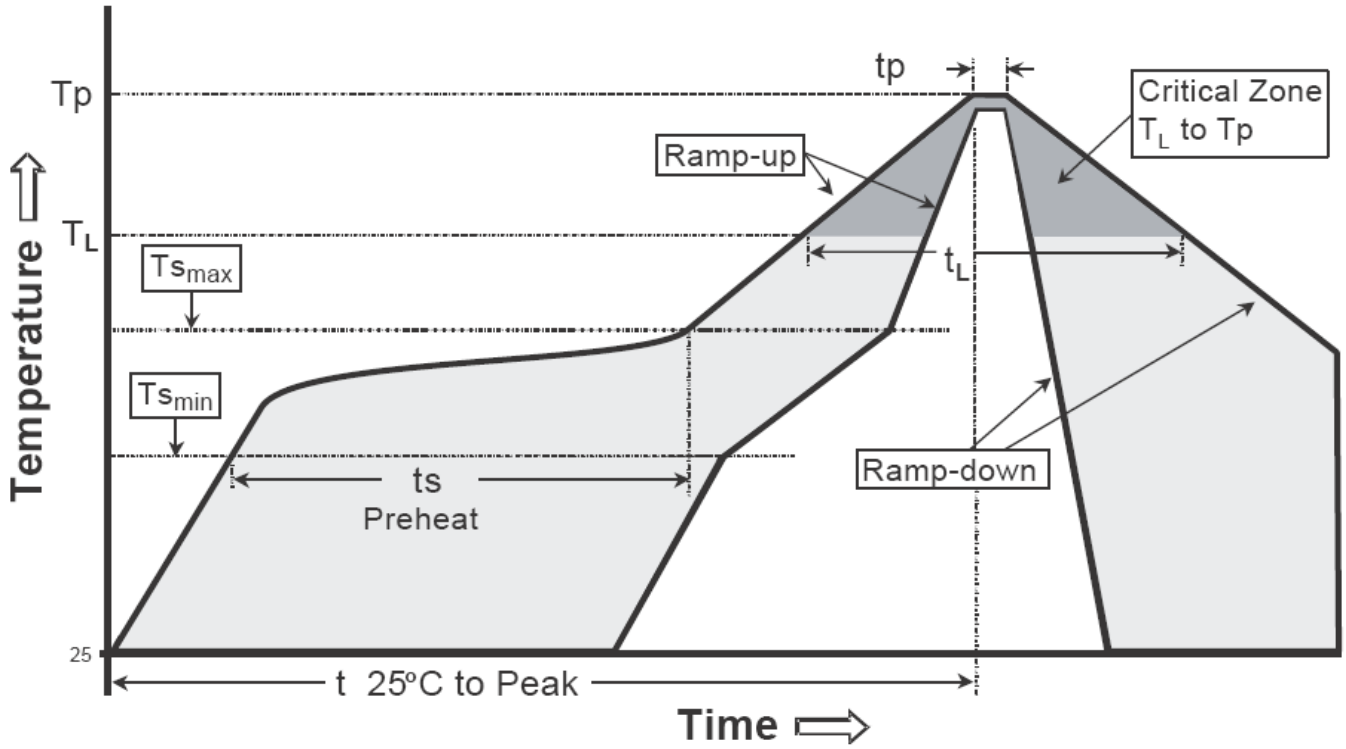
Notes:
1. 10 sprocket hole pitch cumulative tolerace ±0.2.
2. Camber not to exceed 1mm in 100mm.
3. Material: Conductive Black Advantek Polystyrene.
4. Ao & Bo measured on a plane 0.3mm above the bottom of the pocket.
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Unit : millimeter

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

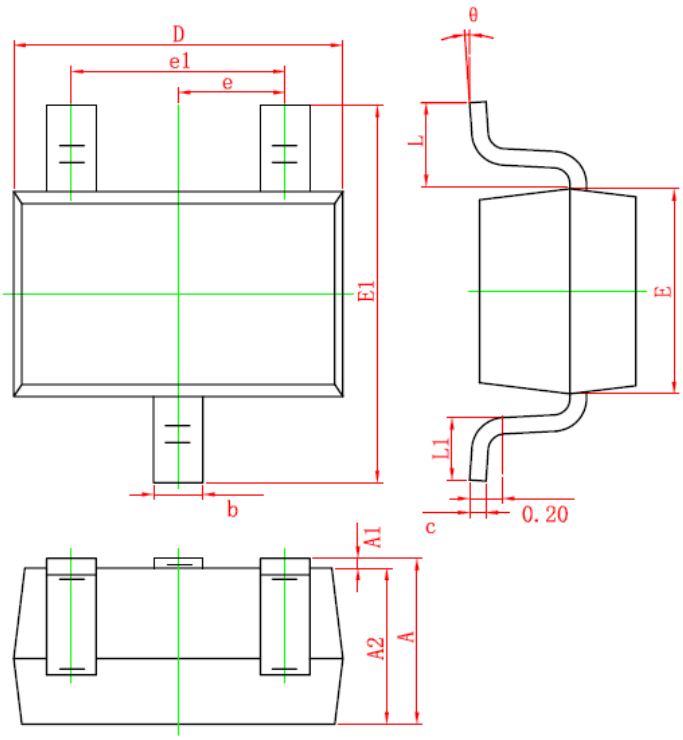
Recommended temperature profile for IR reflow

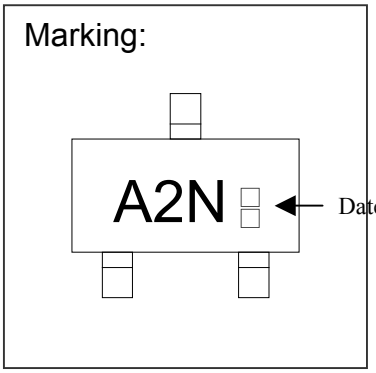


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(T_S min) −Temperature Max(T_S max) −Time($t_{s\ min}$ to $t_{s\ max}$)	 100°C 150°C 60-120 seconds	 150°C 200°C 60-180 seconds
Time maintained above: −Temperature (T_L) − Time (t_L)	 183°C 60-150 seconds	 217°C 60-150 seconds
Peak Temperature(T_P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-323 Dimension





3-Lead SOT-323 Plastic
Surface Mounted Package
CYStek Package Code: S3

Style: Pin 1.Gate 2.Source 3.Drain

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043	E1	2.150	2.450	0.085	0.096
A1	0.000	0.100	0.000	0.004	e	0.650 TYP		0.026 TYP	
A2	0.900	1.000	0.035	0.039	e1	1.200	1.400	0.047	0.055
b	0.200	0.400	0.008	0.016	L	0.525 REF		0.021 REF	
c	0.080	0.150	0.003	0.006	L1	0.260	0.460	0.010	0.018
D	2.000	2.200	0.079	0.087	θ	0°	8°	0°	8°
E	1.150	1.350	0.045	0.053					

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.